



Product Specification

SPECIFICATION FOR APPROVAL

(•)	Preliminary	Specification
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() Final Specification

Title 55.0" WUXGA TFT LCD

BUYER	LGE
MODEL	

SUPPLIER	LG Display Co., Ltd.	
*MODEL	LC550EUH	
SUFFIX	KEF1 (RoHS Verified)	

^{*}When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE DATE
Please return 1 copy for your	confirmation with
your signature and co	mments.

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Ver. 0.1 0 /43



Product Specification

CONTENTS

Number	ITEM	Page
	COVER	1
	CONTENTS	2
	RECORD OF REVISIONS	3
1	GENERAL DESCRIPTION	4
2	ABSOLUTE MAXIMUM RATINGS	5
3	ELECTRICAL SPECIFICATIONS	6
3-1	ELECTRICAL CHARACTERISTICS	6
3-2	INTERFACE CONNECTIONS	8
3-3	SIGNAL TIMING SPECIFICATIONS	11
3-4	SIGNAL TIMING WAVEFORMS	12
3-5	COLOR DATA REFERENCE	15
3-6	POWER SEQUENCE	16
4	OPTICAL SPECIFICATIONS	18
5	MECHANICAL CHARACTERISTICS	24
6	RELIABILITY	27
7	INTERNATIONAL STANDARDS	28
7-1	SAFETY	28
7-2	EMC	28
7-3	Environment	28
8	PACKING	29
8-1	DESIGNATION OF LOT MARK	29
8-2	PACKING FORM	29
9	PRECAUTIONS	30
9-1	MOUNTING PRECAUTIONS	30
9-2	OPERATING PRECAUTIONS	30
9-3	ELECTROSTATIC DISCHARGE CONTROL	31
9-4	PRECAUTIONS FOR STRONG LIGHT EXPOSURE	31
9-5	STORAGE	31
9-6	HANDLING PRECAUTIONS FOR PROTECTION FILM	31
9-7	OPERAGING CONDITION GUIDE	31

Ver. 0.1 1/43

Product Specification

RECORD OF REVISIONS

Revision No.	Revision Date	Page	Description
0.1	Sep, 26, 2011	-	Preliminary Specification (First Draft)
0.2	Nov,30,2011	-	Revise spec.
0.3	Dec.08.2011	7	Revise spec.
	-		

Ver. 0.1 2 /43

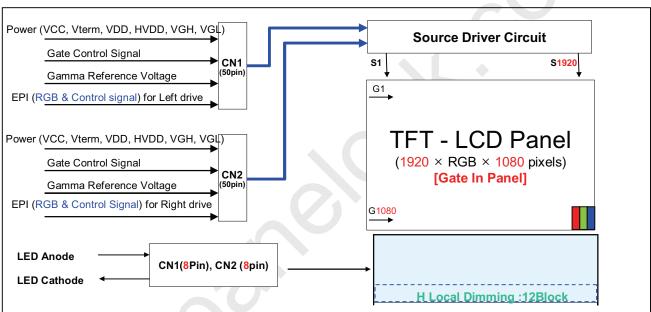


Product Specification

1. General Description

The LC550EUH is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 54.64 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot. Therefore, it can present a palette of more than 16.7M(true) colors.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

General i eatures		
Active Screen Size	54.64 inches(1387.80mm) diagonal	
Outline Dimension	1228.6(H) X 711.4(V) X 9.3(B) X 20.7(D)	
Pixel Pitch	0.630 mm x 0.630 mm	
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement	
Color Depth	8-bit, 16.7 M colors (* 1.06B colors @ 10 bit (D) System Output)	
Drive IC Data Interface	Source D-IC : 8-bit EPI, gamma reference voltage, and control signals Gate D-IC : Gate In Panel	
Luminance, White	400 cd/m² (Center 1point ,Typ.)	
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Min.), U/D 178 (Min.))	
Power Consumption	Total TBD W (Typ.) (Logic=TBD W with T-CON, LED Backlight =98.3W)	
Weight	15.4Kg (Typ.)	
Display Mode	Transmissive mode, Normally black	
Surface Treatment	Hard coating(2H), Anti-glare treatment (Haze < 1%),	
Ver. 0.1		3 /43



Product Specification

2. Absolute Maximum Ratings

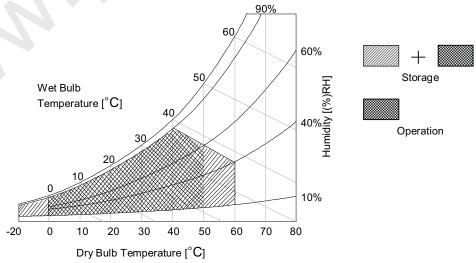
The following items are maximum values which, if exceeded, may cause faulty operation or permanent damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

D	0h.al	Va	lue	1114	N. 4
Parameter	Symbol	Min	Max	Unit	Note
Logic & EPI Power Voltage	VCC	-0.5	+2.2	VDC	
Gate High Voltage	VGH	+18.0	+30.0	VDC	
Gate Low Voltage	VGL	-8.0	-4.0	VDC	
Source D-IC Analog Voltage	VDD	-0.3	+18.0	VDC	1
Gamma Ref. Voltage (Upper)	VGMH	½VDD-0.5	VDD+0.5	VDC	
Gamma Ref. Voltage (Low)	VGML	-0.3	½ VDD+0.5	VDC	
LED Input Voltage	VF	-	+95.0	VDC	
Panel Front Temperature	Tsur	-	+68	°C	4
Operating Temperature	Тор	0	+50	°C	
Storage Temperature	Тѕт	-20	+60	°C	
Operating Ambient Humidity	Нор	10	90	%RH	2,3
Storage Humidity	Нѕт	10	90	%RH	

Note 1. Ambient temperature condition (Ta = 25 ± 2 °C)

- 2. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be Max 39°C, and no condensation of water.
- 3. Gravity mura can be guaranteed below 40°C condition.
- 4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68°C. The range of operating temperature may be degraded in case of improper thermal management in final product design.



Ver. 0.1 4 /43

Product Specification

3. Electrical Specifications

3-1. Electrical Characteristics

It requires several power inputs. The VCC is the basic power of LCD Driving power sequence, Which is used to logic power voltage of Source D-IC and GIP.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Condition	MIN	TYP	MAX	Unit	Note
Logic Power Voltage	VCC	-	1.62	1.8	1.98	VDC	
Logic High Level Input Voltage	VIH	-	1.62	-	VCC	VDC	
Logic Low Level Input Voltage	VIL	-	0	-	0.4(TBD)	VDC	
Source D-IC Analog Voltage	VDD	-	16.4(TBD)	16.7(TBD)	17.1(TBD)	VDC	
Half Source D-IC Analog Voltage	H_VDD	-	GMA10+50mV	8.37	GMA9-50mV	VDC	6
	V_{GMH}	(GMA1 ~ GMA9)	½*VDD	-	VDD-0.2	VDC	
Gamma Reference Voltage	V_{GML}	(GMA10 ~ GMA18)	0.2	-	½*VDD	VDC	
		Reverse	7.08(TBD)	7.38(TBD)	7.68(TBD)	V	
EPI Clock frequency	CLK	3.0V≤VCC ≤3.6V	-	-	-	MHz	
Vterm	Vterm	VCC=1.62V~1.98V Vcore_tx=1.0V,1.2 V	Vcore IX	Vcore_Tx	Vcore_Tx +10%	V	_
EPI Input eye diagram Veye		- (90(TBD)	-	-	mV	5
EPI input differential voltage Vdiff			150(TBD)	-	500	mV	
Gate High Voltage	VGH	@ 25℃	27.7(TBD)	28(TBD)	28.3(TBD)	VDC	
Cate High Voltage	VOIT	@0℃	29.7(TBD)	30(TBD)	30.3(TBD)	VDC	
Gate Low Voltage	VGL	-	-5.2(TBD)	-5.0(TBD)	-4.8(TBD)	VDC	
GIP Bi-Scan Voltage	VGI_P VGI_N	-	VGL	-	VGH	VDC	
GIP Refresh Voltage	VGH even/odd	-	VGL	-	VGH	V	
GIP Start Pulse Voltage	VST	-	VGL	-	VGH	V	
GIP Operating Clock	GCLK	-	VGL	-	VGH	V	
Total Power Current	ILCD	-	-	680 (TBD)	890 (TBD)	mA	1
Total Power Consumption	PLcd	-	-	8.2 (TBD)	10.6 (TBD)	Watt	1

Note:

- 1. The specified current and power consumption are under the V_{LCD}=12V., $25 \pm 2^{\circ}$ C, f_V =120Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
- 2. The above spec is based on the basic model.
- 3. All of the typical gate voltage should be controlled within 1% voltage level
- 4. Ripple voltage level is recommended under $\pm 5\%$ of typical voltage
- In case of EPI signal spec, refer to Fig 2 for the more detail.
 Vcore_Tx is the core voltage of EPI_Tx. Ex) Vcore_Tx=1.0V, 1.2.V
- 6. HVDD Voltage level is half of VDD and it should be between Gamma9 and Gamma10.

Ver. 0.1 5 /43

Global LCD Panel Exchange Center



LC550EUH

Product Specification

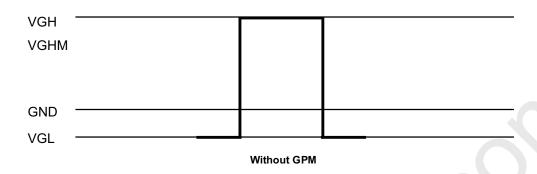


FIG. 1 Gate Output Wave form without GPM and with GPM

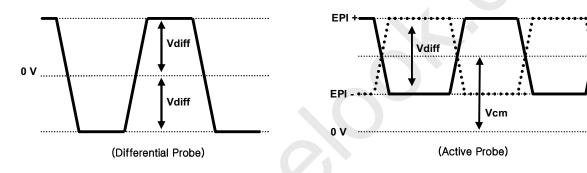


FIG. 2-1 EPI Differential signal characteristics

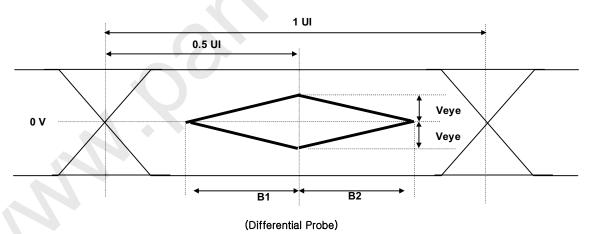


FIG. 2-2 Eye Pattern of EPI Input



FIG. 3 Measure point

Product Specification

Table 3. ELECTRICAL CHARACTERISTICS (Continue)

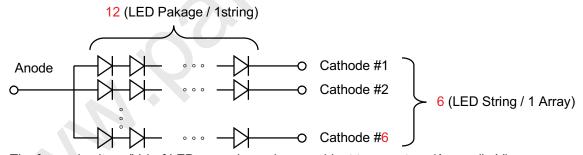
Parameter		Symbol	Values			Unit	Note
		Cymbol	Min Typ		Max	Oilit	Note
Backlight Assembly :		-					
Forward Current	Anode	I _{F (anode)}		630		mAdc	±5%
(one array)	Cathode	I _{F (cathode)}	99.75	105	110.25	mAdc	2, 3
Forward Voltage		V _F	70.8	78.0	84.9	Vdc	4
Forward Voltage Variation		$\triangle V_{F}$			1.7	Vdc	5
Power Consumption	on	P _{BL}		98.3	107.4	W	6
Burst Dimming Duty		On duty	1		100	%	
Burst Dimming Frequency		1/T	95		182	Hz	8
LED Array :							
Life Time			30,000	50,000		Hrs	7

Note: The design of the LED driver must have specifications for the LED array in LCD Assembly.

The electrical characteristics of LED driver are based on Constant Current driving type.

The performance of the LED in LCM, for example life time or brightness, is extremely influenced by the characteristics of the LED Driver. So, all the parameters of an LED driver should be carefully designed. When you design or order the LED driver, please make sure unwanted lighting caused by the mismatch of the LED and the driver (no lighting, flicker, etc) has never been occurred. When you confirm it, the LCD—Assembly should be operated in the same condition as installed in your instrument.

- 1. Electrical characteristics are based on LED Array specification.
- Specified values are defined for a Backlight Assembly. (IBL: 2 LED array/LCM)
- Each LED array has one anode terminal and 6 cathode terminals.
 The forward current(I_F) of the anode terminal is 630mA and it supplies 105mA into 6 strings, respectively



- 4. The forward voltage(V_F) of LED array depends on ambient temperature (Appendix-V)
- 5. ΔV_F means Max V_F -Min V_F in one Backlight. So V_F variation in a Backlight isn't over Max. 1.7V
- 6. Maximum level of power consumption is measured at initial turn on.

 Typical level of power consumption is measured after 1hrs aging at $25 \pm 2^{\circ}$ C.
- 7. The life time(MTTF) is determined as the time at which brightness of the LED is 50% compared to that of initial value at the typical LED current on condition of continuous operating at $25 \pm 2^{\circ}$ C, based on duty 100%
- The reference method of burst dimming duty ratio.
 It is recommended to use synchronous V-sync frequency to prevent waterfall (Vsync x 1 =Burst Frequency)
 - Though PWM frequency is over 182Hz (max252Hz), function of backlight is not affected.

Ver. 0.1 7 /43



Product Specification

3-2. Interface Connections

This LCD module employs two kinds of interface connection, two 50-pin FFC connector are used for the module electronics and 8-pin / 8-pin connectors are used for the integral backlight system.

3-2-1. LCD Module

-LCD Connector (CN1): TF06L-50S-0.5SH (Manufactured by HRS) or Compatible

Table 3-1. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	7		No	Symbol	Description		
1	LTD_OUT	LTD OUTPUT	26	GND	Ground		
2	NC	No Connection	27	EPI2-	EPI Receiver Signal(2-)		
3	GCLK1	GIP GATE Clock 1	28	EPI2+	EPI Receiver Signal(2+)		
4	GCLK2	GIP GATE Clock 2	29	GND	Ground		
5	GCLK3	GIP GATE Clock 3	30	GND	Ground		
6	GCLK4	GIP GATE Clock 4	31	EPI1-	EPI Receiver Signal(1-)		
7	GCLK5	GIP GATE Clock 5	32	EPI1+	EPI Receiver Signal(1+)		
8	GCLK6	GIP GATE Clock 6	33	GND	Ground		
9	VGI_N	GIP Bi-Scan (Normal =VGL Rotate = VGH)	34	VCC	Logic & EPI Power Voltage		
10	VGI_P	GIP Bi-Scan (Normal =VGH Rotate = VGL)	35	Vterm	Vterm Power Voltage		
11	VGH_ODD	GIP Panel VDD for Odd GATE TFT	36	LOCKOUT3	LOCKOUT3		
12	VGH_EVEN	GIP Panel VDD for Even GATE TFT	37	NC	No Connection		
13	VGL	GATE Low Voltage	38	GND	Ground		
14	VST	VERTICAL START PULSE	39	GMA 18	GAMMA VOLTAGE 18 (Output From LCD)		
15	GIP_Reset	GIP Reset	40	GMA 16	GAMMA VOLTAGE 16		
16	VCOM_L_FB	VCOM Left Feed-Back Output	41	GMA 15	GAMMA VOLTAGE 15		
17	VCOM_L	VCOM Left Input	42	GMA 14	GAMMA VOLTAGE 14		
18	GND	Ground	43	GMA 12	GAMMA VOLTAGE 12		
19	VDD	Driver Power Supply Voltage	44	GMA 10	GAMMA VOLTAGE 10 (Output From LCD)		
20	VDD	Driver Power Supply Voltage	45	GMA 9	GAMMA VOLTAGE 9 (Output From LCD)		
21	H_VDD	Half Driver Power Supply Voltage	46	GMA 7	GAMMA VOLTAGE 7		
22	GND	Ground	47	GMA 5	GAMMA VOLTAGE 5		
23	EPI3-	EPI Receiver Signal(3-)	48	GMA 4	GAMMA VOLTAGE 4		
24	EPI3+	EPI Receiver Signal(3+)	49	GMA 3	GAMMA VOLTAGE 3		
25	GND	Ground	50	GMA 1	GAMMA VOLTAGE 1 (Output From LCD)		

Note:

1. Please refer to application note for details.

(GIP & Half VDD & Gamma Voltage setting)

Ver. 0.1 8 /48



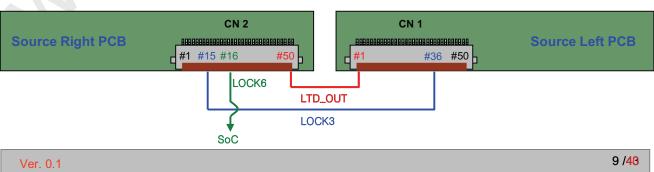
Product Specification

-LCD Connector (CN1): TF06L-50S-0.5SH (Manufactured by HRS) or Compatible

Table 3-2. MODULE CONNECTOR(CN2) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	GMA 1	GAMMA VOLTAGE 1 (Output From LCD)	26	GND	Ground
2	GMA 3	GAMMA VOLTAGE 3	27	EPI1-	EPI Receiver Signal(4-)
3	GMA 4	GAMMA VOLTAGE 4	28	EPI1+	EPI Receiver Signal(4+)
4	GMA 5	GAMMA VOLTAGE 5	29	GND	Ground
5	GMA 7	GAMMA VOLTAGE 7	30	H_VDD	Half Driver Power Supply Voltage
6	GMA 9	GAMMA VOLTAGE 9 (Output From LCD)	31	VDD	Driver Power Supply Voltage
7	GMA 10	GAMMA VOLTAGE 10 (Output From LCD)	32	VDD	Driver Power Supply Voltage
8	GMA 12	GAMMA VOLTAGE 12	33	GND	Ground
9	GMA 14	GAMMA VOLTAGE 14	34	VCOM_R	VCOM Right Input
10	GMA 15	GAMMA VOLTAGE 15	35	VCOM_R_FB	VCOM Right Feed-Back Output
11	GMA 16	GAMMA VOLTAGE 16	36	GIP_Reset	GIP Reset
12	GMA 18	GAMMA VOLTAGE 18 (Output From LCD)	37	VST	VERTICAL START PULSE
13	GND	Ground	38	VGL	GATE Low Voltage
14	LOCKOUT6	LOCKOUT6	39	VGH_EVEN	GIP Panel VDD for Even GATE TFT
15	LOCKIN3	LOCKIN3	40	VGH_ODD	GIP Panel VDD for Odd GATE TFT
16	Vterm	Vterm Power Voltage	41	VGI_P	GIP Bi-Scan (Normal =VGH Rotate = VGL)
17	VCC	Logic & EPI Power Voltage	42	VGI_N	GIP Bi-Scan (Normal =VGL Rotate = VGH)
18	GND	Ground	43	GCLK6	GIP GATE Clock 6
19	EPI6-	EPI Receiver Signal(6-)	44	GCLK5	GIP GATE Clock 5
20	EPI6+	EPI Receiver Signal(6+)	45	GCLK4	GIP GATE Clock 4
21	GND	Ground	46	GCLK3	GIP GATE Clock 3
22	GND	Ground	47	GCLK2	GIP GATE Clock 2
23	EPI5-	EPI Receiver Signal(5-)	48	GCLK1	GIP GATE Clock 1
24	EPI5+	EPI Receiver Signal(5+)	49	NC	No Connection
25	GND	Ground	50	LTD_OUT	LTD OUTPUT

Note: 1. Please refer to application note for details. (GIP & Half VDD & Gamma Voltage setting)





Product Specification

3-2-2. Backlight Module

[CN201]

- 1) LED Array ass'y Connector (Plug)
 - : HS100-L08N-N62, (black color, manufactured by UJU)
- 2) Mating Connector (Receptacle)
 - : IS100-L08T-C46 (black color, manufactured by UJU)

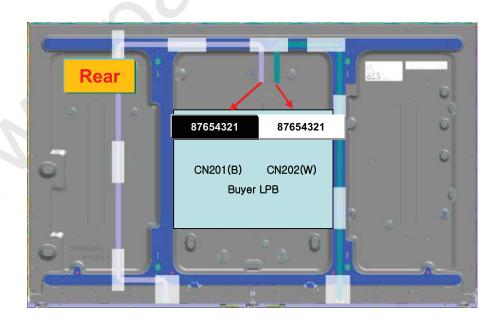
[CN202]

- 1) LED Array ass'y Connector (Plug)
 - : HS100-L08N-N62-A (natural color, manufactured by UJU)
- 2) Mating Connector (Receptacle)
 - : IS100-L08T-C46-A (natural color, manufactured by UJU)

Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION(CN201,CN202)

No	Symbol(CN201)	Description	Note
1	R6 Cathode	LED Output Current	
2	R5 Cathode	LED Output Current	
3	R4 Cathode	LED Output Current	
4	R3 Cathode	LED Output Current	
5	R2 Cathode	LED Output Current	
6	R1 Cathode	LED Output Current	
7	N.C	Open	
8	Anode_R	LED Input Current for R1~R6	

No	Symbol(CN202)	Description	Note
1	Anode_L	LED Input Current for L1~L6	
2	N.C	Open	
3	L6 Cathode	LED Output Current	
4	L5 Cathode	LED Output Current	
5	L4 Cathode	LED Output Current	
6	L3 Cathode	LED Output Current	
7	L2 Cathode	LED Output Current	
8	L1 Cathode	LED Output Current	



Product Specification

3-3. Signal Timing Specifications

Table 4. Timing Requirements

Parameter	Symbol	Condition	Min	Тур	Max	Unit	Note
Unit Interval	UI	-	0.59(TBD)	ı	ı	ns	
DLL Lock time	Tlock	-	1.6(TBD)	-	200(TBD)	Us	Fig 4
Effective Veye width time	B1&B2	-	0.25(TBD)	-	-	UI	Fig. 2
SSC	Vspread	@100KHz	-	-	2(TBD)	%	
Receiver off to SOE rising time	tSOE_ Rising		5(TBD)	-	-	Packet	Fig.5
SOE pulse width	tSOE_ Width	-	4(TBD)	-	-	Packet	Fig.5
SOE rising to 1st data time	tSOE_DA TA	-	5(TBD)	-	-	Packet	Fig.5
EPI Data Rate	BW	-	1.251	-	1.456	GBPS	

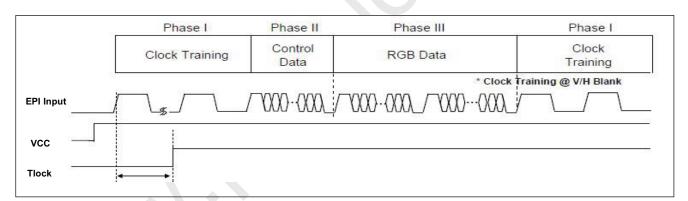


FIG 4. Power On to DLL Lock time

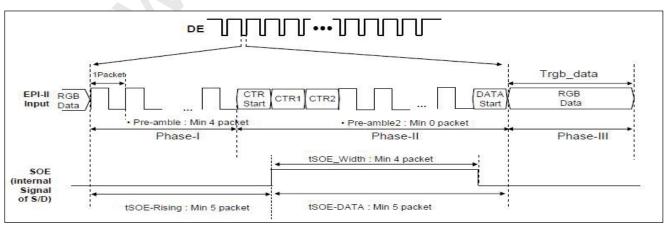


FIG 5. SOE Width & Timing



Product Specification

3-5. Panel Pixel Structure

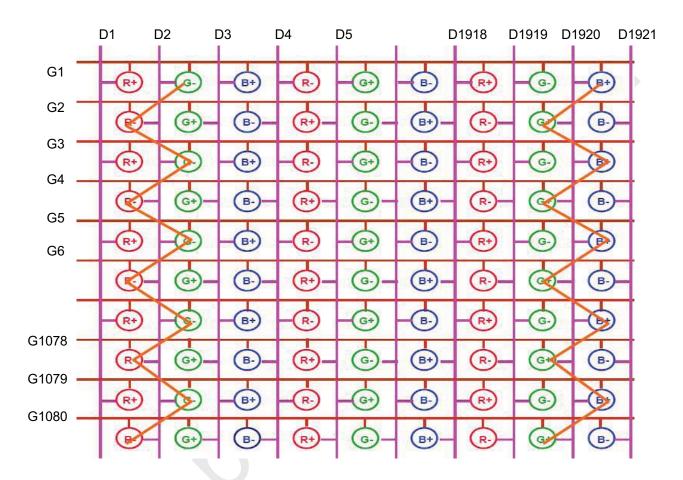
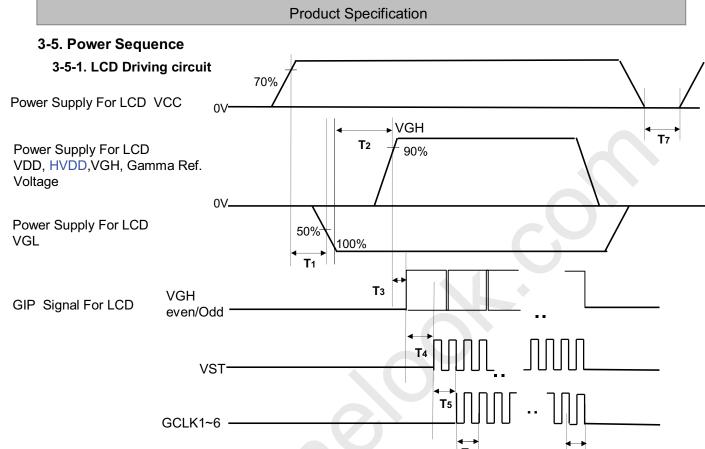


FIG. 8 Panel Pixel Structure



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Power For LED

Table 7. POWER SE	able 7. POWER SEQUENCE							
Daramatar		Linit	Notes					
Parameter	Min Typ Max		Max	Unit	Notes			
T1	0.5	0.5		ms				
T ₂	0.5	-	-	ms				
Т3	0	-	-	ms				
T4	10	-	-	ms	2			
T ₅	0	-	-	ms				
T6 / T6'	20	-	-	ms	6			
T 7	2	-	-	S				

LED on

Note: 1. Power sequence for Source D-IC must follow the Case1 & 2. Please refer to Appendix IV for more details.

- 2. VGH Odd signal should be started "High" status and VGH even & odd can not be "High at the same time.
- 3. Power Off Sequence order is reverse of Power On Condition including Source D-IC.
- 4. GCLK On/Off Sequence

Normal : GCLK4 \rightarrow GCLK5 \rightarrow GCLK6 \rightarrow GCLK1 \rightarrow GCLK2 \rightarrow GCLK3. Reverse :GCLK3 → GCLK2 →GCLK1 → GCLK6 → GCLK5 → GCLK4.

- 5. VDD_odd/even transition time should be within V_blank
- 6. In case of T6', If there is no abnormal display, no problem



Product Specification

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at 25±2°C. The values are specified at distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 °. FIG. 1 shows additional information concerning the measurement equipment and method.

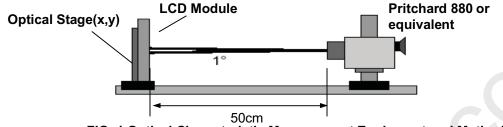


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 10. OPTICAL CHARACTERISTICS

Ta= 25±2°C, VDD,H_VDD,VGH,VGL=typ, fV=120Hz, BW=1,386GBPS, I_E = 105mA (Tvp)

						Value	120112, 077-	1.386GBPS, I _F =	,		
	Parame	ter	Sym	bol	Min	Тур	Max	Unit	Note		
Contrast F	Ratio		CR		CR		1100	1600	-		1
Surface Luminance, wh		nito.		2D	320	400		cd/m ²	2		
Surface Luminance, white Luminance Variation		iite	L _{WH}	3D	120	150		Cu/III-	7		
Luminanc	e Variation		δ_{WHITE}	5P			1.3		3		
		Rising	Tf	f		8	12		4		
Response Time Color Coordinates [CIE1931]	Falling	Tf	f	-	10	14	ms	4			
		DED	R	x		TBD TBD TBD TBD TBD Typ					
		RED	Ry	у	TBD						
			G	x		TBD					
	ordinates	GREEN	Gy	y	Тур	TBD	Тур				
			В	K	-0.03	TBD	+0.03				
		BLUE	Ву	y		TBD					
			W	Wx		0.279					
		WHITE	W	у		0.292					
Color Ter	mperature					10,000		К			
Color Gai	mut					72		%			
		right(φ=0°)	θr (x a	axis)	89	-	-				
	2D	left (φ=180°)	θl (х а	axis)	89	-	-		5		
\	(CR>10)	(CR>10) up (φ=90°) θu (y axis	axis)	89	-	-	degree -				
Viewing Angle		down (φ=270°)	θ d (y a	axis)	89	-	-				
, algic	3D	up + down	θu (y a +θd (axis) (y axis)	22	26 -		7			
	(CT≤10%)	up	θu (y a		10				7.0		
		down	θ d (y a	axis)	10				7,8		
3D Crosst	talk		3D C	C/T	-	1	3	%			
Gray Scal	е				-	-	_		6		

Product Specification

Note: 1. Contrast Ratio(CR) is defined mathematically as:

Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

It is measured at center 1-point.

- 2. Surface luminance is determined after the unit has been 'ON' and 1 Hour after lighting the backlight in a dark environment at 25±2°C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
- 3. The variation in surface luminance , δ WHITE is defined as : δ WHITE(5P) = Maximum($L_{on1}, L_{on2}, L_{on3}, L_{on4}, L_{on5}$) / Minimum($L_{on1}, L_{on2}, L_{on3}, L_{on4}, L_{on5}$) Where L_{on1} to L_{on5} are the luminance with all pixels displaying white at 5 locations . For more information, see the FIG. 2.
- 4. Response time is the time required for the display to transit from G(255) to G(0) (Rise Time, Tr_R) and from G(0) to G(255) (Decay Time, Tr_D). For additional information, see the FIG. 11. (N<M)
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- Gray scale specification Gamma Value is approximately 2.2. For more information, see the Table 11.
- 7. 3D performance specification is expressed by 3D luminance, 3D Crosstalk and 3D viewing angle. 3D luminance and 3D crosstalk is measured at center 1-point. For more information, see the FIG 5~8.
- 8. The specification of 3D viewing angle is temporarily permitted by 8° until 4Q of 2011

Table 11. GRAY SCALE SPECIFICATION

Gray Level	Luminance [%] (Typ)
L0	0.063
L15	0.28
L31	1.05
L47	2.50
L63	4.69
L79	7.67
L95	11.47
L111	16.11
L127	21.64
L143	28.07
L159	35.43
L175	43.73
L191	52.99
L207	63.23
L223	74.47
L239	86.72
L255	100

	Gray Level	Gamma Ref.
	LO	Gamma9
	L1	Gamma8
	L31	Gamma7
Positive	L63	Gamma6
Voltage	L127	Gamma5
	L191	Gamma4
	L223	Gamma3
	L255	Gamma1
	L255	Gamma18
	L223	Gamma16
	L191	Gamma15
Negative	L127	Gamma14
Voltage	L63	Gamma13
	L31	Gamma12
	L1	Gamma11
	L0	Gamma10

Ver. 0.1 15 /43

Global LCD Panel Exchange Center

LC550EUH

Product Specification

Measuring point for surface luminance & measuring point for luminance variation.

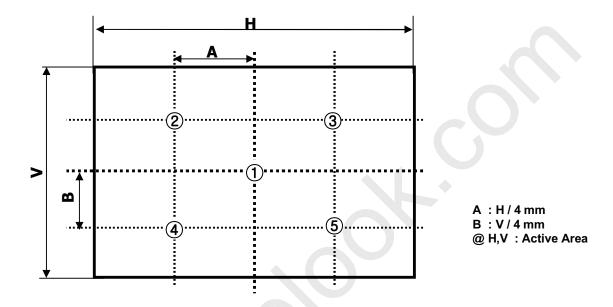


FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

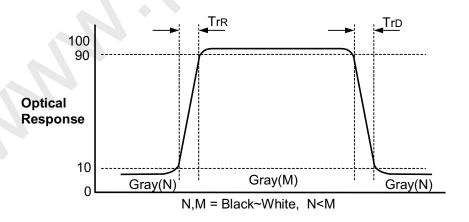


FIG. 3 Response Time

Product Specification

Dimension of viewing angle range

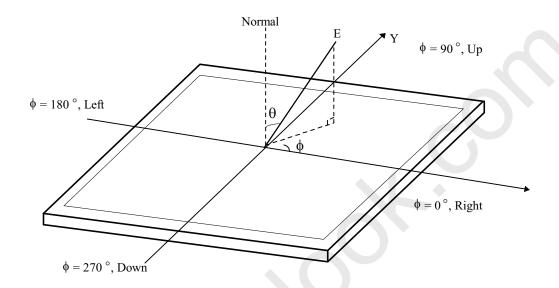
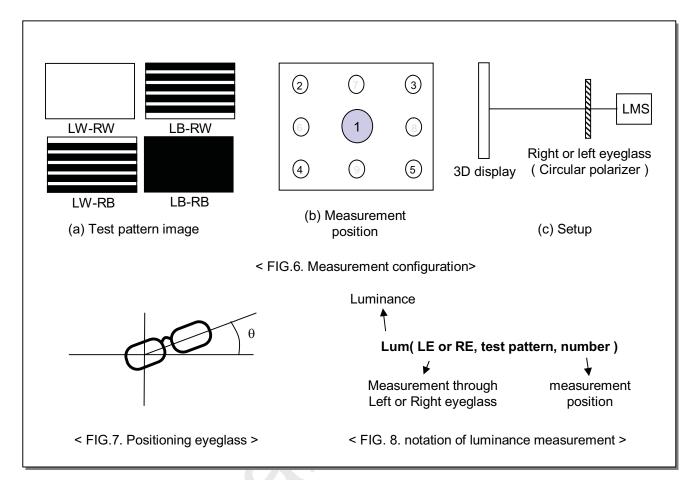


FIG. 5 Viewing Angle



Product Specification



In order to measure 3D luminance, 3D crosstalk and 3D viewing angle, it need to be prepared as below;

- 1) Measurement configuration
 - 4-Test pattern images. Refer to FIG 8.
 - -. LW-RW: White for left and right eye
 - -. LW-RB : White for left eye and Black for right eye
 - -. LB-RW : Black for left eye and white for right eye
 - -. LB-RB: Black for left eye and right eye

Image files where black and white lines are displayed on even or odd lines.

Luminance measurement system (LMS) with narrow FOV (field of view) is used. Refer to FIG 1.

2) Positioning Eyeglass (refer to appendix-VIII for standard specification of eyeglass) Find angle of minimum transmittance.

This value would be provided beforehand or measured by the following steps;

- (i) Test image (LB-RW) is displayed.
- (ii) Left eyeglass are placed in front of LMS and luminance is measured, rotating right eyeglass such as FIG 7. The notation for luminance measurement is "Lum(LE, LB-RW,1)".
- (iii) Find the angle where luminance is minimum.
- * Following measurements should be performed at the angle of minimum transmittance of eyeglass.

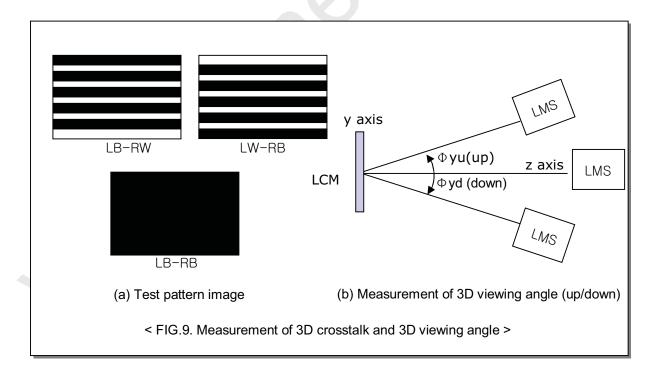


Product Specification

- 3) Measurement of 3D luminance
 - (i) Test image (LW-RW) is displayed.
 - (ii) Left or right eyeglass are placed in front of LMS successively and luminance is measured at center 1 point where the notation for luminance measurement is "Lum(LE, LW-RW,1)" or "Lum(RE, LW-RW,1).
- 4) Measurement of 3D crosstalk
 - (i) Test image (LB-RW, LW-RB and LB-RB) is displayed.
 - (ii) Right or left eyeglass are placed in front of LMS successively and luminance is measured for position 1. with rotating LMS or sample vertically.

5) Measurement of 3D Viewing Angle

3D viewing angle is the angle at which the 3D crosstalk is under 10%. The angles are determined for the vertical or y axis with respect to the z axis which is normal to the LCD module surface and measured for position 1. For more information, see the Fig 9





Product Specification

5. Mechanical Characteristics

Table 12 provides general mechanical characteristics.

Table 12. MECHANICAL CHARACTERISTICS

Item	Value		
	Horizontal	1228.6 mm	
Outline Dimension	Vertical	711.4 mm	
	Depth	20.7 mm	
Denot Arra	Horizontal	1230.4 mm	
Bezel Area	Vertical	693.9 mm	
Active Diepley Area	Horizontal	1209.6 mm	
Active Display Area	Vertical	680.4 mm	
Weight	15.4Kg (Typ.), 16.0kg (Max.)		

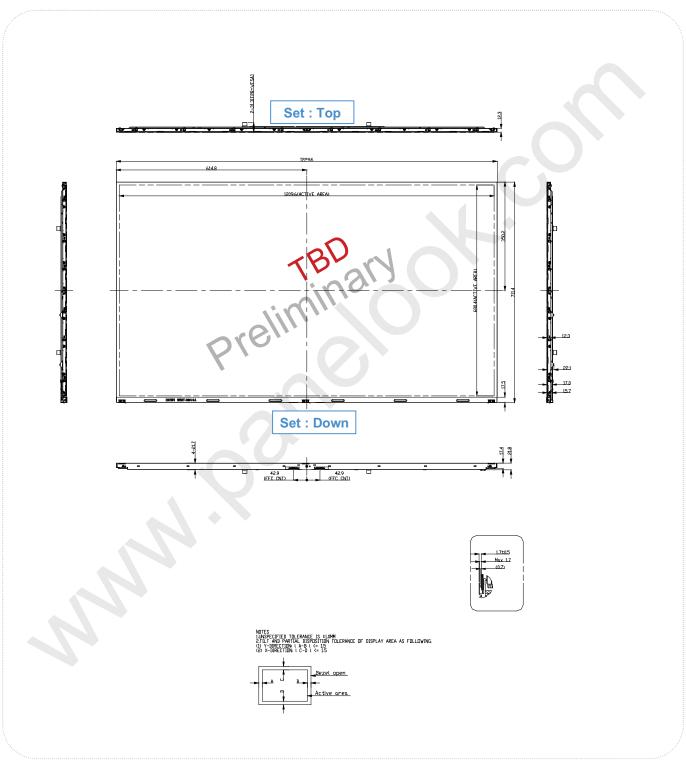
Note: Please refer to a mechanical drawing in terms of tolerance at the next page.

Ver. 0.1 20 /43



Product Specification

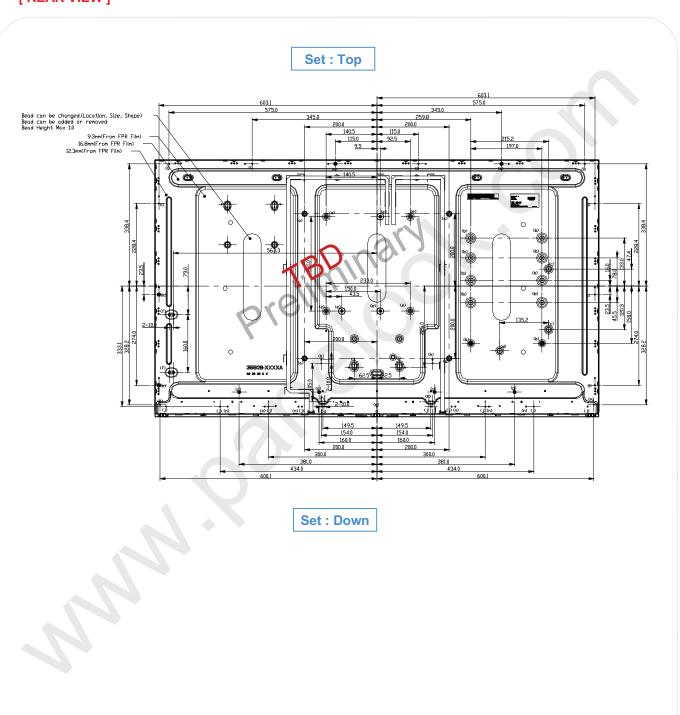
[FRONT VIEW]





Product Specification

[REAR VIEW]



Ver. 0.1 22 /43



Product Specification

6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition			
1	High temperature storage test	Ta= 60°C 240h			
2	Low temperature storage test	Ta= -20°C 240h			
3	High temperature operation test	Ta= 50°C 50%RH 240h			
4	Low temperature operation test	Ta= 0°C 240h			
5	Humidity condition Operation	Ta= 40 °C ,90%RH			
6	Altitude operating storage / shipment	0 - 15,000 ft 0 - 40,000 ft			

Note: Before and after Reliability test, LCM should be operated with normal function.

Ver. 0.1 23 /43



Product Specification

7. International Standards

7-1. Safety

- a) UL 60065, Underwriters Laboratories Inc.
 Audio, Video and Similar Electronic Apparatus Safety Requirements.
- b) CAN/CSA C22.2 No.60065:03, Canadian Standards Association.Audio, Video and Similar Electronic Apparatus Safety Requirements.
- c) EN 60065, European Committee for Electrotechnical Standardization (CENELEC).
 Audio, Video and Similar Electronic Apparatus Safety Requirements.
- d) IEC 60065, The International Electrotechnical Commission (IEC). Audio, Video and Similar Electronic Apparatus Safety Requirements. (Including report of IEC60825-1:2001 clause 8 and clause 9)

Notes

1. Laser (LED Backlight) Information

Class 1M LED Product IEC60825-1 : 2001 Embedded LED Power (Class1M)

2. Caution

: LED inside.

Class 1M laser (LEDs) radiation when open. Do not open while operating.

7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics – Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

Ver. 0.1 24 /43



Product Specification

8. Packing

8-1. Information of LCM Label

a) Lot Mark

A B C D E F G H I J K L M

A,B,C: SIZE(INCH)

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	Α	В	С	D	Е	F	G	I	J	K

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

D:YEAR

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one Pallet: 16 pcs

b) Pallet Size: 1300 mm(W) X 1140 mm(D) X 848.5mm(H)

Ver. 0.1 25 /43



Product Specification

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 \text{mV}$ (Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

 And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw. (if not, it can causes conductive particles and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.

Ver. 0.1 26 /43



Product Specification

9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.
- (3) Storage condition is guaranteed under packing conditions.
- (4) The phase transition of Liquid Crystal in the condition of the low or high storage temperature will be recovered when the LCD module returns to the normal condition

9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normalhexane.

9-7. Operating condition guide

- (1) The LCD product should be operated under normal conditions. Normal condition is defined as below;
 - Temperature : 5 ~ 40 °C, normal humidity.
 - Display pattern : continually changing pattern (Not stationary)
- (2) If the product will be used in extreme conditions such as high temperature, display patterns or operation time etc...

It is strongly recommended to contact LGD for Qualification engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems. The LCD product should be applied by global standard environment. (refer ETSI EN 300, IEC 60721)

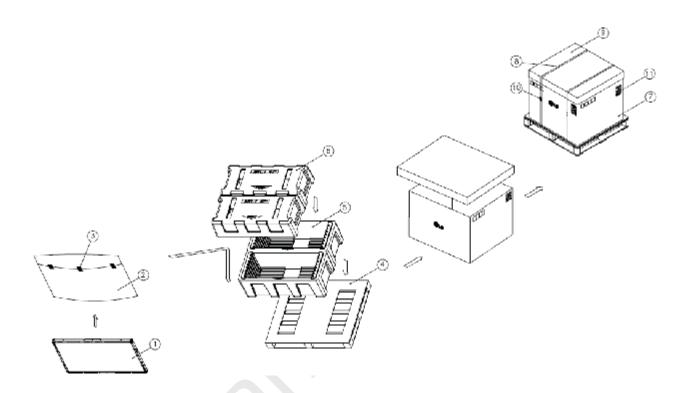
Ver. 0.1 27 /43



Product Specification

APPENDIX-I

■ Pallet Ass'y



NO.	DESCRIPTION	MATERIAL
1	LCD Module	55" LCD
2	BAG	55INCH
3	TAPE	MASKING 20MMX50M
4	PALLET	Plywood 1300X1140X125.5mm
5	PACKING,BOTTOM	EPS
6	PACKING,TOP	EPS
7	ANGLE,PACKING	PAPER
8	BAND	PP
9	ANGLE,COVER	PAPER
10	BAND	STEEL OR PP
11	LABEL	YUPO 80G 100X70

Product Specification

APPENDIX- II-1

■ LCM Label



■ Production site

- LG Display (Paju) Co., LTD
- LG Display (WR) Co., LTD (cDMS Line)
- (TBD)

Note 1. The origin of LCM Label will be changed according to the production site.

Ver. 0.1 29 /43



Product Specification

APPENDIX- II-2

■ Box Label

■ Pallet Label





Ver. 0.1 30 /43

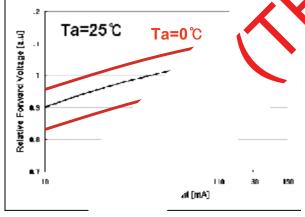
Product Specification

APPENDIX-III

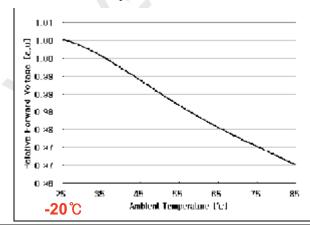
■ LED Array Electrical Spec

Items	Symbol	Condition	Min	Тур	√Max	Unit
Module Current	I _F		-	150 (**	1,0	mA
Array Operating Voltage	V_{F}	I _{FM} =150mA	126	12-		V
Array Operating Voltage	△Vop *2)	I _{FM} =150m♠				V
Luminous of White	lv	I _{FM} =150mA		F	_1	nit
Color Chromoticity	cx C	I _{FM} =150mA	16.31		٥1 _	
Color Chromaticity		I _{FM} =150mA			0.236	
Bright Uniformity *3)	Bu	150m2				%
Color Uniformity *4)	(ux)	V _{FM} +NF′			0.007	

■ Forward Current vs. Forward Vo¹′



■ Ambient Tem *Acure vs. Forward Voltage*

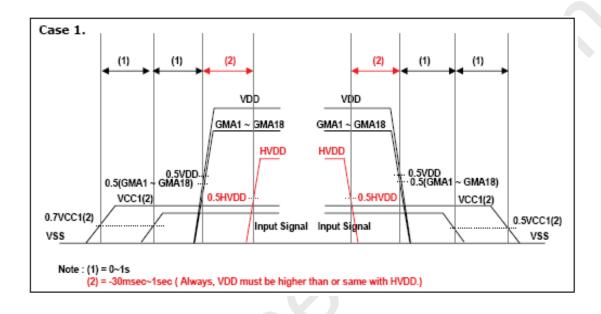


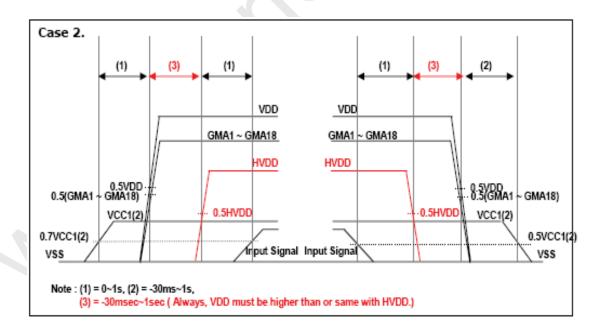


Product Specification

APPENDIX- IV

■ Source D-IC Power Sequence





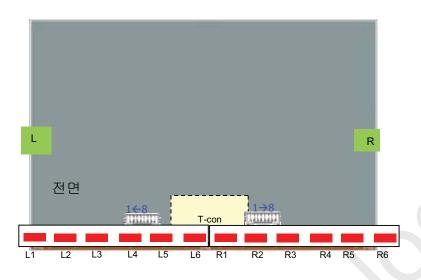
Ver. 0.1 32 /43

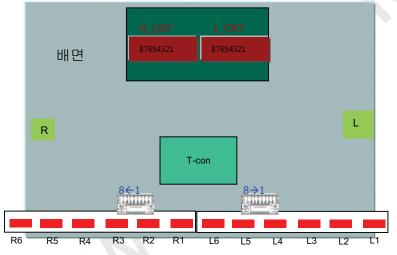


Product Specification

APPENDIX-V

■ Local Dimming Block Pin Matching





LED Driver CNT					
출력 L_CNT(8pin)	출력 R_CNT(8pin)				
Anode_L	R6 Cathode				
N.C	R5 Cathode				
L1 Cathode	R4 Cathode				
L2 Cathode	R3 Cathode				
L3 Cathode	R2 Cathode				
L4 Cathode	R1 Cathode				
L5 Cathode	N.C				
L6 Cathode	Anode_R				
	출력 L_CNT(8pin) Anode_L N.C L1 Cathode L2 Cathode L3 Cathode L4 Cathode				

Ver. 0.1 33 /43

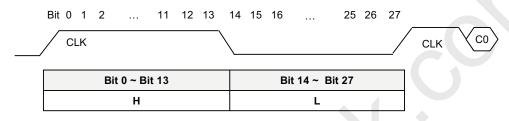
Product Specification

APPENDIX-VI

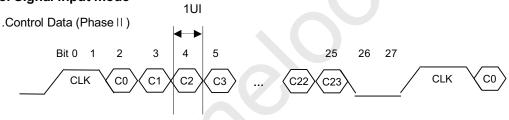
■ EPI Input Protocol

1. Clock Training Pattern input mode

. Clock Training Pattern (Phase $\ensuremath{\,\mid\,}$)



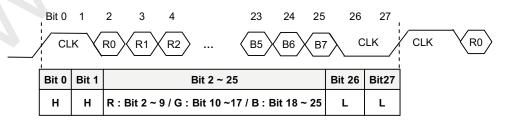
2. Control Signal input mode



Bit 0	Bit 1	Bit 2 ~ 25	Bit 26	Bit 27
Н	Н	Control Data	L	L

3. Display Data input mode

. RGB Data (Phase III)



Ver. 0.1 34 /43



Product Specification

APPENDIX- VII

■ Standard specification of Eyeglasses

This is recommended data of Eyeglasses for LC420EUF-SDF1 model. (details refer to table)

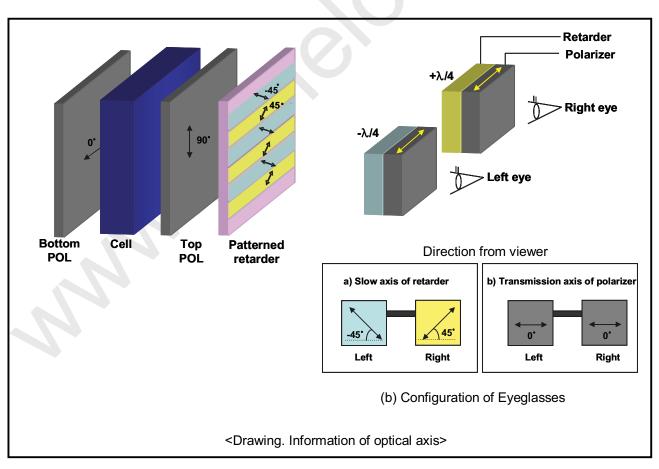
For each item, depending on the eyeglass manufacturer tolerances may occur, this tolerance can affect 3D performance. (3D Crosstalk, 3D luminance, 3D viewing angle)

<Table. Standard specification of Eyeglasses>

De	Left	Right	Remark	
Optical	a) Slow axis of retarder	-45°	45°	Refer to
axis	b) Transmission axis of polarizer	0°	0°	drawing
Retardation Retarder		125	inm	@550nm

※ Recommended polarizer

Polarization efficiency: more than 99.90%





Product Specification

APPENDIX- VIII-1

■ Fixer Ass'y Mechanical Characteristics

Item	Value			
	Horizontal	1228.6 mm		
Outline Dimension (Only Glass)	Vertical	699.5 mm		
(C.i.) Class,	Thickness	5.4 mm		
A (Horizontal	1209.6 mm		
Active Display Area	Vertical	680.4 mm		
Weight	2.5 kg (typ.)			
Surface Treatment	Hard coating(2H) Anti-glare treatment of the front polarizer(1%)			

Note: Please refer to a mechanic drawing in terms of tolerance at the next page.

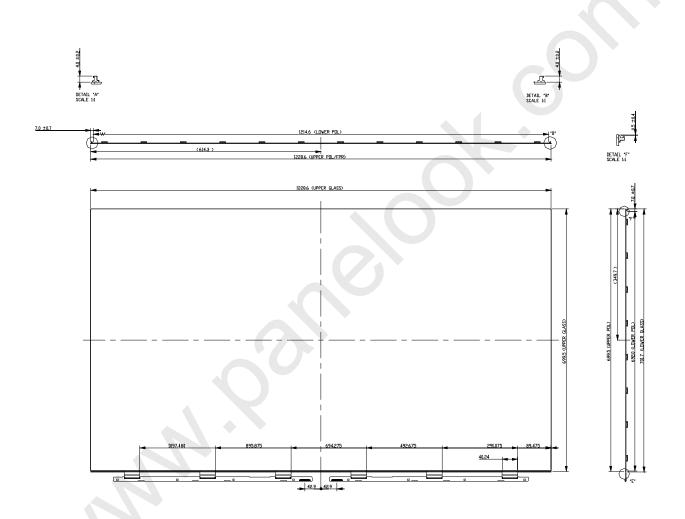
Ver. 0.1 36 /43



Product Specification

APPENDIX- VIII-2

■ Fixer Ass'y Mechanic Drawing (Front View)

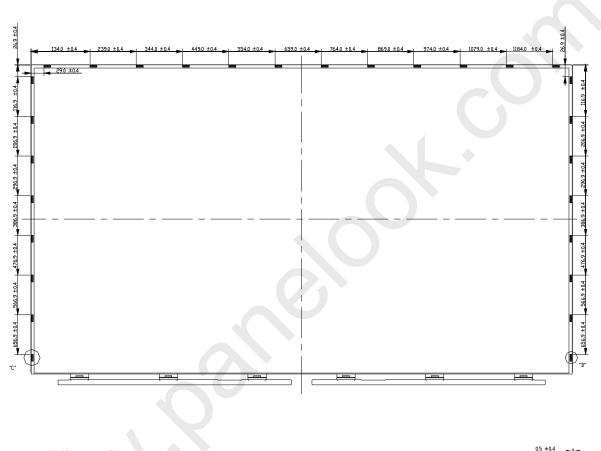




Product Specification

APPENDIX- VIII-3

■ Fixer Ass'y Mechanic Drawing (Rear View)



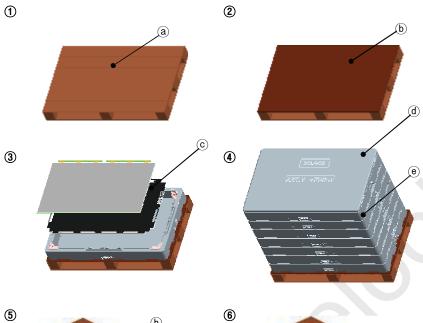


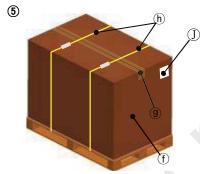


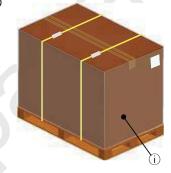
Product Specification

APPENDIX- VIII-4

■ Fixer Ass'y Pallet







No.	Description	Material		
a	Pallet	Plywood		
b	Carton Plate	Single Wall		
©	PE Sheet	Carbon		
(d)	Top Packing	EPP		
(8)	Bottom Packing	EPP		
(f)	Angle Packing	Single Wall		
9	Tape	OPP		
h	Band	PP		
(j)	Wrap	PE		

Ver. 0.1 39 /43



Product Specification

APPENDIX- VIII-5

■ Fixer Ass'y ID Label



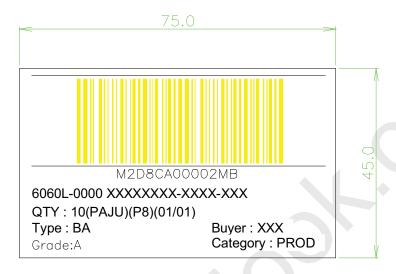
Ver. 0.1 40 /43



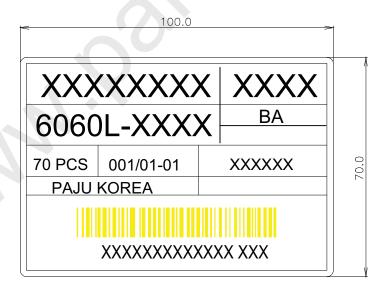
Product Specification

APPENDIX- VIII-6

■ Fixer Ass'y Box Label



■ Fixer Ass'y Pallet Label



Ver. 0.1 41 /43



Product Specification

APPENDIX- IV-1

■ Fixer Ass'y Precaution

1. Assembly Precautions

- (1) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (2) You should adopt radiation structure to satisfy the temperature specification.
- (3) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (4) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (5) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer
- (6) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (7) Board ass'y should be put on the mold frame properly.
- (8) FFC Cable should be connected between System board and Source PCB correctly.
- (9) Mechanical structure for backlight system should be designed for sustaining board ass'y safely.

2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm200mV(Over\ and\ under\ shoot\ voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

 And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or electrical impact to board assy. Otherwise, it can't be operated its full characteristics perfectly.

3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly. Panel ground path should be connected to metal ground.

Ver. 0.1 42 /43

Product Specification

APPENDIX- IV2

■ Fixer Ass'y Precaution

4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

5. Storage

When storing the board ass'y as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the board ass'y to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

Ver. 0.1 43 /43