NAN YA PLASTICS CORPORATION

SPECIFICATION OF
LCD MODULE
PRODUCT NO.: LCBHBTB61MS

SPEC. NO: LMB61-0C-△0

CUSTOMER
APPROVED BY
DATE:

LCD DEPARTMENT
ELECTRONIC MATERIALS DIVISION
NAN YA PLASTICS CORPORATION
201, TUNG HWA N. ROAD, TAIPEI
TEL: 886-2-27122211 EXT. 5993~5995
FAX: 886-2-27178253
E-mail: lcdsales@npc.com.tw

EDITED ON : Mar. 03. 2006

Q.C. DEPT.	DESIGN MANAGER	DESIGN CHECK	DESIGNER
			W.R.HSU

RECORDS OF REVISION

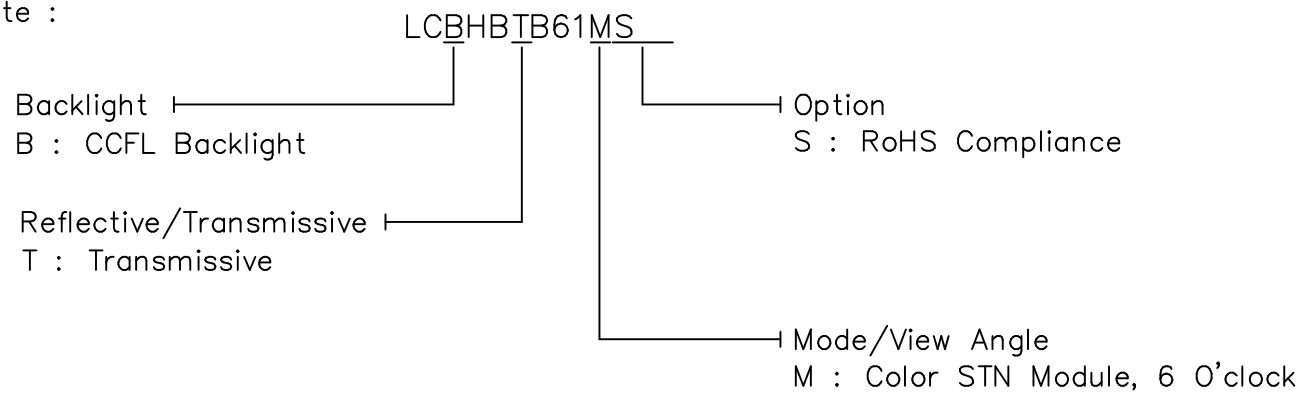
SPEC. NO.: LMB61-0C

DATE	REVISED NO.	REF. PAGE	SUMMARY	DESIGN	CHECK
03.03.06'	0	1~26/26	First Issue	W.R.HSU	

1. MECHANICAL DATA

NO	ITEM	CONTENTS	UNIT
1	Product No.	LCBHBTB61MS	–
2	Module Size	168.0 (W) x 111.0 (H) x 7.4 (D)	mm
3	Dot Size	0.10 (W) x 0.34 (H)	mm
4	Dot Pitch	0.12 (W) x 0.36 (H)	mm
5	Number of Dots	320 x RGB (W) x 240 (H)	Dot
6	Duty	1/240	–
7	LCD Display Mode	FSTN, Color STN Module	–
8	Rear Polarizer	Color Transmissive Type	–
9	Viewing Direction	6	O'clock
10	Backlight	CCFL	–
11	Controller	Excluded	–
12	DC/DC Converter	Excluded	–
13	Touch Panel	Excluded	–
14	Weight	280 (Approx.)	g

Note :



RoHS Compliance.

Nan Ya guarantees that this project doesn't include any materials (6 materials) or includes less than specified quantities which are regulated by RoHS Compliance.

2. ABSOLUTE MAXIMUM RATINGS

(1) ELECTRICAL ABSOLUTE RATINGS

VSS=0V

	SYMBOL	MIN.	MAX.	UNIT	COMMENT
Power Supply for Logic	VDD-VSS	-0.3	5.5	V	
Input Voltage	VI	-0.3	VDD	V	
Static Electricity	-	-	-	-	Note 1

Note 1 LCM should be grounded during handling LCM.

(2) ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	WIDE TEMP.			
	OPERATING		STORAGE	
	MIN.	MAX.	MIN.	MAX.
Ambient Temperature	-20	70	-40	80
Humidity (Without Condensation)	Note 2,4		Note 3,4	

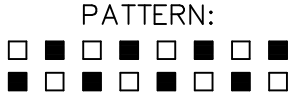
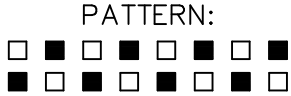
Note 2 Ta ≦ 70°C : 75%RH max

Note 3 Please refer to item of reliability test

Note 4 Background color will change slightly depending on ambient temperature. That phenomenon is reversible.

3. ELECTRICAL CHARACTERISTICS

3-1. ELECTRICAL CHARACTERISTICS OF LCM

ITEM		SYMBOL	CONDITION		MIN.	TYP.	MAX.	UNIT
Logic Circuit Power Supply		VDD-VSS	Ta= 25°C		4.5	5.0	5.5	V
Logic Circuit Power Supply		VDD-VSS	Ta= 25°C		2.7	3.0	3.3	V
Input Voltage		VIH	H level		0.8VDD	-	VDD	V
		VIL	L level		0	-	0.2VDD	
Recommended LCD Driving Voltage		VLCD-VSS	Duty=1/240 VDD=3V/5V	-20°C	24.6	25.0	25.4	V
				0°C	23.2	23.6	24.0	
				25°C	22.2	22.6	23.0	
				50°C	21.1	21.5	21.9	
				70°C	20.5	20.9	21.3	
Supply Current for Logic		IDD	VDD-VSS = 5.0V VLCD-VSS = 22.6V Ta= 25°C PATTERN: 		-	1	6	mA
Supply Current for LCD		ILCD			-	10	20	
Supply Current for Logic		IDD	VDD-VSS = 3.0V VLCD-VSS = 22.6V Ta= 25°C PATTERN: 		-	2	6	mA
Supply Current for LCD		ILCD			-	10	20	
LCM	Surface Luminance	L	PATTERN: (Dots All On of White Color)		60	90	-	cd/m²
			PATTERN: (Dots All Off)		-	6	10	
Recommended Frame Frequency for Optimum Coctrast		FLM	-		115	120	125	Hz

3-2.ELECTRICAL CHARACTERISTICS OF BACKLIGHT

Used Lamp Rating Temp.=25°C

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	REMARK
Lamp Voltage	V_L	–	296	–	Vrms	–
Lamp current	I_L	–	5	–	mArms	–
Lamp power consumption	P_L	–	1.48	–	W	(*1)
Starting voltage	Vs	–	–	460	Vrms	Ta=25°C
		–	–	580	Vrms	Ta=0°C
Lamp life time	L_L	–	20000	–	hrs	at I_L = 5 mArms Ta=25°C (*2)

(*1) Power consumption excluded inverter loss .

(*2) Lamp life time is defined as follows : The final brightness is at 50% of original brightness

3-3.ELECTRICAL CHARACTERISTICS OF RECOMMENDED INVERTER TDK CXA-L10L

3-3-1 GENERAL SPECIFICATIONS

OPERATION TEMPERATURE : -10°C~60°C
STORAGE TEMPERATURE : -20°C~85°C
DIMENSION : 44.0(L)mm x 21.0(W)mm x MAX 18.0(H)mm

3-3-2 PIN ASSIGNMENTS

INPUT (CN1) CONNECTOR :

NO.	FUNCTION
1	VIN
2	GND

OUTPUT (CN2) CONNECTOR :

NO.	FUNCTION
3	OUT1
4	OUT2
5	OUT GND

3-3-3 RELATIONSHIP BETWEEN VIN & TUBE CURRENT

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	REMARK
Input Voltage	VIN	-	10.1	-	V	
Tube Current	IL	-	5	-	mA	

4.OPTICAL CHARACTERISTICS

4-1.Optical Char. of Wide Temp. Mode

AT V_{OP}

ITEM / MODE		Cr(Contrast Ratio)										θ(Viewing Angle)		φ(Viewing Angle)	
		−20℃		0℃		25℃		50℃		70℃		25℃		25℃	
		MIN.	TYP.	MIN.	TYP.	MIN.	TYP.	MIN.	TYP.	MIN.	TYP.	MIN.	TYP.	MIN.	TYP.
T	M	12	18	14	20	17	25	3.5	5	1.4	2	−	F: 45 R: 40	−	L: 35 R: 35
NOTE		NOTE 6										NOTE 5			

NOTE :

T : Transmissive

V : Color STN Module, 6 O'clock

AT φ=0° θ=0°

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
Response Time (rise)	Tr	−20℃	2300	2900	4300	ms	NOTE 2
		0℃	640	800	1200		
		25℃	240	300	450		
		50℃	80	100	150		
		70℃	45	55	80		
Response Time (fall)	Tf	−20℃	1500	1800	2700	ms	NOTE 2
		0℃	360	450	670		
		25℃	80	100	150		
		50℃	48	60	90		
		70℃	32	40	60		

4-2.Color of CIE Coordinate

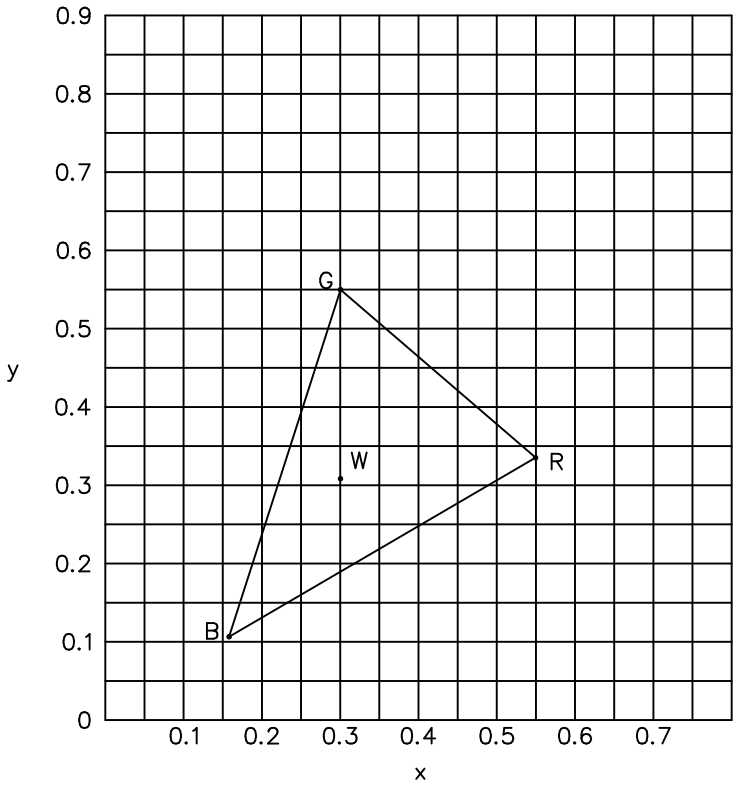
Ta = 25°C

ITEM		SYMBOL	CONDITION	VALUE	NOTE
Color of CIE Coordinate	Red	X	ϕ=0° , θ=0°	0.55	Note※
		y		0.33	
	Green	X	ϕ=0° , θ=0°	0.30	
		y		0.55	
	Blue	X	ϕ=0° , θ=0°	0.16	
		y		0.11	
	White	X	ϕ=0° , θ=0°	0.30	
		y		0.31	

Tolerance : ±0.05

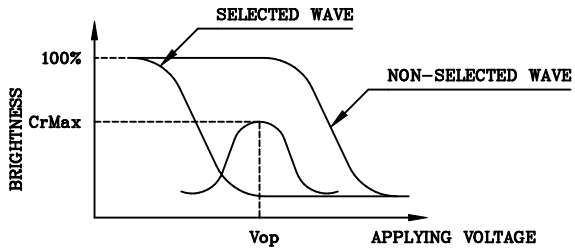
Note※ Measuring at position 3 on Fig.1
CIE chromaticity diagram

Fig.1

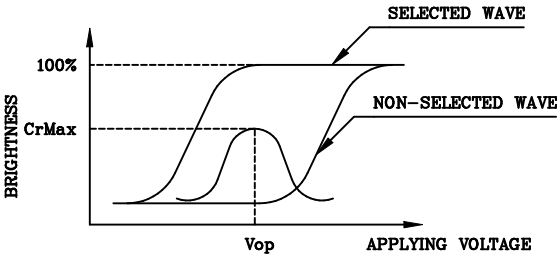


(NOTE 1)

Definition of Operation Voltage(Vop)



(positive type)



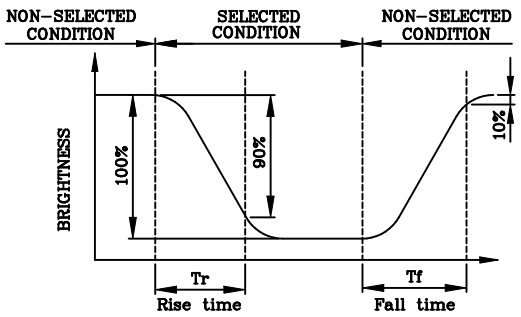
(negative type)

*Conditions

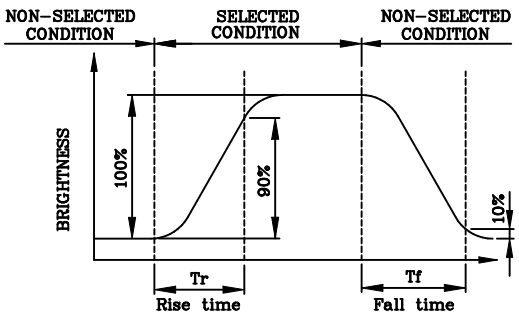
Viewing Angle : 0
Frame Frequency : 120Hz
Applying Waveform : 1/N duty 1/a bias

(NOTE 2)

Definition of Response Time(Tr,Tf)



(positive type)



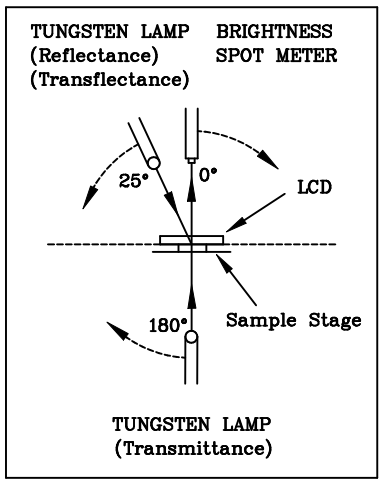
(negative type)

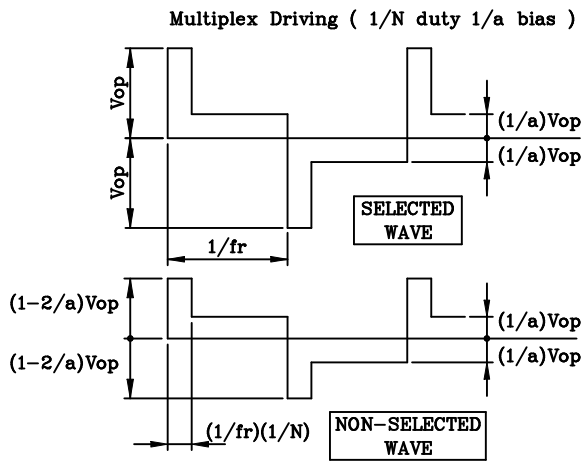
*Conditions

Operating Voltage : Vop
Viewing Angle (θ,ϕ) : (0,0)
Frame Frequency : 120Hz
Applying Waveform : 1/N duty 1/a bias

(NOTE 3)

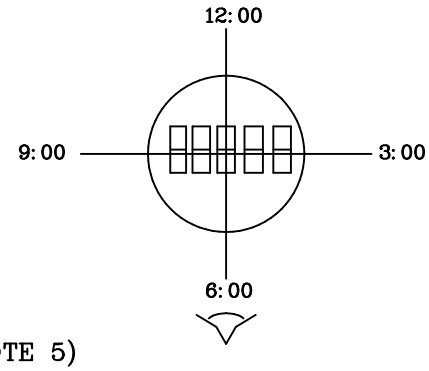
Description of Measuring Equipment and Driving Waveforms





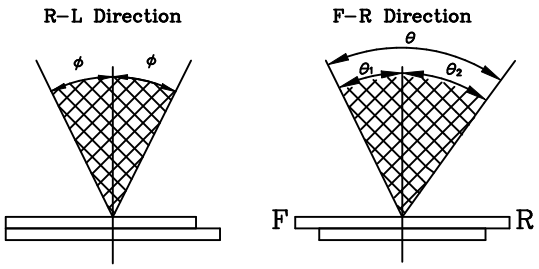
(NOTE 4)

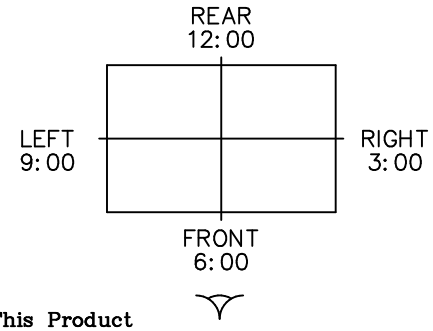
Definition of Viewing Direction



(NOTE 5)

Definition of Viewing Angle





*For This Product

The Viewing Direction Is 6 O'clock

So $\theta_1 > \theta_2$

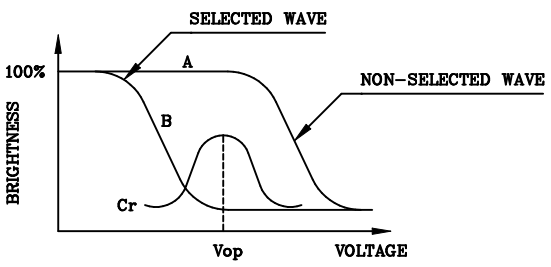
$\theta = \theta_1 + \theta_2$

*Conditions

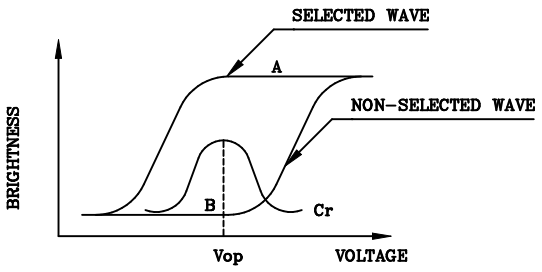
Operating Voltage : Vop
Frame Frequency : 120Hz
Applying Waveform : 1/N duty 1/a bias
Contrast Ratio : larger than 2

(NOTE 6)

Definition of Contrast Ratio (Cr)



(positive type)



(negative type)

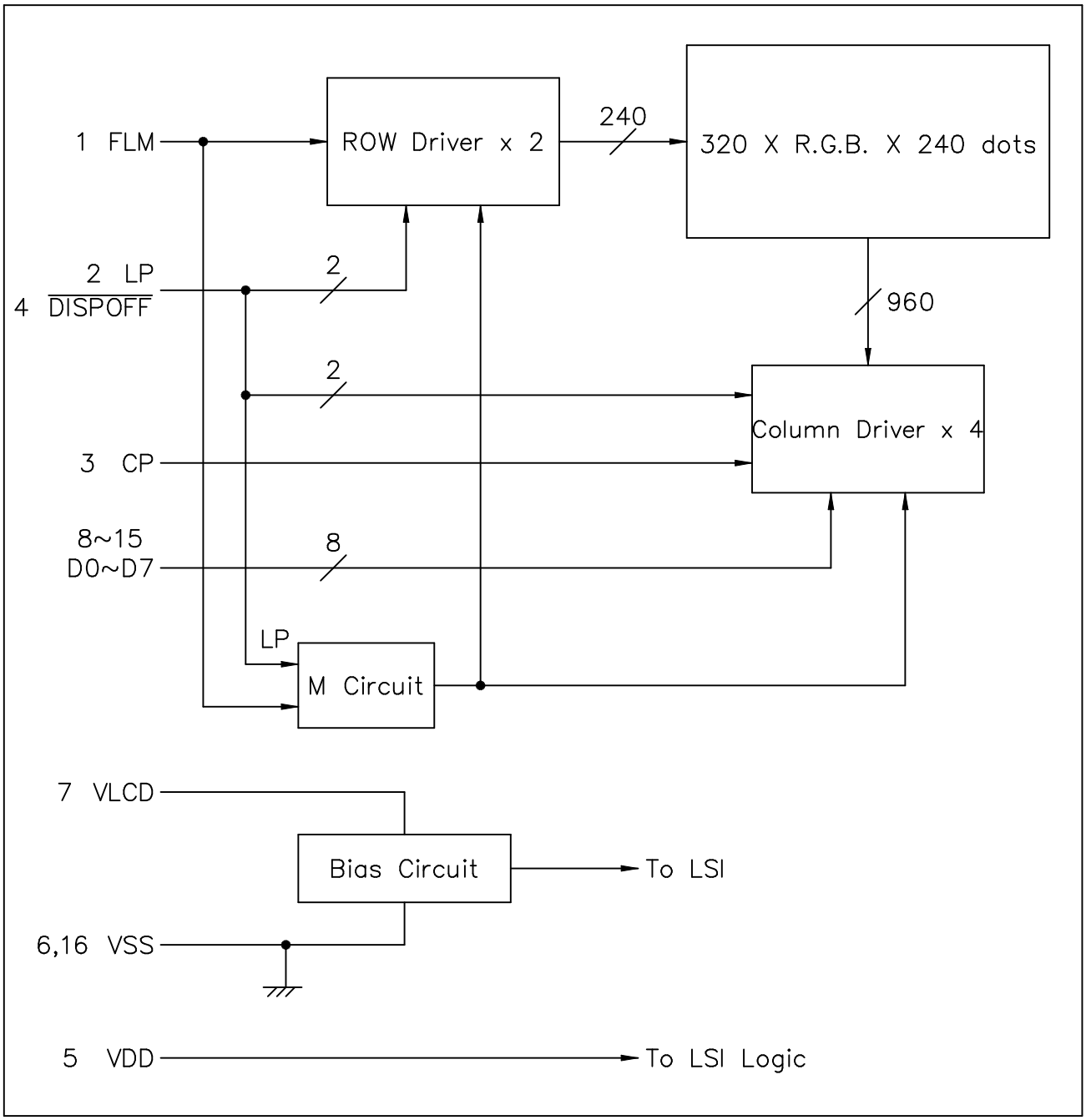
Contrast Ratio : Cr=A/B

*Conditions

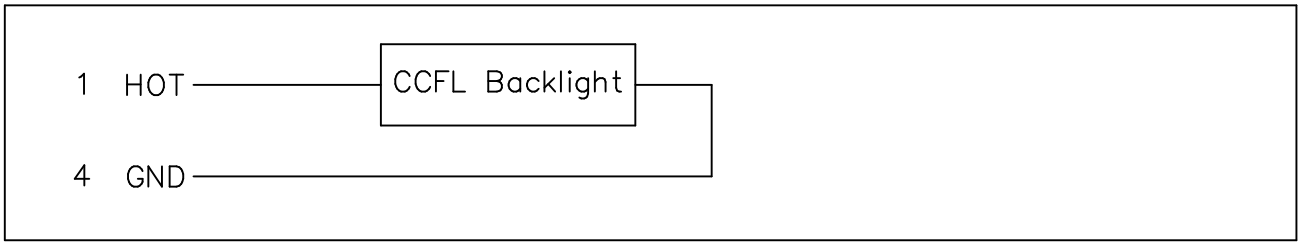
Viewing Angle : 0
Frame Frequency : 120Hz
Applying Waveform : 1/N duty 1/a bias

5. BLOCK DIAGRAM

LCD



CCFL



6. INTERNAL PIN CONNECTION

LCD

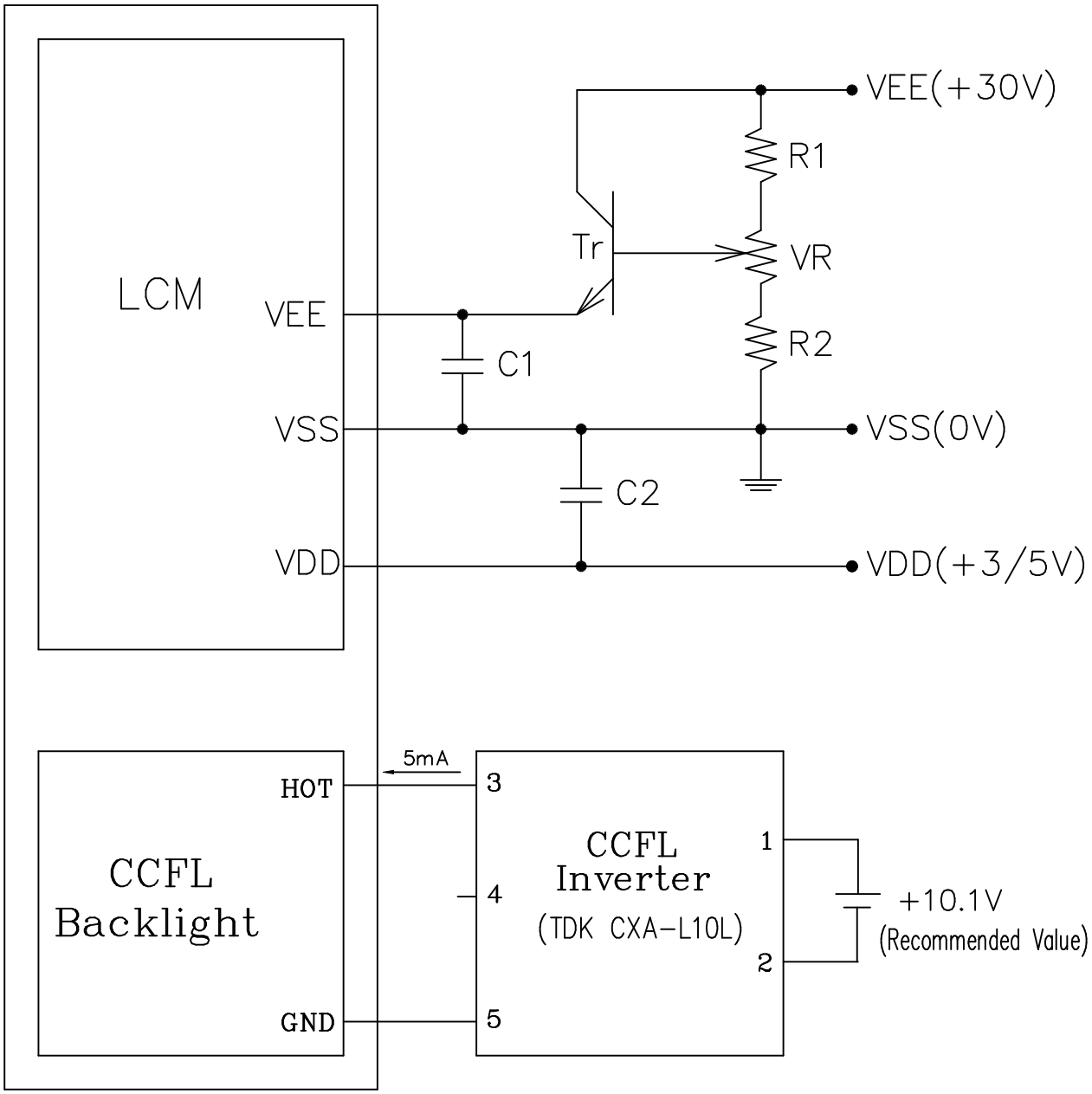
Pin No.	Symbol	Level	Function
1	FLM	H/L	First Line Marker
2	LP	H→L	Data Latch Signal
3	CP	H→L	Clock Signal for Shifting Data
4	$\overline{\text{DISPOFF}}$	H/L	Display Control Signal, H :Display on L :Display off
5	VDD	–	Power Supply for Logic
6	VSS	–	Power Supply (0V,GND)
7	VLCD	–	Power Supply for LCD Drive
8	D0	H/L	Display Data
9	D1	H/L	Display Data
10	D2	H/L	Display Data
11	D3	H/L	Display Data
12	D4	H/L	Display Data
13	D5	H/L	Display Data
14	D6	H/L	Display Data
15	D7	H/L	Display Data
16	VSS	–	Power Supply (0V,GND)

CCFL

Pin No.	Symbol	Level	Function
1	HOT	–	Power Supply for CCFL(HOT)
2	NC	–	Non-connection
3	NC	–	Non-connection
4	GND	–	Power Supply for CCFL(GND)

LCD INTERFACE CABLE :
FFC,N16,Pitch 1.0 mm (Thickness = 0.3 mm)
CORRESPONDABLE LCD CONNECTOR :
MOLEX 52207-1685 or COMPATIBLE
CCFL CONNECTOR :
M63M83-04 (MITSUMI)
CORRESPONDABLE CCFL CONNECTOR :
M60-04-30-134P or M60-04-30-114P or M61M73-04 (MITSUMI)

7. POWER SUPPLY



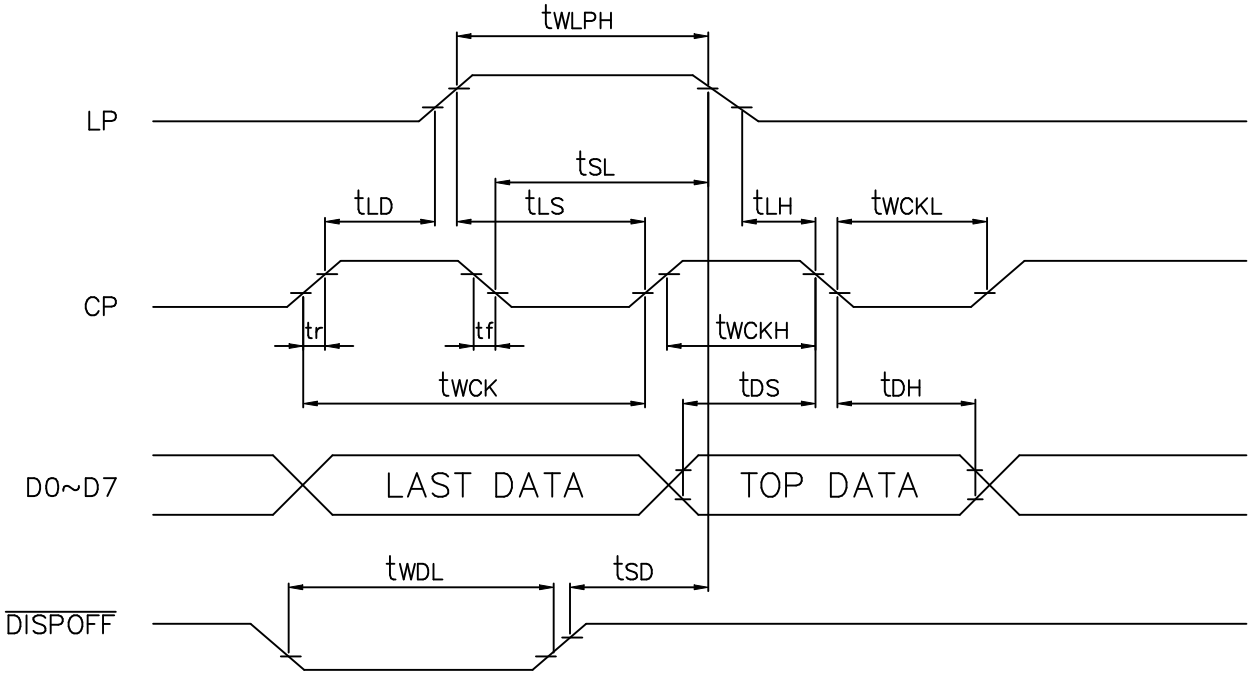
1. R1+R2+VR=10~20KΩ
   C1,C2=10μF

8. TIMING CHARACTERISTICS

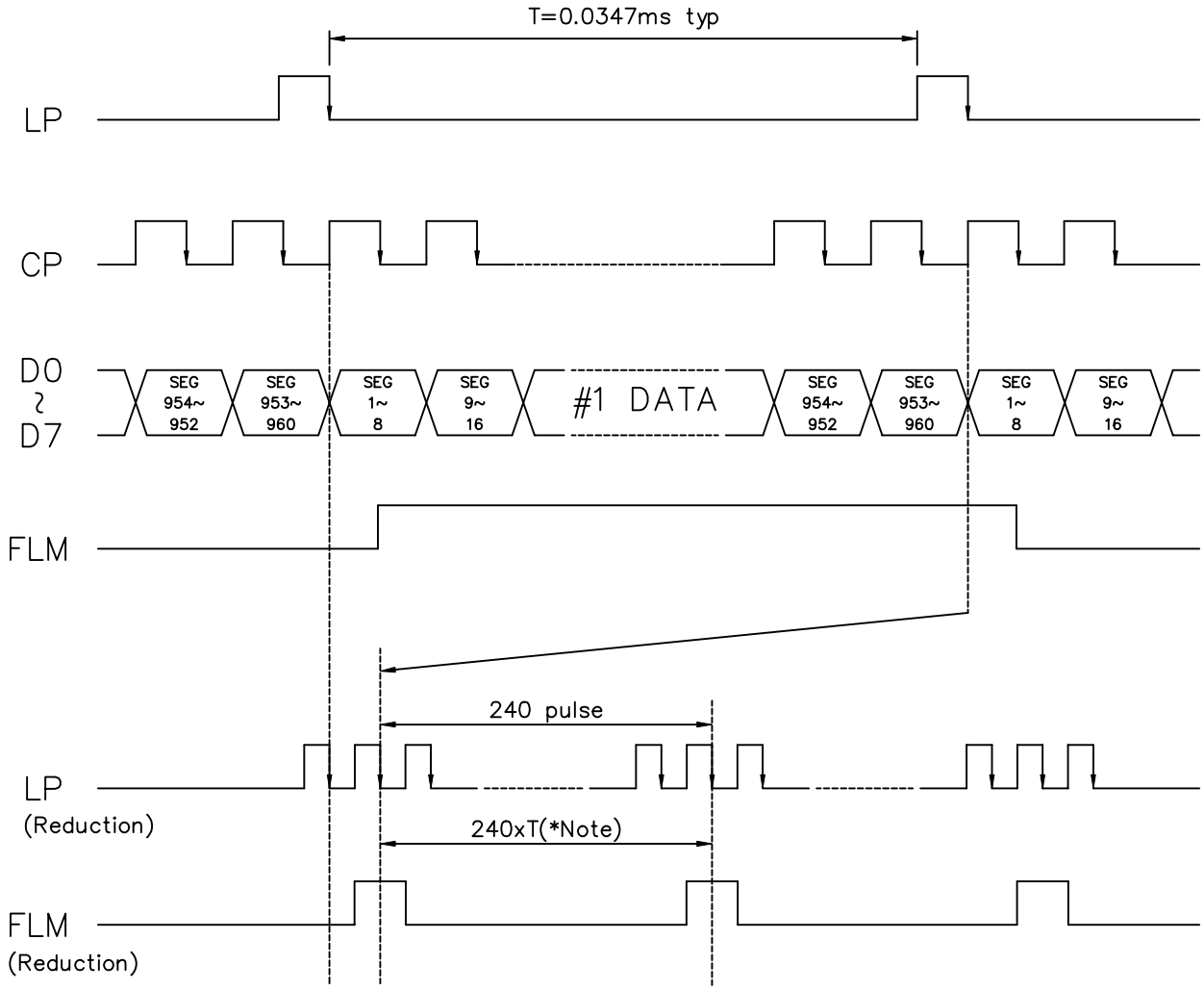
8-1. INTERFACE TIMING

VDD=3.3V ± 10%

Parameter	SYMBOL	MIN.	MAX.	UNIT
CLOCK PULSE CYCLE TIME	t_{WCK}	66	–	ns
CLOCK PULSE HIGH LEVEL WIDTH	t_{WCKH}	23	–	ns
CLOCK PULSE LOW LEVEL WIDTH	t_{WCKL}	23	–	ns
LATCH PULSE HIGH LEVEL WIDTH	t_{WLPH}	30	–	ns
CP→LP RISE TIME	t_{LD}	10	–	ns
CP→LP FALL TIME	t_{SL}	30	–	ns
LP→CP RISE TIME	t_{LS}	30	–	ns
LP→CP FALL TIME	t_{LH}	30	–	ns
CLOCK PULSE RISE/FALL TIME	t_r , t_f	–	30	ns
DATA SETUP TIME	t_{DS}	10	–	ns
DATA HOLD TIME	t_{DH}	25	–	ns
$\overline{\text{DISPOFF}}$ LOW LEVEL WIDTH	t_{WDL}	1.2	–	μs
$\overline{\text{DISPOFF}}$ CANCELLATION TIME	t_{SD}	100	–	ns



8-2. TIMING CHART OF INPUT SIGNAL



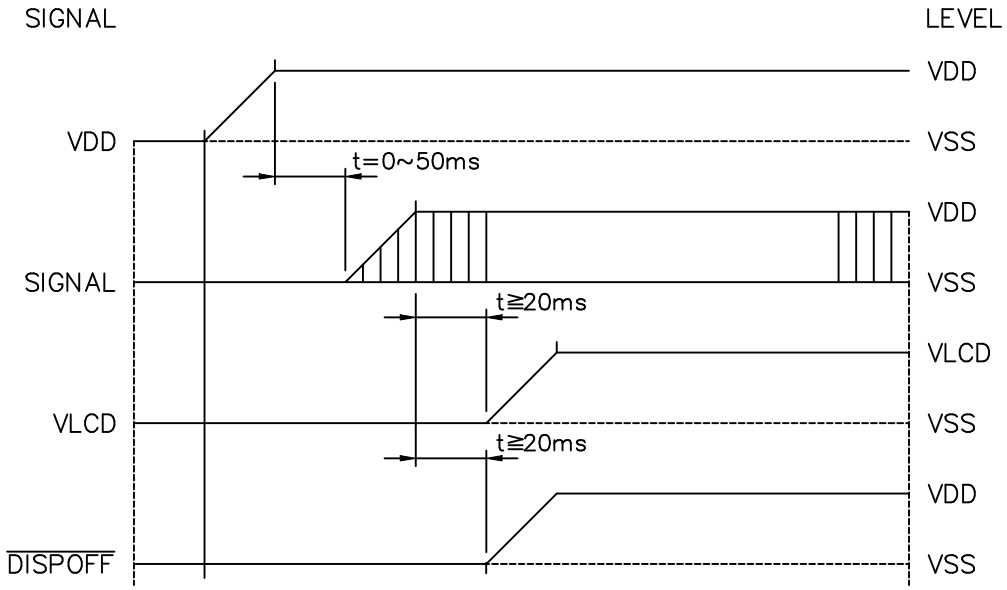
*Note :

If you have problem of display quality , you may modify the LP pulse to improve .

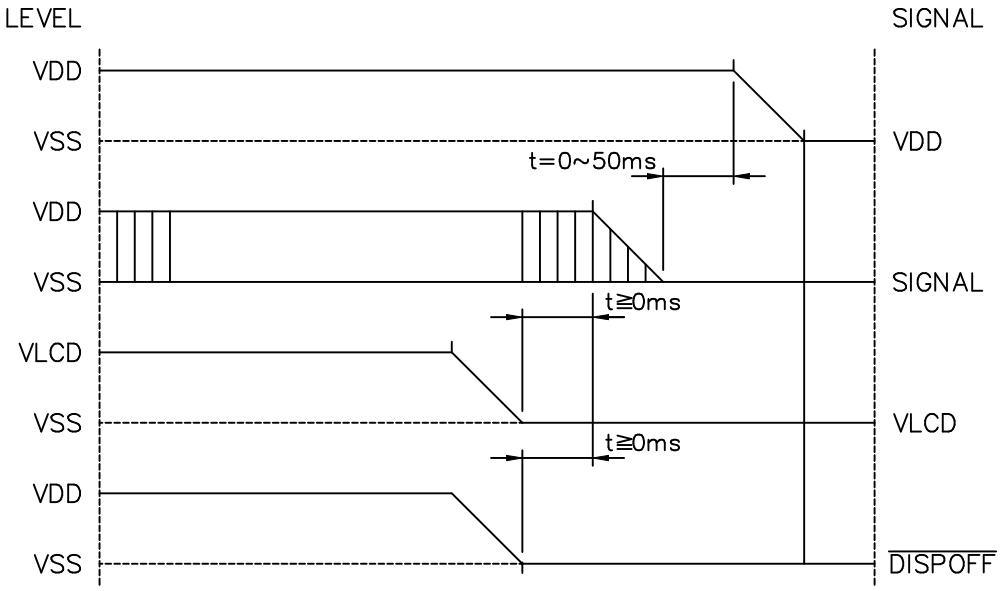
Method : 1.Increase one pulse of LP.
2.Check the display quality.
3.If display quality N.G. , redo iterm 1.
4.If you try many value (ex: 240~260) , you can't get the best quality . The display error may cause by others.

8-3.POWER ON/OFF TIMING

ON SEQUENCE

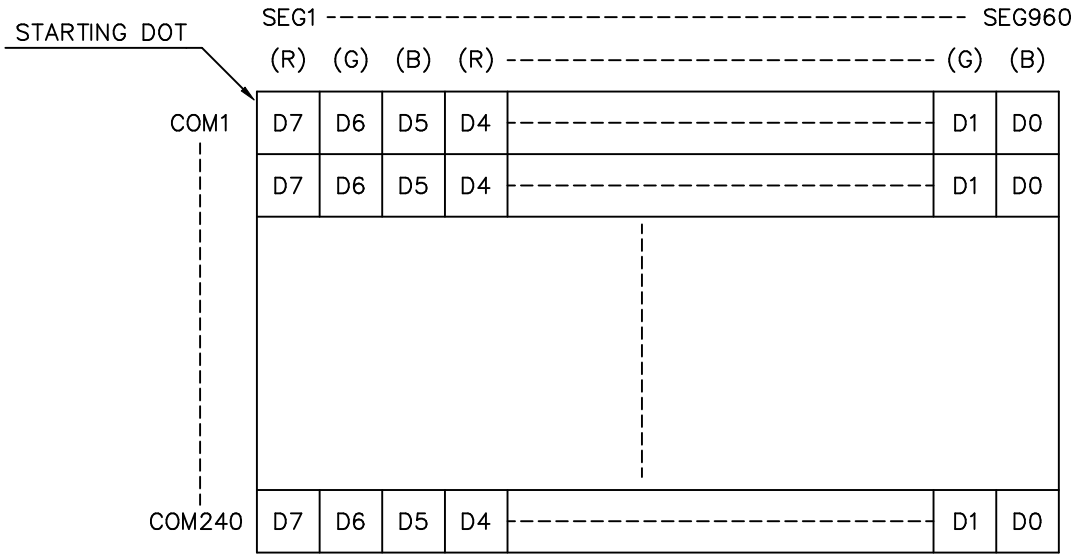


OFF SEQUENCE



Please maintain the above sequence when turning on and off the power supply of the module. If $\overline{\text{DISPOFF}}$ is supplied to the module while internal alternate signal for LCD driving(M) is unstable, DC component will be su-pplied to the LCD panel. This may cause damage the LCD module.

8-4.DISPLAY PATTERN



D0~D7 are 8 bits transmitted data, where D0 is LSB and D7 is MSB.

9. RELIABILITY TEST

WIDE TEMPERATURE RELIABILITY TEST

NO	ITEM	CONDITION			STANDARD	NOTE
1	High Temp. Storage	80°C	120Hrs		Appearance without defect	
2	Low Temp. Storage	−40°C	120Hrs		Appearance without defect	
3	High Temp. & High Humi. Storage	60°C 90%RH	120Hrs		Appearance without defect	
4	High Temp. Operating Display	70°C	120Hrs		Appearance without defect	
5	Low Temp. Operating Display	−20°C	120Hrs		Appearance without defect	
6	Thermal Shock	−20°C,30min → 70°C,30min ↑ (1cycle)			Appearance without defect	10 cycles

Inspection Provision

1.Purpose

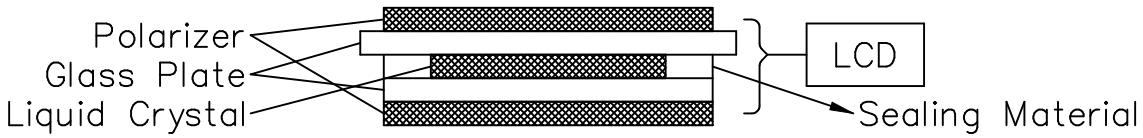
The NAN YA inspection provision provides outgoing inspection provision and its expected quality level based on our outgoing inspection of NAN YA LCD produces.

2.Applicable Scope

The NAN YA inspection provision is applicable to the arrangement in regard to outgoing inspection and quality assurance after outgoing.

3.Technical Terms

3-1 NAN YA Technical Terms



4.Outgoing Inspection

4-1 Inspection Method

MIL-STD-105E Level Ⅱ Regular inspection

4-2 Inspection Standard

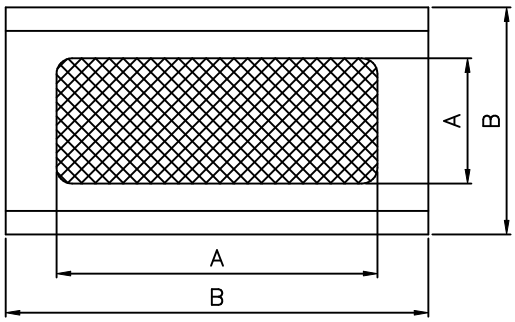
	Item		AQL(%)	Remarks
Major Defect	Dots	Opens Shorts Erroneous operation	0.4	faults which substantially lower the practicality and the initial purpose difficult to achieve.
	Solder appearance	Shorts Loose		
	Cracks	Display surface cracks		

	Dimensions	External from Dimensions	0.4	
Minor Defect	Inside the glass	Black spots	0.65	faults which appear to pose almost no obstacle to the practicality, effective use, and operation.
	Polarizing plate	Scratches, foreign Matter, air bubbles, and peeling		
	Dots	Pinhole, deformation		
	Color tone	Color unevenness		
	Solder appearance	Cold solder Solder projections		

4-3 Inspection Provisions

*Viewing Area Definition

Fig. 1



A : Zone Viewing Area
B : Zone Glass Plate Outline

*Inspection place to be 500 to 1000 lux illuminance uniformly without glaring.
The distance between luminous source(daylight fluorescent lamp and cool white fluorescent lamp) and sample to be 30cm to 50cm.

*Test and measurement are performed under the following conditions, unless otherwise specified.

Temperature	20± 15°C
Humidity	65± 20%R.H.
Pressure	860~1060hPa(mmbar)

In case of doubtful judgment, it is performed under the following conditions.

Temperature	20± 2°C
Humidity	65± 5%R.H.
Pressure	860~1060hPa(mmbar)

5. Specification for quality check

5-1 Electrical characteristics

NO.	Item	Criterion
1	Non operational	Fail
2	Miss operating	Fail
3	Missing dot	Fail
4	Contrast irregular	Fail
5	Response time	Within Specified value
6	Backlight turn on/off	Within Specified value

5-2 External Appearance Defect

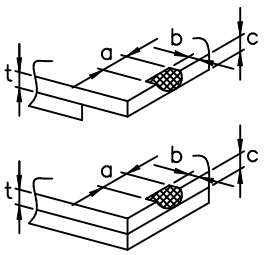
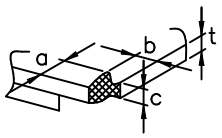
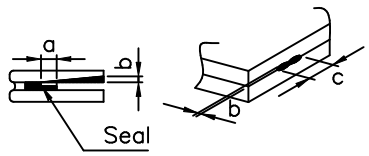
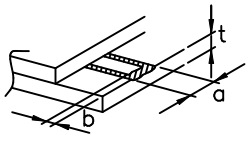
<table>
<tr><th>NO.</th><th>Item</th><th>Criterion</th></tr>
<tr><td>1</td><td>Black spots, foreign matter, and white spots (Including light leakage due to pinholes of polarizing plates, etc.)</td><td>
(1)-1-Spots
<table>
<tr><th>Average Diameter(mm): D</th><th>Number of pieces permitted</th><th>Minimum Space</th></tr>
<tr><td>D≦0.2</td><td>Ignore</td><td>-</td></tr>
<tr><td>0.2<D≦0.3</td><td>5</td><td>10mm</td></tr>
<tr><td>0.3<D≦0.4</td><td>2</td><td>30mm</td></tr>
<tr><td>0.4<D</td><td>0</td><td></td></tr>
</table>
Number of total pieces is set to within 5 pieces.

Note that when there are 2 pieces or more, they are not to be concentrated.
Set as: Average diameter = (Long diameter + Short diameter)/2

(1)-2-Blurred Spots(At lighting condition)
<table>
<tr><th>Average Diameter(mm): D</th><th>Number of pieces permitted</th></tr>
<tr><td>D≦0.3</td><td>Ignore</td></tr>
<tr><td>0.3<D≦0.75</td><td>5</td></tr>
<tr><td>0.75<D</td><td>0</td></tr>
</table>
Number of total pieces is set to within 5 pieces.

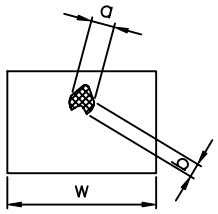
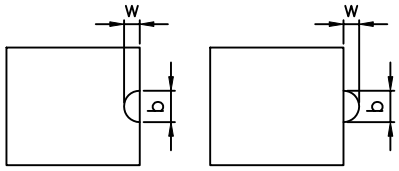
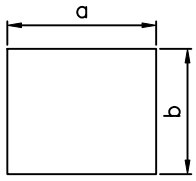
Note that when there are 2 pieces or more, they are not to be concentrated.
Set as: Average diameter = (Long diameter + Short diameter)/2
</td></tr>
</table>

<table>
<tr><td>1</td><td>Line</td><td>(1)-1-Lines
<table><tr><th>Width(mm): W</th><th>Length(mm): L</th><th>Number of pieces permitted</th></tr><tr><td>W≦0.03</td><td>Ignore</td><td>Ignore</td></tr><tr><td>0.03<W≦0.08</td><td>L≦4</td><td>2</td></tr><tr><td>0.08<W≦0.1</td><td>L≦1</td><td>1</td></tr></table>Object exceeding 0.1mm follow the standards of the spots form.
Note that when there are 2 pieces or more, they are not to be concentrated.
(1)-2-Blurred Lines(At lighting condition)
<table><tr><th>Width(mm): W</th><th>Length(mm): L</th><th>Number of pieces permitted</th></tr><tr><td>W≦0.03</td><td>Ignore</td><td>Ignore</td></tr><tr><td>0.03<W≦0.08</td><td>L≦3</td><td>6</td></tr><tr><td>0.08<W</td><td>3<L</td><td>None</td></tr></table>Object exceeding 0.1mm follow the standards of the spots form.
Note that when there are 2 pieces or more, they are not to be concentrated.</td></tr>
<tr><td>2</td><td>Scratches(Glass, reflection plates, and polarizing plates)</td><td>In accordance with black spots.
(At non lighting condition)</td></tr>
<tr><td>3</td><td>Color irregular</td><td>Not remarkable color irregular.</td></tr>
</table>

<table>
<tr><td>4</td><td>Air bubbles polarizing plates, and reflection plates</td><td colspan="2">
<table>
<tr><th>Average Diameter (mm): D</th><th>Number of pieces permitted</th></tr>
<tr><td>D≦0.3</td><td>Ignore</td></tr>
<tr><td>0.3<D</td><td>0</td></tr>
</table>
Average diameter = (Long diameter + Short diameter)/2
Note that when there are 4 pieces or more, they are not to be concentrated.
</td></tr>
<tr><td rowspan="5">5</td><td rowspan="5">Cracks</td><td>(1)General crack
 
</td><td>a≦5
b≦2
c≦t
Where, a and b are ignored when less than or equal to 0.5 The numbers of pieces are set at up to 5 pieces.</td></tr>
<tr><td>(2)Corner crack
 
</td><td>a≦2.5
b≦2.5
c≦t
a+b≦4</td></tr>
<tr><td>(3)Seal portion crack
 
</td><td>a≦The seal widthx1/3
b≦tx2/3
c≦5
The numbers of pieces are set at up to 5 pieces.</td></tr>
<tr><td>(4)ITO Pin crack
 
</td><td>a≦5
b≦1/3 pin length
c≦t</td></tr>
<tr><td>(5)Progressive cracks</td><td>All taken to be unacceptable.</td></tr>
</table>

6	Outer dimensions	Should be within the tolerance.
7	Newton ring(touch panel)	Orbicular of interference fringes is not allowed in the optimum contrast within the active area under viewing angle.
8	Soldering	Should be no defective soldering such as shorting, loose terminal cold solder, peeling of printed circuit board pattern, improper mounting position, etc.

5-3 Dot Appearance Defect

NO.	Item	Criteria
1	Pinhole	 Dot display a and b are each ≦0.2mm The overall total is taken be with in 10 units. Note that they are not to be concentrated.
2	Missing	 Dot display a and b are each ≦0.2mm The overall total is taken to be with in 10 units.
3	Thick and thin display	 Taken to be within ±1.5% of display character width(a) and height(b).

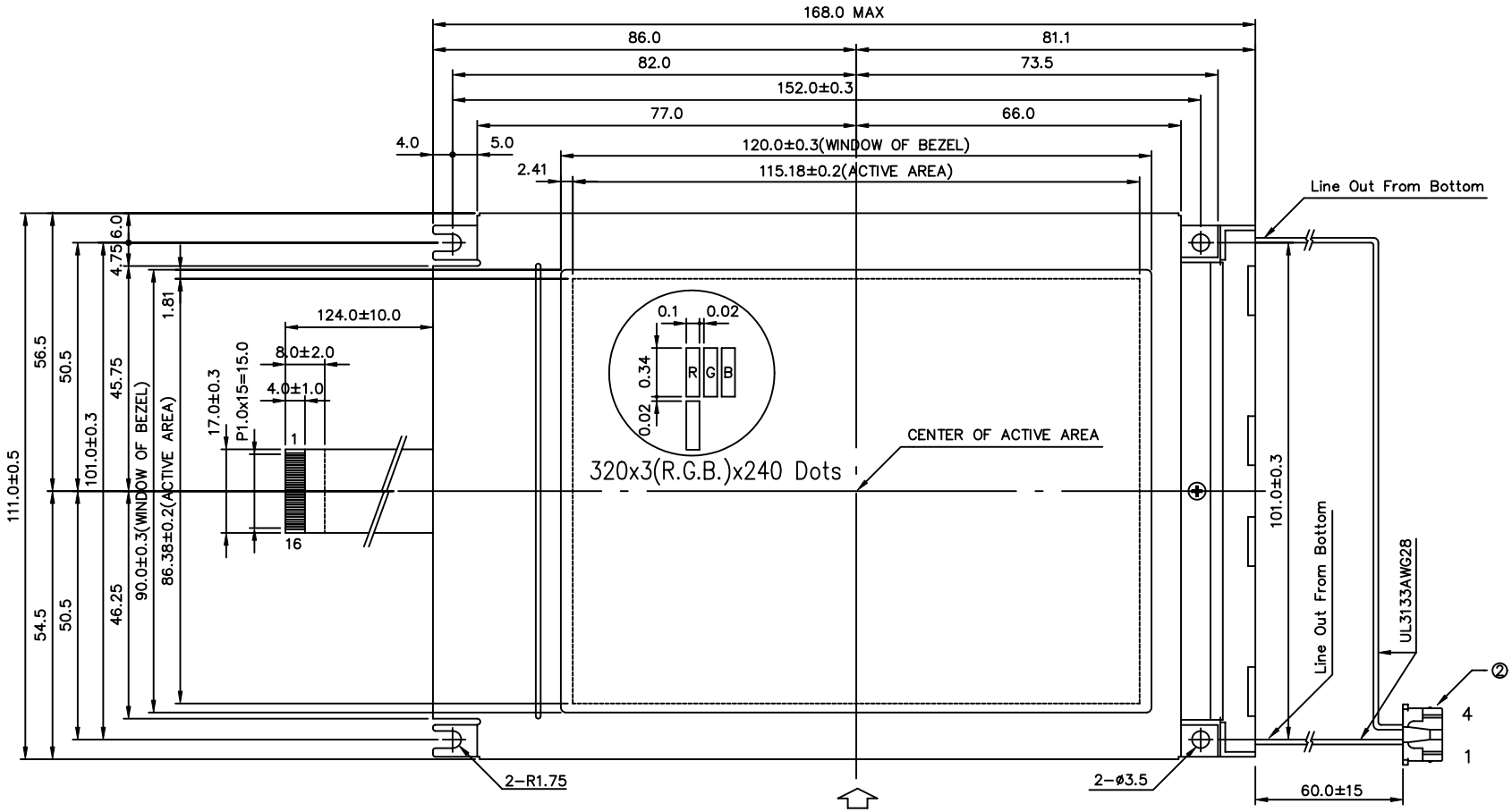
NOTICE:

- SAFETY
  1. If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
  2. If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

- HANDLING
  1. Avoid static electricity which can damage the CMOS LSI.
  2. Do not remove the panel or frame from the module.
  3. The polarizing plate of the display is very fragile. So, please handle it very carefully.
  4. Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
  5. Do not use ketonics solvent & Aromatic solvent. Use a soft cloth soaked with a cleaning naphtha solvent.

- STORAGE
  1. Store the panel or module in a dark place where the temperature is 25°C±5°C and the humidity is below 65% RH.
  2. Do not place the module near organics solvents or corrosive gases.
  3. Do not crush, shake, or jolt the module.

- TERMS OF WARRANT
  1. Acceptance inspection period
     The period is within one month after the arrival of contracted commodity at the buyer's factory site.
  2. Applicable warrant period
     The period is within twelve months since the date of shipping out under normal using and storage conditions.



PIN ASSIGNMENT OF I/O CONNECTION

Pin No.	SYMBOL	LEVEL	FUNCTION
1	FLM	H/L	First Line Marker
2	LP	H→L	Data Latch Signal
3	CP	H→L	Data Shift Clock Signal
4	$\overline{DISPOFF}$	H/L	H : Display On L : Display Off
5	V_{DD}	–	Power Supply for Logic
6	Vss	–	Power Supply(0V,GND)
7	V_{LCD}	–	Power Supply for LCD Drive
8	D0	H/L	Display Data
9	D1	H/L	Display Data
10	D2	H/L	Display Data
11	D3	H/L	Display Data
12	D4	H/L	Display Data
13	D5	H/L	Display Data
14	D6	H/L	Display Data
15	D7	H/L	Display Data
16	Vss	–	Power Supply(0V,GND)

PIN ASSIGNMENT OF CCFL CONNECTION

Pin No.	SYMBOL	LEVEL	FUNCTION
1	HOT	–	Power Supply for CCFL(HOT)
2	NC	–	No Connection
3	NC	–	No Connection
4	GND	–	Power Supply for CCFL(GND)

NOTE :

1. RESOLUTION : 320 X 3(R.G.B.) X 240 DOTS
2. CONTROLLER : EXCLUDED
3. DC/DC CONVERTER : EXCLUDED
4. ① INTERFACE CONNECTOR
   FFC, N16 P1.0mm
   ② CCFT CONNECTOR
   M63M83-04 (MITSUMI)
5. TOLERANCE NO SPECIFIED : ±0.5mm

REV. NO.	DESCRIPTION	DATE	DESIGN	CHECK	APPROVE
5					
4					
3					
2					
1					

南亞塑膠工業股份有限公司
NAN YA PLASTICS CORPORATION

製 品 圖

LCBHBTB61MS

	NAME	DATE	THIRD ANGLE P.
APPROVE			
CHECK			
DESIGN	Campos Chen	95.02.21	SCALE: 1/1
DRAWN	Campos Chen	95.02.21	UNIT: mm

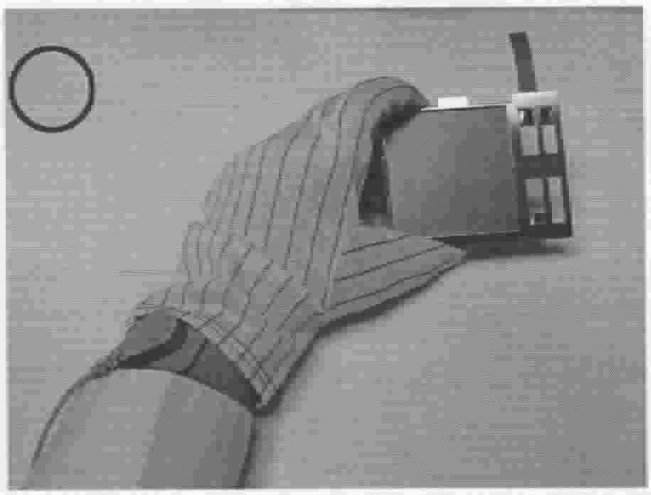
DWG NO. MB61DD0A (1/1) (0)

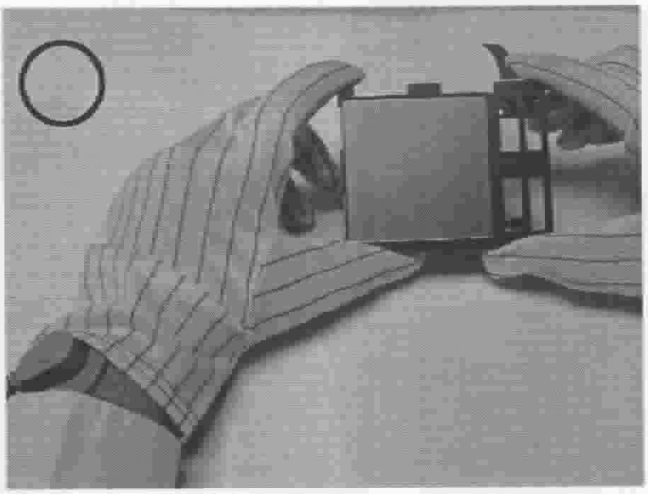
THE NOTES OF LCM USING

LCM is easy to damage.

Please follow the notes as bellows, and be careful of handling!

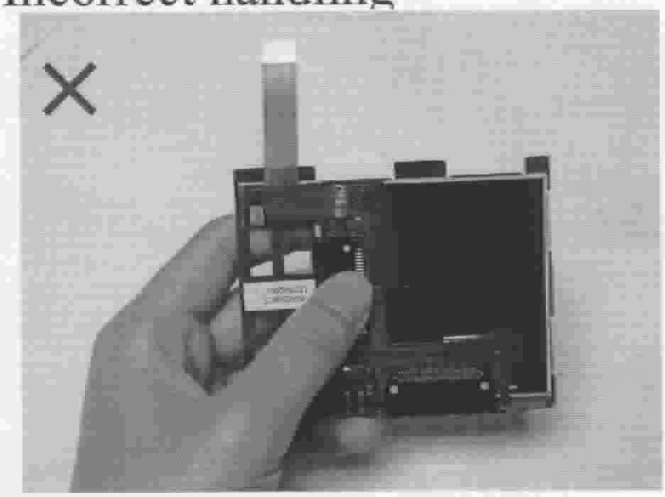
Correct handling



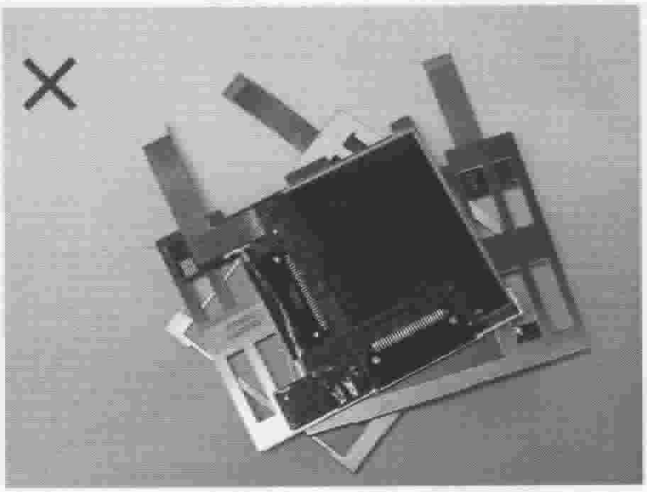


As above picture, please handle with glove by LCM edges and full EOS/ESD protection.

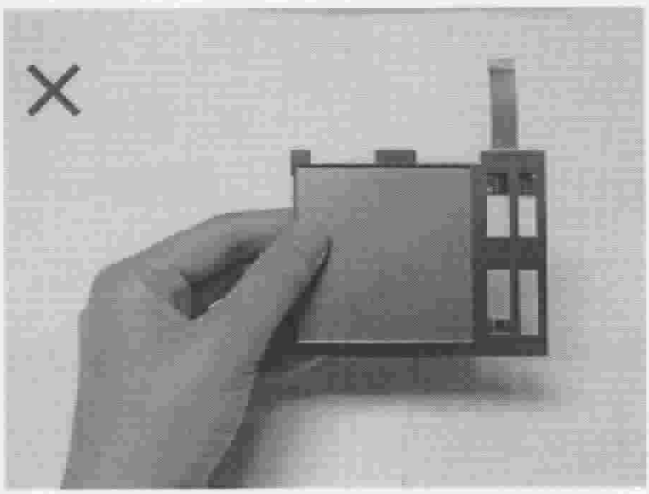
Incorrect handling



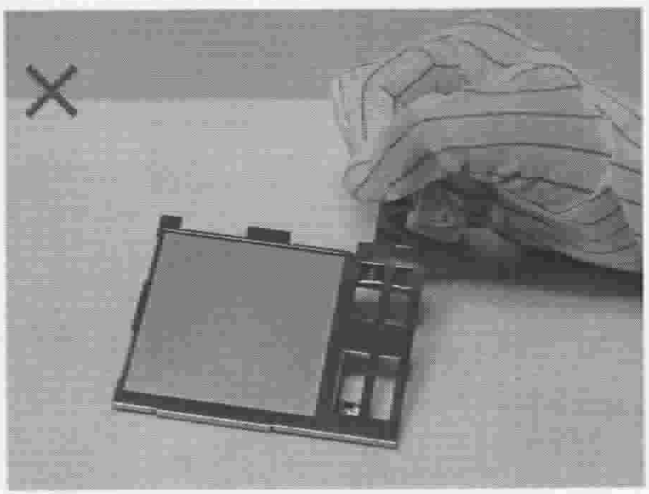
Please don't touch IC directly.



Please don't put one on another LCM.



Please don't hold the surface of LCM.

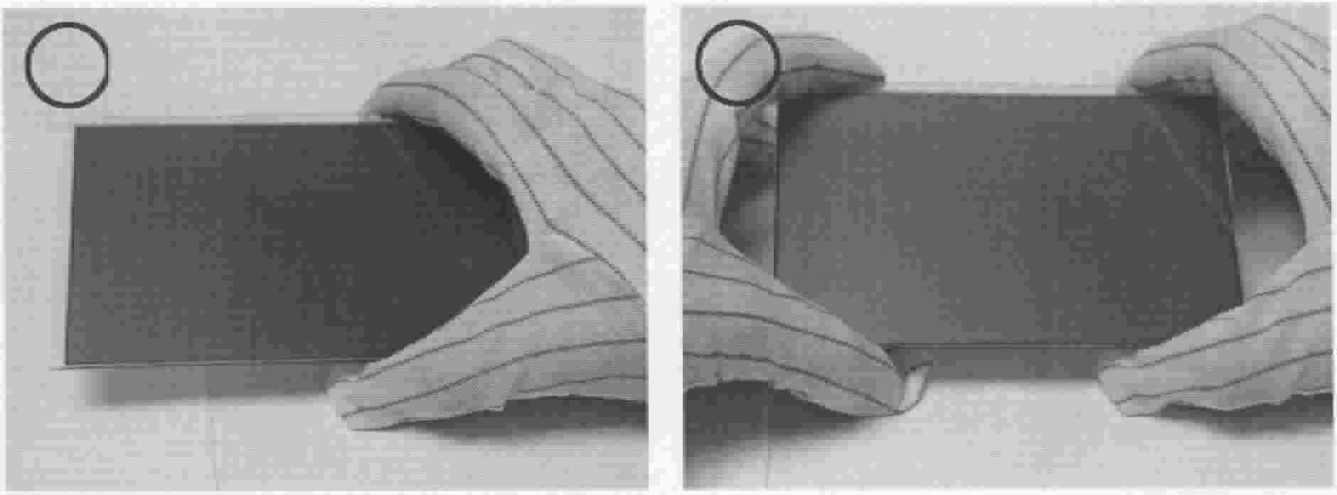


Please don't stretch interface of output.

THE NOTES OF LCD USING

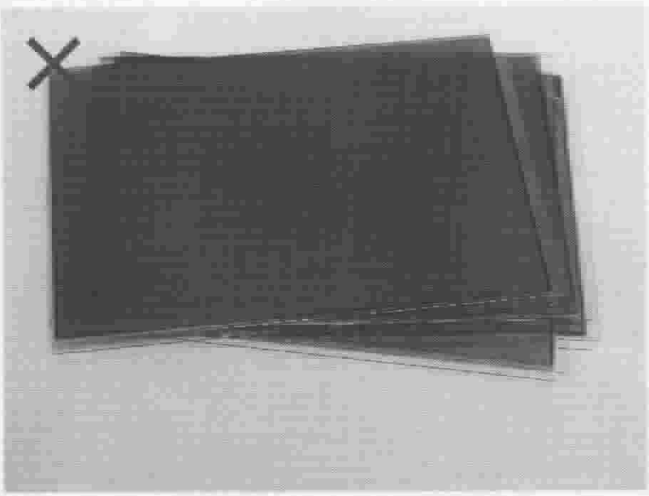
LCD is easy damage.
Please follow notes as bellows, and be careful of handling!

Correct handling

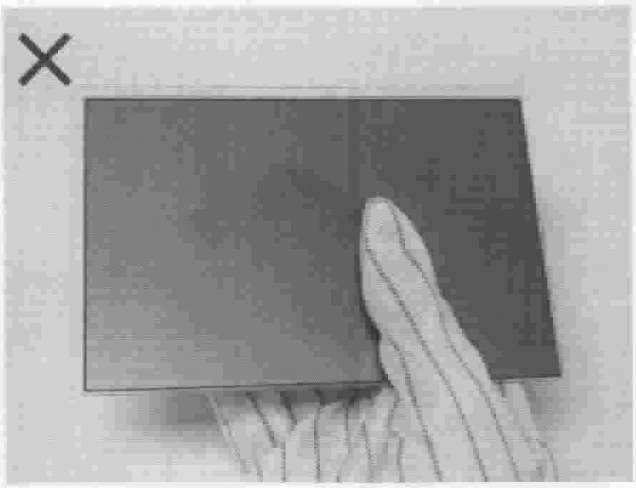


As above picture, please handle with glove by LCD edges and full EOS/ESD protection.

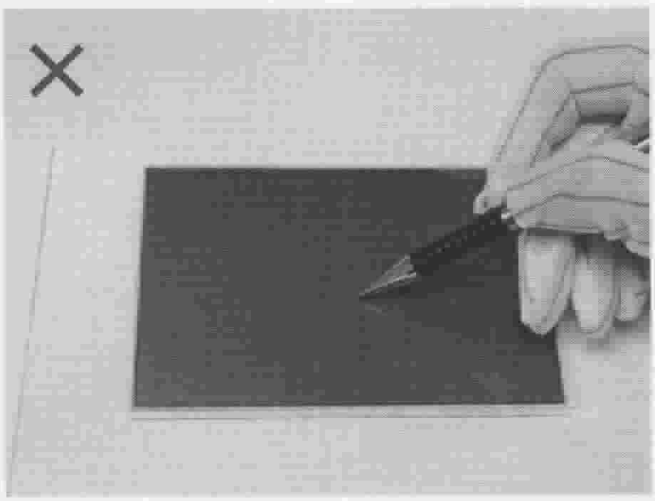
Incorrect handling



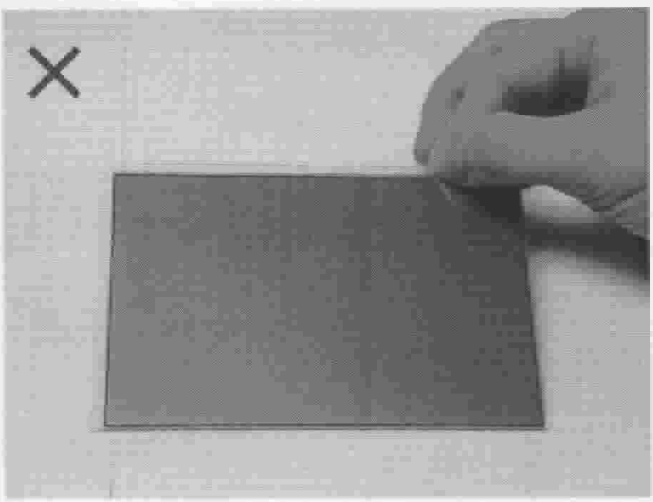
Please don't put one on another LCD.



Please don't hold the surface of LCD.



Please don't operate with sharp stick such as sharp pencil.



Please don't touch ITO glass without anti-static gloves.