



Product Specification

SPECIFICATION FOR APPROVAL

() Preliminary	Specification
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() Final Specification

Title	47.0" WUXGA TFT LCD
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BUYER	Gleichmann
MODEL	

SUPPLIER	LG DISPLAY Co., Ltd.
MODEL	LD470WUB
SUFFIX	SCC1(RoHS Verified)

APPROVED BY	SIGNATURE DATE
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Please return 1 copy for your	confirmation with
your signature and co	mments.

APPROVED BY	SIGNATURE DATE
/ Chief Senior Engineer	
REVIEWED BY	
/ Chief Senior Engineer	
PREPARED BY	
/ Senior Engineer	
PD Product Design LG Display Co., L	•

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RECORD OF REVISIONS

Revision No.	Revision Date	Page	Description		
0.0	Feb. 23. 2012	-	Preliminary Specification		
0.1	May. 30. 2012	5	Updated absolute maximum ratings with notes		
0.2	July. 05. 2012	26, 30, 31	Updated Pallet size, Packing Method, LCM Label format.		

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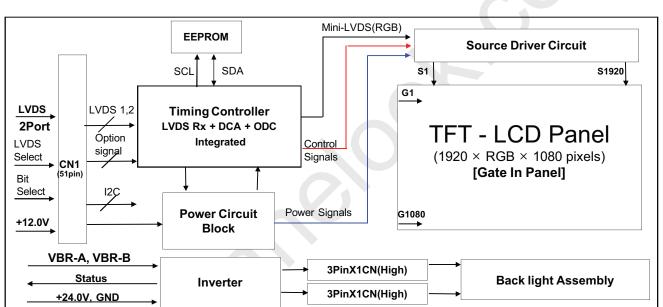
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1. General Description

The LD470WUB is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp(CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 46.96 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 10-bit gray scale signal for each dot. Therefore, it can present a palette of more than 1.07 Billion colors.

It has been designed to apply the 10-bit 2-port LVDS interface.

It is intended to support Public Display where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

<u>General i catules</u>	
Active Screen Size	46.96 inch (1192.87mm) diagonal
Outline Dimension	1068.0(H) x 613.2.0 (V) x 54.7 mm(D) (Typ.)
Pixel Pitch	0.5415 mm x 0.5415 mm
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Color Depth	10Bit (D), 1.07 Billion colors
Luminance, White	1000 cd/m ² (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Min.), U/D 178 (Min.))
Power Consumption	Total 219.84W (Typ.) [Logic=6.84 W, Backlight=213W(V _{BR-A} =1.65V)]
Weight	14.5 Kg (Typ.)
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer (Haze 10%)
Possible Display Type	Landscape and Portrait Enabled

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2. Absolute Maximum Ratings

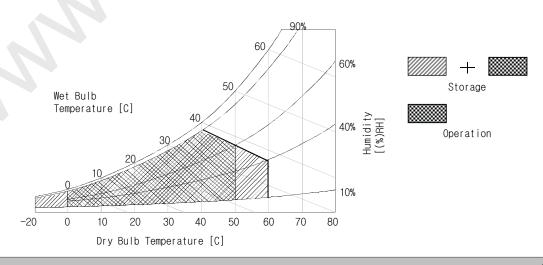
The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Table 1. Absolute MAXIMOM NATINGS								
) a va va a ta v	Curahal	Val	ue	1.1:4	Damasila		
Parameter		Symbol	Min	Max	Unit	Remark		
Power	LCM	VLCD	-0.3	+14.0	VDC	at 25 ± 2 °C		
Input Voltage	Backlight inverter	VBL	-0.3	+27.0	VDC			
ON/OFF Control Voltage		VON/OFF	-0.3	+5. 5	VDC			
Brightness Control Voltage		VBR	0	+5.0	VDC			
Operating T	Temperature	Тор	0	+50	°C			
Storage Te	Storage Temperature		-20	+60	°C			
Front Panel Temperature		Tsur	-	+68	°C	Note		
Operating Ambient Humidity		Нор	10	90	%RH			
Storage Hu	midity	Нsт	10	90	%RH			

Note 1. Ambient temperature condition (Ta = 25 ± 2 °C)

- 2. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be Max 39°C, and no condensation of water.
- 3. Gravity mura can be guaranteed below 50°C condition.
- 4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68°C. The range of operating temperature may degraded in case of improper thermal management in final product design.



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3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power for the LCD circuit. The other Is used for the CCFL backlight and inverter circuit.

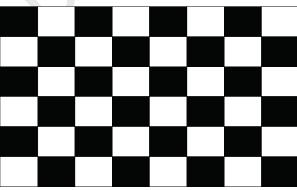
Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol		Value	Unit	Note		
r drameter	Cymbol	Min Typ		Max		1,1310	
Circuit :							
Power Input Voltage	V_{LCD}	11.4	12.0	12.6	V_{DC}		
Dower Input Current		-	570	740	mA	1	
Power Input Current	I _{LCD}	-	920	1196	mA	2	
Power Consumption	P _{LCD}	-	6.84	8.88	Watt	1	
Rush current	I _{RUSH}	-	-	5.0	Α	3	

Notes: 1. The specified current and power consumption are under the V_{LCD} =12.0V, 25 ± 2 °C, f_V =60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.

- 2. The current is specified at maximum current pattern.
- 3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).

White: 1023Gray Black: 0Gray



Mosaic Pattern(8 x 6)

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Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Parameter			Symbol		Values			Notes
Fai	Syllibol	Min	Тур	Max	Unit	Notes		
Inverter:	Inverter:							
Power Supply Inp	out Voltage		VBL	22.8	24.0	25.2	Vdc	1
Power Supply Inp	out Voltage	Ripple		-	-	0.5	Vp-p	1
	After Aging		IBL_A	-	8.9	10.3	Α	V _{BR-A} = 1.65V 1
Power Supply			IDL_A	-	10.0	11.0	Α	V _{BR-A} = 3.3V 1
Input Current	Defens Asian	IDI D	-	11.0	12.0	A	V _{BR-A} = 1.65V 2	
	Before Aging		IBL_B	-	12.0	13.0	Α	V _{BR-A} = 3.3V 2
Power Supply Inp	Power Supply Input Current (In-Rush)			-		14.0	А	V _{BL} = 22.8V V _{BR-B} = 3.3V V _{BR-A} = 1.65V
Power Consumpt	ion		PBL	-	213	250	W	V _{BR-A} = 1.65V 1
	Brightness	Adjust	V _{BR-A}	0.0	1.65	3.3	Vdc	
Input Voltage for Control System	On/Off	On	V on	2.5	-	5.0	Vdc	
Signals	On/On	Off	V off	-0.3	0.0	0.8	Vdc	
	Brightness Adjust		V _{BR-B}	0	-	3.3	V	
Lamp:								
Discharge Stabili	Discharge Stabilization Time					3	min	3
Life Time				50,000			Hrs	4

Notes:

- 1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 120 minutes at 25±2°C. The specified current and power consumption are under the typical supply Input voltage 24Vand VBR (VBR-A: 1.65V & VBR-B: 3.3V), it is total power consumption.
 - The ripple voltage of the power supply input voltage is under 0.5 Vp-p. LPL recommend Input Voltage is $24.0V \pm 5\%$.
- 2. Electrical characteristics are determined within 30 minutes at $25\pm2^{\circ}$ C. The specified currents are under the typical supply Input voltage 24V.
- 3. The brightness of the lamp after lighted for 5minutes is defined as 100%.

 TS is the time required for the brightness of the center of the lamp to be not less than 95% at typical current.
- The screen of LCD module may be partially dark by the time the brightness of lamp is stable after turn on.
- 4. Specified Values are for a single lamp which is aligned horizontally.

 The life time is determined as the time which luminance of the lamp is 50% compared to that of initial value at the typical lamp current (VBR-A: 1.65V & VBR-B:3.3V), on condition of continuous operating at 25± 2°C
- 5. The duration of rush current is about 10ms.

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3-2. Interface Connections

This LCD module employs two kinds of interface connection, a 51-pin connector is used for the module electronics and Master 14-pin and Slave 12-pin connectors are used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector(CN1): FI-RE51S-HF or Equivalent, Refer to below table.

- Mating Connector : FI-RE51HL

<u> </u>	ie 4. MODULI	E CONNECTOR(CN1) PIN CONF	IGUR	ATION	
No	Symbol	Description	No	Symbol	Description
1	GND	Ground	27	Bit Select	'H' or NC= 10bit(D) , 'L' = 8bit
2	NC	No Connection	28	RE0N	SECOND LVDS Receiver Signal (A-)
3	NC	No Connection	29	RE0P	SECOND LVDS Receiver Signal (A+)
4	NC	No Connection (Reserved for LGD)	30	RE1N	SECOND LVDS Receiver Signal (B-)
5	NC	No Connection (Reserved for LGD)	31	RE1P	SECOND LVDS Receiver Signal (B+)
6	NC	No Connection (Reserved for LGD)	32	RE2N	SECOND LVDS Receiver Signal (C-)
7	LVDS Select	'H' =JEIDA , 'L' or NC = VESA	33	RE2P	SECOND LVDS Receiver Signal (C+)
8	NC	No Connection	34	GND	Ground
9	NC	No Connection	35	RECLKN	SECOND LVDS Receiver Clock Signal(-)
10	NC	No Connection	36	RECLKP	SECOND LVDS Receiver Clock Signal(+)
11	GND	Ground	37	GND	Ground
12	RO0N	FIRST LVDS Receiver Signal (A-)	38	RE3N	SECOND LVDS Receiver Signal (D-)
13	RO0P	FIRST LVDS Receiver Signal (A+)	39	RE3P	SECOND LVDS Receiver Signal (D+)
14	RO1N	FIRST LVDS Receiver Signal (B-)	40	RE4N	SECOND LVDS Receiver Signal (E-)
15	RO1P	FIRST LVDS Receiver Signal (B+)	41	RE4P	SECOND LVDS Receiver Signal (E+)
16	RO2N	FIRST LVDS Receiver Signal (C-)	42	NC	No Connection
17	RO2P	FIRST LVDS Receiver Signal (C+)	43	NC	No Connection
18	GND	Ground	44	NC	No Connection
19	ROCLKN	FIRST LVDS Receiver Clock Signal(-)	45	GND	Ground
20	ROCLKP	FIRST LVDS Receiver Clock Signal(+)	46	GND	Ground
21	GND	Ground	47	AGP	'H'=AGP, 'L or NC' = NSB
22	RO3N	FIRST LVDS Receiver Signal (D-)	48	VLCD	Power Supply +12.0V
23	RO3P	FIRST LVDS Receiver Signal (D+)	49	VLCD	Power Supply +12.0V
24	RO4N	FIRST LVDS Receiver Signal (E-)	50	VLCD	Power Supply +12.0V
25	RO4P	FIRST LVDS Receiver Signal (E+)	51	VLCD	Power Supply +12.0V
26	NC	No Connection	-	-	-

- 1. All GND(ground) pins should be connected together to the LCD module's metal frame.
- 2. All VLCD (power input) pins should be connected together.
- 3. All Input levels of LVDS signals are based on the EIA 644 Standard.
- 4. Specific pins(pin No. #2~#6) are used for internal data process of the LCD module. These pins should be no connection.
- 5. LVDS pin (pin No. #24,25,40,41) are used for 10Bit(D) of the LCD module. If used for 8Bit(R), these pins are no connection.
- 6. Specific pin No. #47 is used for "No signal detection" of system signal interface. It should be GND or NC for NSB(No Signal Black) during the system interface signal is not. If this pin is "H", LCD Module displays AGP(Auto Generation Pattern).

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3-2-2. Backlight Inverter

Master

-Inverter Connector : S14B-PH-SMC (JST)

or Equivalent

- Mating Connector : PHR-14 or Equivalent

Slave

-Inverter Connector: S12B-PH-SMC (JST)

or Equivalent

-Mating Connector: PHR-12 or Equivalent

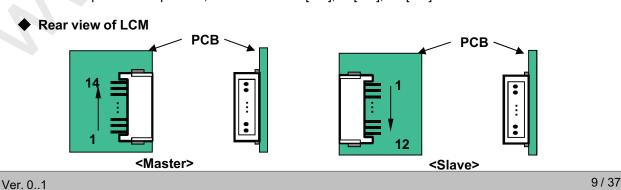
Table 5. INVERTER CONNECTOR PIN CONFIGULATION

i able 5. Ir	VERTERCO	NNECTOR PIN CONFIGULATION	ON		
Pin No	Symbol	Description	Master	Slave	Note
1	VBL	Power Supply +24.0V	VBL	VBL	
2	VBL	Power Supply +24.0V	VBL	VBL	
3	VBL	Power Supply +24.0V	VBL	VBL	
4	VBL	Power Supply +24.0V	VBL	VBL	
5	VBL	Power Supply +24.0V	VBL	VBL	
6	GND	Backlight Ground	GND	GND	
7	GND	Backlight Ground	GND	GND	
8	GND	Backlight Ground	GND	GND	1
9	GND	Backlight Ground	GND	GND	
10	GND	Backlight Ground	GND	GND	
11	VBR-A	Analog dimming voltage DC 0.0V ~ 3.3V (Typ : 1.65V)	VBR-A	Don't care	2, 3
12	Von/off	0.0V ~ 5.0V	On/Off	Don't care	
13	VBR-B	Burst dimming voltage DC 0.0V ~ 3.3V	VBR-B	-	3
14	Status	Normal : Upper 3.0V Abnormal : Under 0.7V	Status	-	4

Notes: 1. GND should be connected to the LCD module's metal frame.

- 2. If Pin #11 is open, V_{BR-A} = 1.65V. When apply over 1.65V(~ 3.3V) continuously, its luminance is increasing however lamp's life time is decreasing.

 It could be usable for boost up luminance when using DCR (=Dynamic contrast ratio) function only.
- 3. Minimum Brightness: VBR-B = 0V Maximum Brightness: VBR-B = 3.3V
- 4. Even though Pin #14 is open, there is no effect on inverter operating, The output terminal of inverter.
- 5. Each impedance of pin #11,12 and 13 is $140[K\Omega]$, $41[K\Omega]$, $125[K\Omega]$





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3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

Table 6-1. TIMING TABLE for NTSC (DE Only Mode)

I	TEM	Symbol	Min	Тур	Max	Unit	Note
	Display Period	t⊦∨	-	960	-	tclk	
Horizontal	Blank	tнв	100	140	240	tclk	
	Total	tHP	1060	1100	1200	tclk	2200/2
	Display Period	tvv	-	1080	4	tHP	
Vertical	Blank	t∨B	11	45	69	tHP	
	Total	tvp	1091	1125	1149	tHP	
	DCLK	fclk	70	74.25	77	MHz	148.5/2
Frequency	Horizontal	fн	65	67.5	70	KHz	
	Vertical	f∨	57	60	63	Hz	

Table 6-2. TIMING TABLE for PAL (DE Only Mode)

		<u> </u>	, , , , ,				
ı	TEM	Symbol	Min	Тур	Max	Unit	Note
	Display Period	thv	-	960	-	tclk	
Horizontal	Blank	tнв	100	140	240	tclk	
	Total	tHP	1060	1100	1200	tclk	2200/2
	Display Period	tvv	-	1080	-	tHP	
Vertical	Blank	t∨B	228	270	300	tHP	
	Total	tvp	1308	1350	1380	tHP	
	DCLK	fclk	70	74.25	77	MHz	148.5/2
Frequency	Horizontal	fH	65	67.5	70	KHz	
	Vertical	fv	47	50	53	Hz	

Note The Input of HSYNC & VSYNC signal does not have an effect on normal operation(DE Only Mode). The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate.

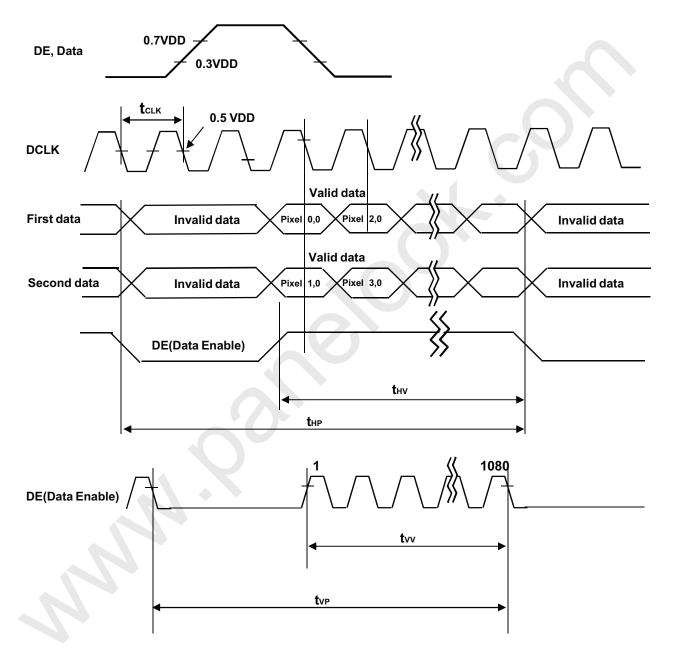
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3-4. LVDS Signal Specification

3-4-1. LVDS Input Signal Timing Diagram



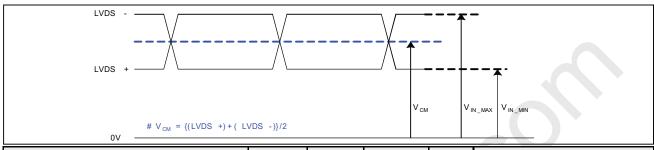
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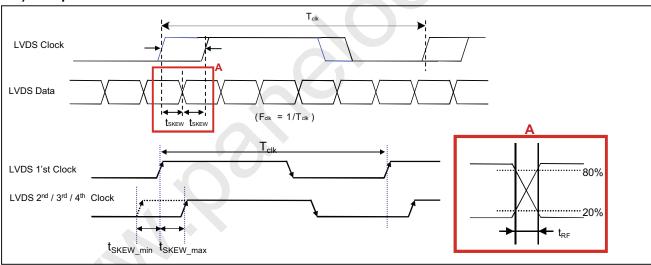
3-4-2. LVDS Input Signal Characteristics

1) DC Specification



Description	Symbol	Min	Max	Unit	Note
LVDS Common mode Voltage	V _{CM}	1.0	1.5	V	_
LVDS Input Voltage Range	V_{IN}	0.7	1.8	٧	<u>-</u>
Change in common mode Voltage	ΔVCM		250	mV	-

2) AC Specification



Description	1	Symbol	Min	Max	Unit	Note
LVDS Differential Voltage	High Threshold			300	mV	2
LVD3 Dillerential Voltage	Low Threshold	V_{TL}	-300	-100	mV	J
LVDS Clock to Data Skew Mar	gin	t _{SKEW}		(<mark>0.25</mark> *T _{clk})/7	ps	-
LVDS Clock/DATA Rising/Fall	ing time	t _{RF}	260	(0.3*T _{clk})/7	ps	2
Effective time of LVDS	t _{eff}	±360		ps	-	
LVDS Clock to Clock Skew Ma	argin (Even to Odd)	t _{SKEW_EO}	·	1/7* T _{clk}	T_{clk}	-

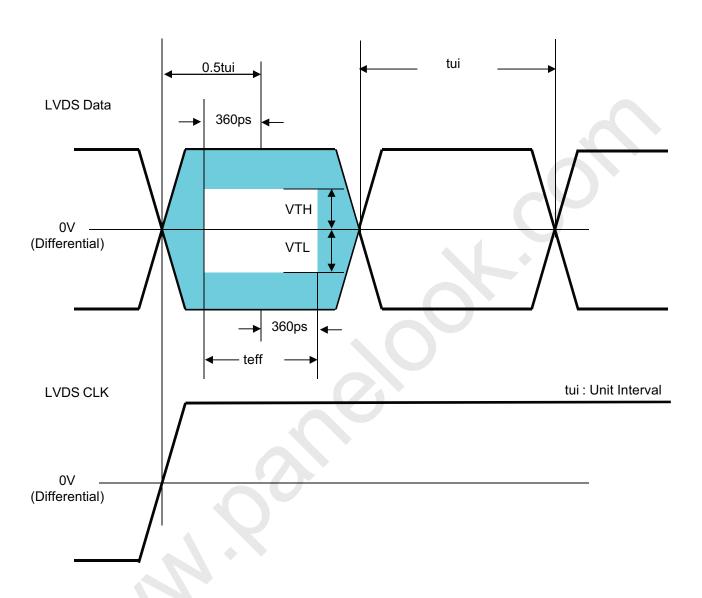
Note 1. All Input levels of LVDS signals are based on the EIA 644 Standard.

- 2. If t_{RF} isn't enough, t_{eff} should be meet the range.
- 3. LVDS Differential Voltage is defined within teff

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^{*} This accumulated waveform is tested with differential probe

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3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 10-bit gray scale data input for the color. The higher binary input, the brighter the color. Table 8 provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

													Ir	pu	t (СоІ	or	Da	ıta												
Со	Color		R8	R7 F		RED 5 R4	R3		_SB R1	R0	MS G9		3 G	7 (GRE G5			G2		_SB			B7	B6		UE B4	В3	B2	LS B1	
	Black	0	0	0 (0 0	0	0	0	0	0	0	0	()	0	0	0	0	0	0	0		0	-			0		0		
	Red (1023)	1	1	1	 1 1	1	1	1	1	1	0	0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1023)	0	0	0 (0	0	0	0	0	0	1	. 1		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
Basic	Blue (1023)	0	0	0 (0	0	0	0	0	0	0	0)	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0 (0	0	0	0	0	0	1	. 1		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1 1			1	1	1	0	0)	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1 1		1	1	1	1	1	1		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	White	1	1	1	1 1	1	1	1	1	1	1	1		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (0000)	0	0	0	0 0	0	0	0	0	0	0	0	()	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (0001)	0	0	0	0 0	0	0	0	0	1	0	0	()	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																															
	RED (1022)	1	1	1	1 1	1	1	1	1	0	0	0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (1023)	1	1	1	1 1	1	1	1	1	1	0	0	()	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(0000)	0	0	0 (0 0	0	0	0	0	0	0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(0001)	0	0	0 (0	0	0	0	0	0	0)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
GREEN						 																					 				
	GREEN(1022)	0	0	0 (0	0	0	0	0	0	1		١	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0
	GREEN(1023)	0	0	0 (0 0	0	0	0	0	0	1	1		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	BLUE (0000)	0	0	0 (0	0	0	0	0	0	0	0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (0001)	0	0	0 (0	0	0	0	0	0	0	0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE	BLUE (1022)			0 (0									 0	0	0									1			 1	 1	0
	BLUE (1023)	0	0	0 () 0	0	0	0	0	0	0	0)	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1

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3-6. Power Sequence

3-6-1. LCD Driving circuit

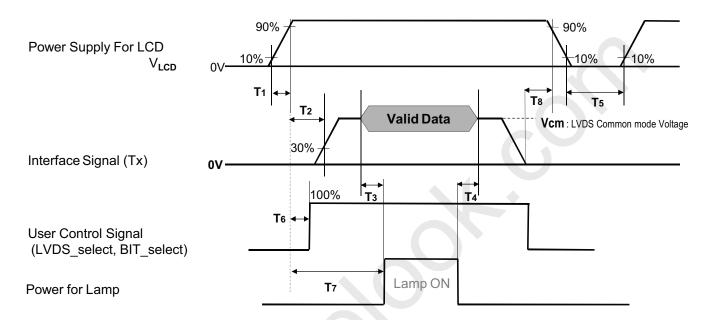


Table 8. POWER SEQUENCE

Davamatar		Unit	Netes		
Parameter	Min	Тур	Max	Onit	Notes
T1	0.5	-	20	ms	
T2	0	-	-	ms	4
Т3	200	-	-	ms	3
T4	200	-	-	ms	3
T 5	1.0	-	-	s	5
Т6	-	-	T2	ms	4
T 7	0.5	-	-	S	
Т8	100	-	-	ms	6

notes:

- 1. Even though T1 is over the specified value, there is no problem if I2T spec of fuse is satisfied.
- 2. If T2 is satisfied with specification after removing LVDS Cable, there is no problem.
- 3. The T3 / T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
- 4. T5 should be measured after the Module has been fully discharged between power off and on period.
- 5. If the on time of signals (Interface signal and user control signals) precedes the on time of Power (V_{LCD}), it will be happened abnormal display. When T6 is NC status, T6 doesn't need to be measured.
- 6. If there is no abnormal display, no problem.
- 7. It is recommendation specification that T8 has to be 100ms as a minimum value.
- * Please avoid floating state of interface signal at invalid period.
- * When the power supply for LCD (VLCD) is off, be sure to pull down the valid and invalid data to 0V.

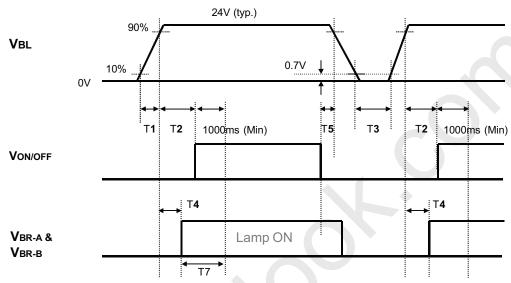
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Product Specification

3-6-2. Sequence for Inverter

Power Supply For Inverter



3-6-3. Deep condition for Inverter

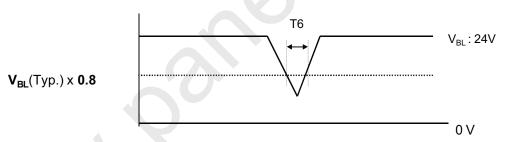


Table 9. Power Sequence for Inverter

Doromotor		Values		Lleite	Domorko
Parameter	Min	Тур	Max	Units	Remarks
T1	20	-	-	ms	1
T2	500	-	-	ms	
Т3	200	-	-	ms	
T4	0		-	ms	2
T5	10	-	-	ms	
T6	-	-	10	ms	V _{BL} (Typ) x 0.8
T7	1000	-	-	ms	3

Notes: 1. T1 describes rising time of 0V to 24V and this parameter does not applied at restarting time.

- 2. T4(max) is less than T2.
- 3. In T7 section, VBR-B is recommended 3.3V & VBR-A = 1.65V

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Product Specification

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at $25\pm2^{\circ}$ C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 °.

FIG. 1 shows additional information concerning the measurement equipment and method.

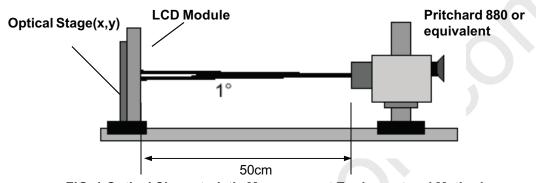


FIG. 1 Optical Characteristic Measurement Equipment and Method

Ta= $25\pm2^{\circ}$ C, V_{LCD} =12.0V, f_{V} =60Hz, Dclk=74.25MHz VBR_A=1.65V, VBR-B=3.3V

LUTARACII	ERISTICS	•	DCIK=74.25MHZ VBR_A=1.65V, VBR-B=3.3							
Parameter Contrast Ratio				Value		l lmit	Nata			
			Min	Тур	Max	Unit	Note			
	CR		1000	1200(TBD)	-		1			
white	L _{WH}		800	1000	-	cd/m ²	2			
	δ_{WHITE}	5P	-	-	1.3		3			
Gray-to-Gray		G	-	9(TBD)	15	ms	4			
RED	Rx			0.639(TBD)						
KLD	Ry			0.334(TBD)						
CDEEN	Gx		Тур	0.290(TBD)						
GILLIN	Gy			0.606(TBD)	Тур					
DILLE	Вх		-0.03	0.146(TBD)	+0.03					
BLUE	Ву			0.058(TBD)						
WHITE	Wx			0.279						
VVIIII	Wy			0.292						
0)										
right(φ=0°)	θr	θr		-	-					
eft (φ=180°)	θΙ		89	-	-	4	-			
y axis, up (φ=90°)		θu		-	-	aegree	5			
down (φ=270°)	θd		89	-	-					
			-	-	-		6			
	rer white Gray-to-Gray RED GREEN BLUE WHITE 0) right(φ=0°) eft (φ=180°) up (φ=90°)	ter Symbol CR white L_{WH} $\frac{\delta_{WHITE}}{\delta_{WHITE}}$ Gray-to-Gray G to G RED Ry GREEN Gy BLUE By WHITE Wy 0) $\frac{\delta_{WHITE}}{\delta_{WHITE}}$ 0)	$ \begin{array}{c c} & & & & \\ & & & \\ \hline white & & & \\ \hline white & & \\ \hline & & \\ \hline white & & \\ \hline & \\ \hline white & & \\ \hline $	Symbol Min white L_{WH} 800 δ_{WHITE} 5P - Gray-to-Gray G to G - RED Rx Ry GREEN Gy Typ BLUE By -0.03 WHITE Wy 0) right(ϕ =0°) θ r 89 eft (ϕ =180°) θ l 89 up (ϕ =90°) θ u 89	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	Symbol Min Typ Max	Symbol Symbol Min Typ Max Unit			

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Product Specification

Notes :1. Contrast Ratio(CR) is defined mathematically as :

CR(Contrast Ratio) = Maximum CRn (n=1, 2, 3, 4, 5)

Surface Luminance at position n with all white pixels

Surface Luminance at position n with all black pixels

n =the Position number(1, 2, 3, 4, 5). For more information, see FIG 2.

- 2. Surface luminance are determined after the unit has been 'ON' and 60min after lighting the backlight in a dark environment at $25\pm2^{\circ}$ C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
- 3. The variation in surface luminance , δ WHITE is defined as : δ WHITE(5P) = Maximum(L_{on1},L_{on2}, L_{on3}, L_{on4}, L_{on5}) / Minimum(L_{on1},L_{on2}, L_{on3}, L_{on4}, L_{on5}) Where L_{on1} to L_{on5} are the luminance with all pixels displaying white at 5 locations . For more information, see the FIG. 2.
- 4. Response time is the time required for the display to transition from G(N) to G(M) (Rise Time, Tr_R) and from G(M) to G(N) (Decay Time, Tr_D). For additional information see the FIG. 3. (N<M)
 ※ G to G Spec is average of measured time (N, M = 0 (Black) ~ 1023(White), 128 gray step).
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- Gray scale specification Gamma Value is approximately 2.2. For more information, see the Table 11.

Table 11. GRAY SCALE SPECIFICATION

Gray Level	Luminance [%] (Typ.)
LO	0.07
L63	0.27
L127	1.04
L191	2.49
L255	4.68
L319	7.66
L383	11.5
L447	16.1
L511	21.6
L575	28.1
L639	35.4
L703	43.7
L767	53.0
L831	63.2
L895	74.5
L959	86.7
L1023	100

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Global LCD Panel Exchange Center

LD470WUB

Product Specification

Measuring point for surface luminance & measuring point for luminance variation.

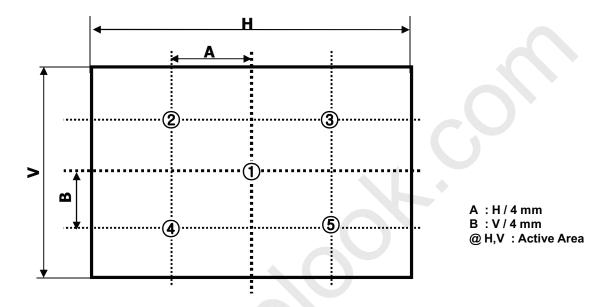


FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

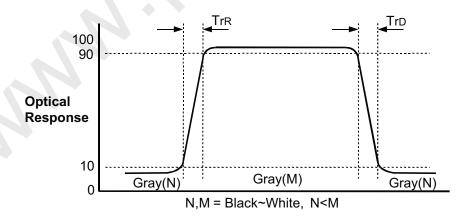


FIG. 3 Response Time

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Product Specification

Dimension of viewing angle range

Global LCD Panel Exchange Center

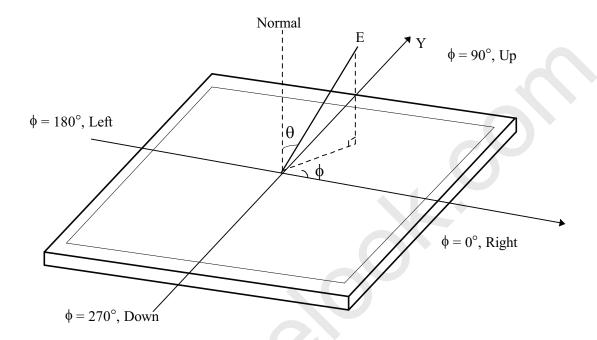


FIG. 4 Viewing Angle

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Product Specification

5. Mechanical Characteristics

Table 12 provides general mechanical characteristics.

Table 12. MECHANICAL CHARACTERISTICS

Item		Value
	Horizontal	1068.0 mm
Outline Dimension	Vertical	613.2 mm
	Depth	54.7 mm
Bezel Area	Horizontal	1046 mm
bezer Area	Vertical	591.2 mm
Active Display Area	Horizontal	1039.68 mm
Active Display Area	Vertical	584.82 mm
Weight	14.5 Kg (Ty	/p.), 16.5 Kg (Max.)

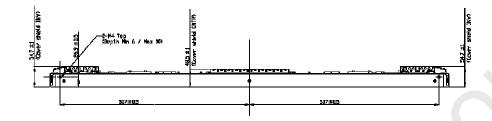
Note: Please refer to a mechanic drawing in terms of tolerance at the next page.

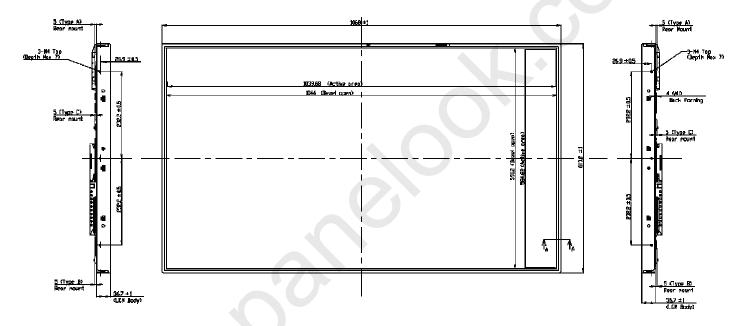
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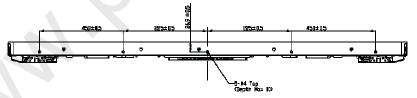


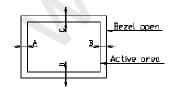
Product Specification

<FRONT VIEW>



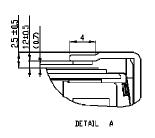






NOTES 1. Unspecified toleronces are to be $\pm 1.0 \, \text{nm}$.

Tilt and portial disposition talerance of display area are as following.
 X-Direction : IA-BI ≤ 1.5mm
 Y-Direction : IC-DI ≤ 1.5mm

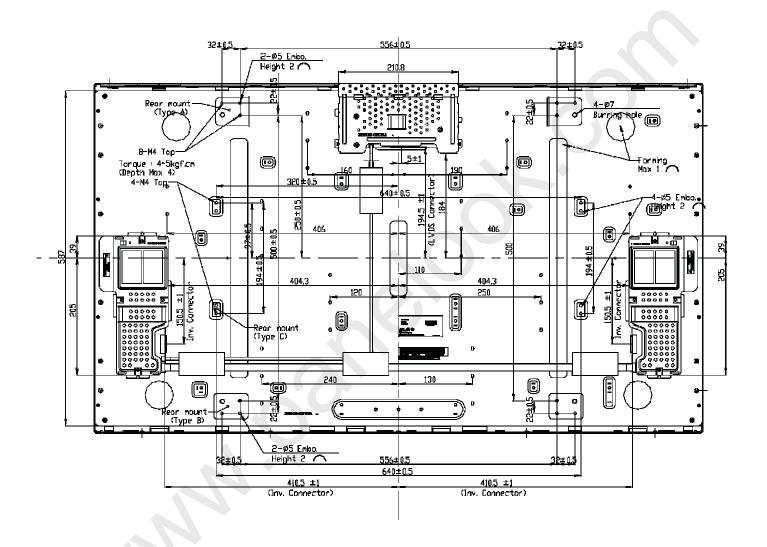


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Product Specification

<REAR VIEW>



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Product Specification

6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition				
1	High temperature storage test	Ta= 60°C 240h				
2	Low temperature storage test	Ta= -20°C 240h				
3	High temperature operation test	Ta= 50°C 50%RH 240h Ta= 0°C 240h Wave form: random Vibration level: 1.0Grms				
4	Low temperature operation test	Ta= 0°C 240h				
5	Vibration test (non-operating)					
6	Shock test (non-operating)	Shock level :50G(X,Y axis) , 35G(Z axis) Waveform : half sine wave, 11ms Direction : \pm X, \pm Y, \pm Z One time each direction				
7	Humidity condition Operation	Ta= 40 °C, 90%RH				
8	Altitude operating storage / shipment	0 - 15,000 feet 0 - 40,000 feet				

Note: Before and after Reliability test, LCM should be operated with normal function.

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Product Specification

7. International Standards

7-1. Safety

- a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc., Standard for Safety of Information Technology Equipment.
- b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association, Standard for Safety of Information Technology Equipment.
- c) EN 60950-1:2001, First Edition,
 European Committee for Electrotechnical Standardization(CENELEC)
 European Standard for Safety of Information Technology Equipment.
- d) IEC 60950-1:2001, First Edition, The International Electrotechnical Commission (IEC) Standard for Safety of Information Technology Equipment.

7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

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Product Specification

8. Packing

8-1. Information of LCM Label

a) LCM Label

Α	В	С	D	Е	F	G	Н	I	J	К	L	М	
---	---	---	---	---	---	---	---	---	---	---	---	---	--

A,B,C: SIZE(INCH)

D : YEAR E : MONTH

F : PANEL CODE G : FACTORY CODE H : ASSEMBLY CODE I,J,K,L,M : SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	Α	В	С	D	Е	F	G	I	J	K

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	Α	В	С

b) Location of LCM Label

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one Pallet: 12 pcs

b) Pallet Size: 1300 mm X 1140 mm X 117.5 mm.

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Product Specification

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=±200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

 And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw. (if not, it can causes conductive particles and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) The conductive material and signal cables are kept away from inverter transformer to prevent abnormal display, sound noise and temperature rising.
- (11) Partial darkness may happen during $3\sim5$ minutes when LCM is operated initially in condition that luminance is under 40% at low temperature (under 5° C). This phenomenon which disappears naturally after $3\sim5$ minutes is not a problem about reliability but LCD characteristic

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Product Specification

9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

9-7. Appropriate Condition for Public Display

- Generally large-sized LCD modules are designed for consumer applications (TV).
 Accordingly, a long-term display like in Public Display (PD) application, can cause uneven display including image sticking. To optimize module's lifetime and function, several operating usages are required.
- 1. Normal operating condition
 - Temperature: 0 ~ 40 °C
 - Operating Ambient Humidity: 10 ~ 90 %
 - Display pattern: dynamic pattern (Real display)

Note) Long-term static display can cause image sticking.

- 2. Operating usages under abnormal condition
- a. Ambient condition
 - Well-ventilated place is recommended to set up PD system.
- b. Power and screen save
 - Periodical power-off or screen save is needed after long-term display.

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Product Specification

- 3. Operating usages to protect against image sticking due to long-term static display
- a. Suitable operating time: under 18 hours a day.
- b. Static information display recommended to use with moving image.
- Cycling display between 5 minutes' information(static) display and 10 seconds' moving image.
- c. Background and character (image) color change
 - Use different colors for background and character, respectively.
 - Change colors themselves periodically.
- d. Avoid combination of background and character with large different luminance.
- 1) Abnormal condition just means conditions except normal condition.
- 2) Black image or moving image is strongly recommended as a screen save.
- 4. Lifetime in this spec. is guaranteed only when PD is used according to operating usages.
- 5. Module should be turned clockwise based on front view when used in portrait mode.

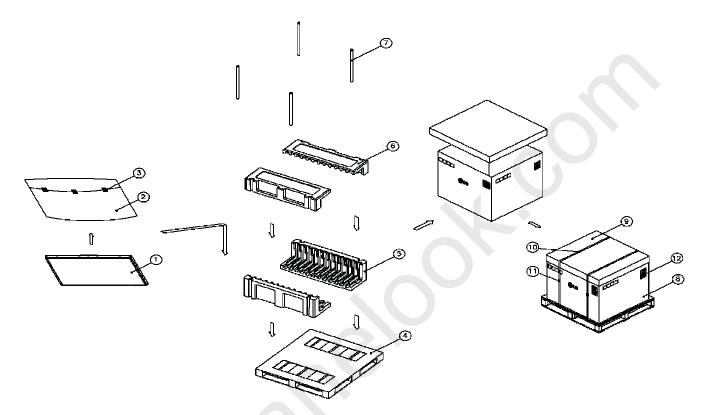
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Product Specification

APPENDIX-III

■ Pallet Ass'y



NO.	DESCRIPTION	MATERIAL
1	LCD Module	47INCH
2	BAG	AL BAG
3	TAPE	MASKING 20MMX50M
4	PALLET	PAPER 1300X1140X130MM
5	PACKING,BOTTOM	EPS
6	PACKING,TOP	EPS
7	ANGLE,POST	PAPER
8	ANGLE,PACKING	PAPER
9	BAND,CLIP	STEEL
10	BAND	PP
11	LABEL	YUPO 80G 100X100

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Product Specification

APPENDIX-IV

■ LCM Label



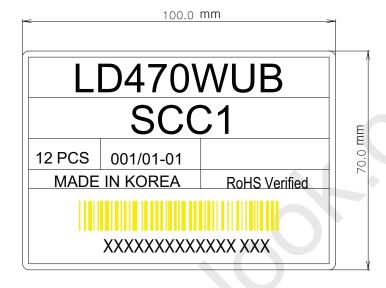
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Product Specification

#APPENDIX- V

■ Pallet Label



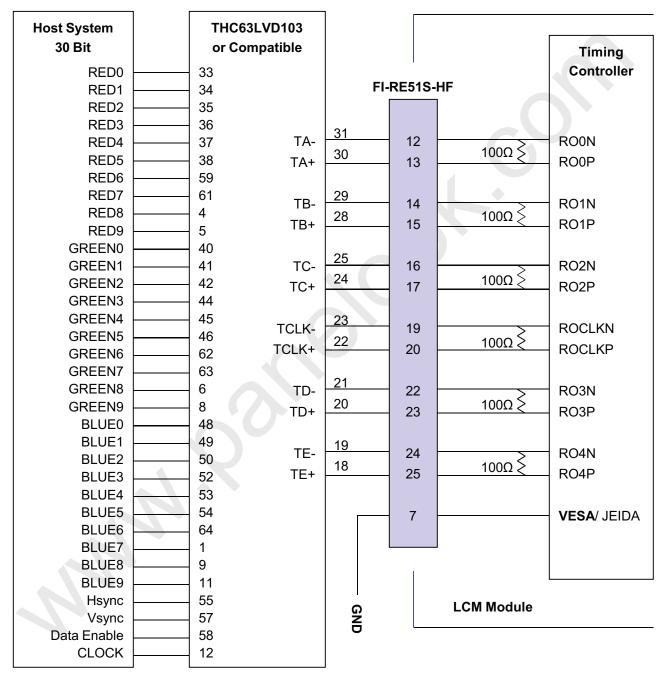
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Product Specification

#APPENDIX-III-1

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7= "L" or "NC")



Note: 1. The LCD module uses a 100 $Ohm[\Omega]$ resistor between positive and negative lines of each receiver input.

- 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)
- 3. '9' means MSB and '0' means LSB at R,G,B pixel data.

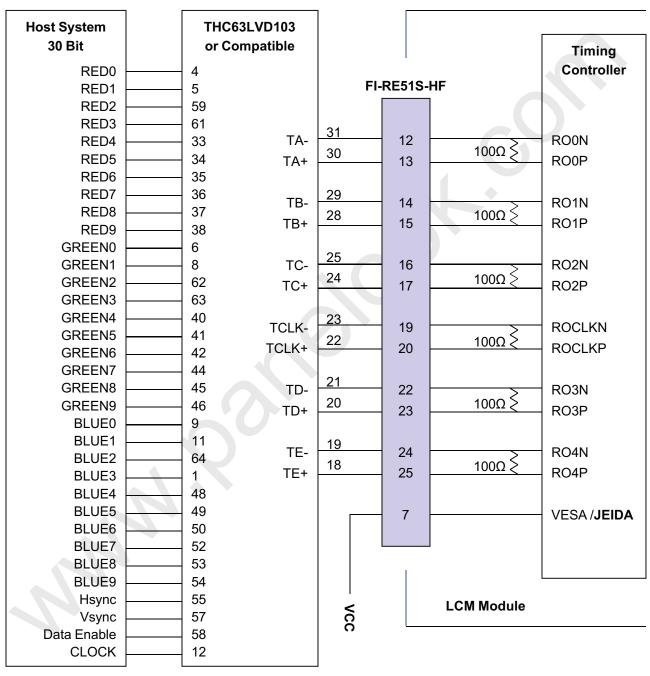
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Product Specification

#APPENDIX-III-2

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7= "H")



Note :1. The LCD module uses a 100 $Ohm[\Omega]$ resistor between positive and negative lines of each receiver input.

- 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)
- 3. '9' means MSB and '0' means LSB at R,G,B pixel data.

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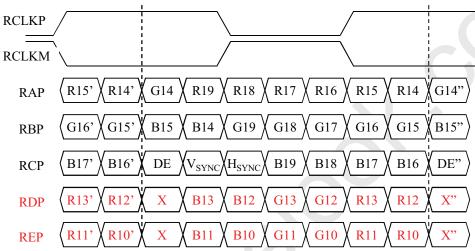


Product Specification

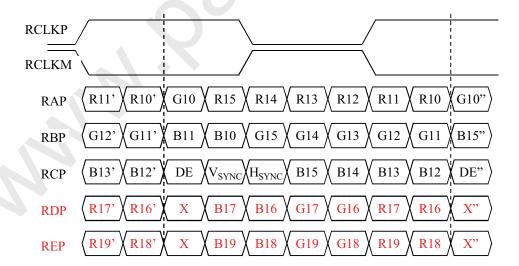
APPENDIX- II-1

LVDS Data-Mapping info. (10bit)

■ LVDS Select: "H" Data-Mapping (JEIDA format)



■ LVDS Select : "L" Data-Mapping (VESA format)



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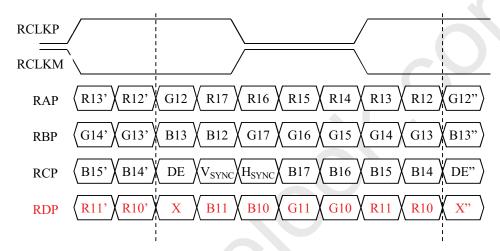


Product Specification

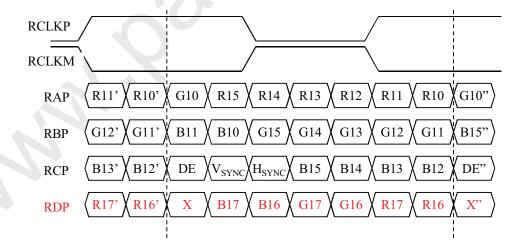
APPENDIX- II-2

LVDS Data-Mapping info. (8bit)

■ LVDS Select: "H" Data-Mapping (JEIDA format)



■ LVDS Select: "L" Data-Mapping (VESA format)



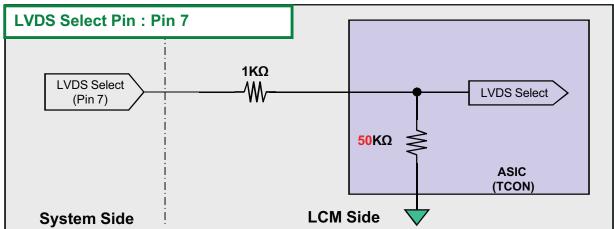
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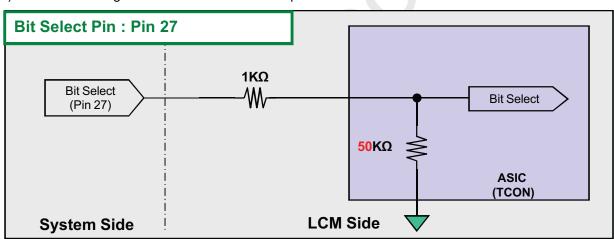
Product Specification

APPENDIX- VI

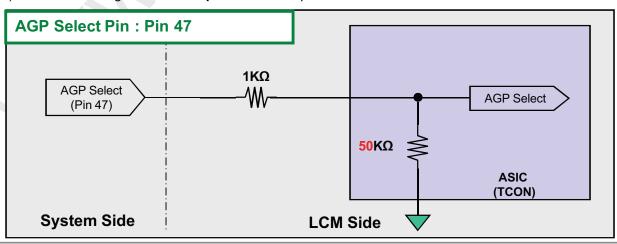
- Option Pin Circuit Block Diagram
 - 1) Circuit Block Diagram of ${\bf LVDS}$ Format Selection pin



2) Circuit Block Diagram of ${\bf Bit\ Format\ Selection\ pin}$



3) Circuit Block Diagram of AGP Option Selection pin



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