

# Low Capacitance Quad Array for ESD Protection

This integrated transient voltage suppressor device (TVS) is designed for applications requiring transient overvoltage protection. It is intended for use in sensitive equipment such as computers, printers, business machines, communication systems, medical equipment, and other applications. Its integrated design provides very effective and reliable protection for four separate lines using only one package. These devices are ideal for situations where board space is at a premium.

#### **Features**

- ESD Protection: IEC61000–4–2: Level 4
  - MILSTD 883C Method 3015-6: Class 3
- Four Separate Unidirectional Configurations for Protection
- Low Leakage Current < 1 μA @ 3 Volts
- Power Dissipation: 380 mW
- Small SOT-553 SMT Package
- Low Capacitance
- Complies to USB 1.1 Low Speed & High Speed Specifications
- We declare that material of product compliance with ROHS requirements.

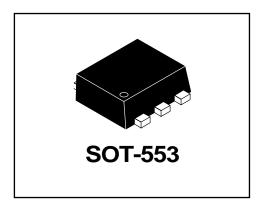
#### **Benefits**

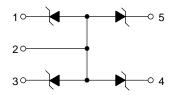
- Provides Protection for ESD Industry Standards: IEC 61000, HBM
- Protects Four Lines Against Transient Voltage Conditions
- Minimize Power Consumption of the System
- Minimize PCB Board Space

## **Typical Applications**

- Instrumentation Equipment
- Serial and Parallel Ports
- Microprocessor Based Equipment
- Notebooks, Desktops, Servers
- Cellular and Portable Equipment

# LESDA6V8AV5T1G





## **Ordering Information**

Device	Package	Shipping		
LESDA6V8AV5T1G	SOT-553	3000/Tape&Reel		

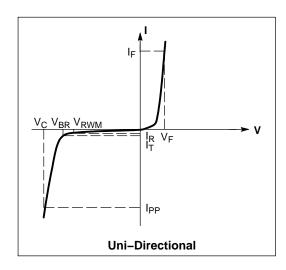


## LESDA6V8AV5T1G

#### **ELECTRICAL CHARACTERISTICS**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

Symbol	Parameter
I <sub>PP</sub>	Maximum Reverse Peak Pulse Current
V <sub>C</sub>	Clamping Voltage @ I <sub>PP</sub>
V <sub>RWM</sub>	Working Peak Reverse Voltage
I <sub>R</sub>	Maximum Reverse Leakage Current @ V <sub>RWM</sub>
V <sub>BR</sub>	Breakdown Voltage @ I <sub>T</sub>
I <sub>T</sub>	Test Current
ΘV <sub>BR</sub>	Maximum Temperature Coefficient of V <sub>BR</sub>
l <sub>F</sub>	Forward Current
V <sub>F</sub>	Forward Voltage @ I <sub>F</sub>
Z <sub>ZT</sub>	Maximum Zener Impedance @ I <sub>ZT</sub>
I <sub>ZK</sub>	Reverse Current
Z <sub>ZK</sub>	Maximum Zener Impedance @ I <sub>ZK</sub>



## **MAXIMUM RATINGS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Value	Unit
Peak Power Dissipation (8 X 20 μs @ T <sub>A</sub> = 25°C) (Note 1)	P <sub>PK</sub>	20	W
Steady State Power – 1 Diode (Note 2)	P <sub>D</sub>	380	mW
Thermal Resistance, Junction–to–Ambient Above 25°C, Derate	$R_{ hetaJA}$	327 3.05	°C/W mW/°C
Maximum Junction Temperature	T <sub>Jmax</sub>	150	°C
Operating Junction and Storage Temperature Range	T <sub>J</sub> T <sub>stg</sub>	-55 to +150	°C
Lead Solder Temperature (10 seconds duration)	T <sub>L</sub>	260	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C)

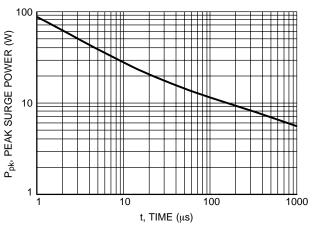
Device	Breakdown Voltage  Device V <sub>BR</sub> @ 1 mA (Volts)		Leakage Current I <sub>RM</sub> @ V <sub>RM</sub>		V <sub>C</sub> Max @ I <sub>PP</sub>		Typ Capacitance @ 0 V Bias (pF) (Note 3)		Typ Capacitance @ 3 V Bias (pF) (Note 3)		
	Min	Nom	Max	V <sub>RWM</sub>	I <sub>RWM</sub> (μA)	V <sub>C</sub> (V)	I <sub>PP</sub> (A)	Тур	Max	Тур	Max
LESDA6V8AV5T1G	6.47	6.8	7.14	4.3	1.0	13	1.6	12	15	6.7	9.5

- Non-repetitive current per Figure 1.
   Only 1 diode under power. For all 4 diodes under power, P<sub>D</sub> will be 25%. Mounted on FR-4 board with min pad.
   Capacitance of one diode at f = 1 MHz, V<sub>R</sub> = 0 V, T<sub>A</sub> = 25°C



## LESDA6V8AV5T1G

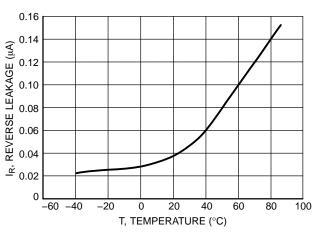
## TYPICAL ELECTRICAL CHARACTERISTICS



110 100 % OF RATED POWER OR IPP 90 80 70 60 50 40 30 20 10 0 100 <del>15</del>0 T<sub>A</sub>, AMBIENT TEMPERATURE (°C)

Figure 1. Pulse Width

Figure 2. Power Derating Curve



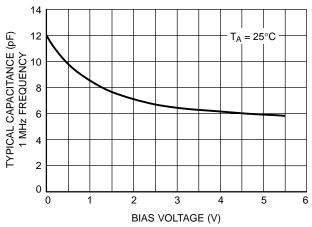
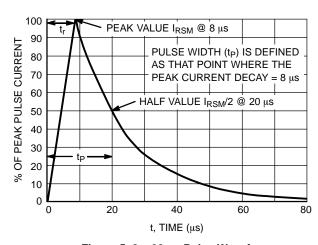


Figure 3. Reverse Leakage versus Temperature

Figure 4. Capacitance



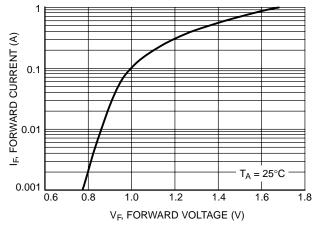


Figure 5.  $8 \times 20$  s Pulse Waveform

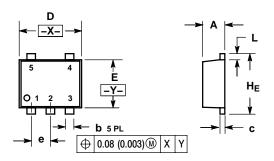
Figure 6. Forward Voltage



## LESDA6V8AV5T1G

## **PACKAGE DIMENSIONS**

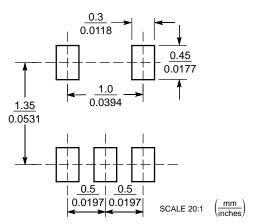
**SOT-553, 5 LEAD** 



	М	ILLIMETE	RS	INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.50	0.55	0.60	0.020	0.022	0.024	
b	0.17	0.22	0.27	0.007	0.009	0.011	
С	0.08	0.13	0.18	0.003	0.005	0.007	
D	1.50	1.60	1.70	0.059	0.063	0.067	
E	1.10	1.20	1.30	0.043	0.047	0.051	
е		0.50 BSC		0.020 BSC			
L	0.10	0.20	0.30	0.004	800.0	0.012	
HE	1.50	1.60	1.70	0.059	0.063	0.067	

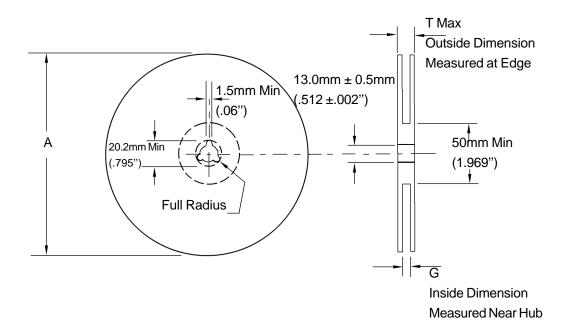
STYLE 2:
PIN 1. CATHODE
2. COMMON ANODE
3. CATHODE 2
4. CATHODE 3
5. CATHODE 4

## **SOLDERING FOOTPRINT\***





## **EMBOSSED TAPE AND REEL DATA** FOR DISCRETES



Size	A Max	G	T Max	
8 mm	178.0mm	8.4mm+1.5mm, -0.0	10.9mm	
	(7.0")	(.33"+.039", -0.00)	(.43")	

## Reel Dimensions

Metric Dimensions Govern — English are in parentheses for reference only

## **Storage Conditions**

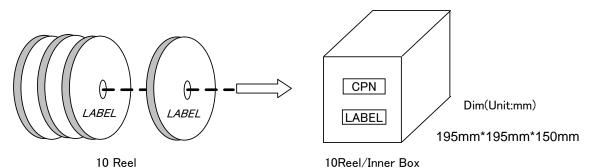
Temperature: 5 to 40 Deg.C (20 to 30 Deg. C is preferred) Humidity: 30 to 80 RH (40 to 60 is preferred) Recommended Period: One year after manufacturing (This recommended period is for the soldering condition only. The

characteristics and reliabilities of the products are not restricted to

this limitation)

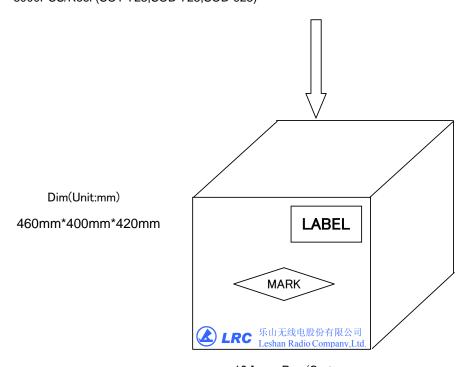


# **Shipment Specification**



30KPCS/Inner Box

3000PCS/Reel 80KPCS/Inner Box (SOT-723,SOD-723,SOD-923) 8000PCS/Reel (SOT-723,SOD-723,SOD-923)



12 Inner Box/Carton

360KPCS/Carton 960KPCS/Carton (SOT-723,SOD-723,SOD-923)