

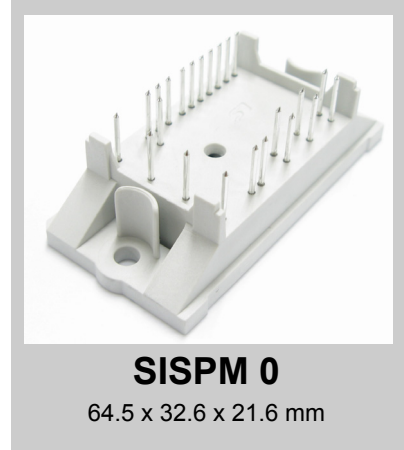
Features

- Trench Field Stop Technology IGBT adopted (IGBT4)
- Low saturation voltage
- Positive temperature coefficient
- Fast switching
- Free wheeling diodes with fast and soft reverse recovery
- Industrial standard package with insulated substrate
- Temperature sensor (NTC) included
- Input from single or three phase grid

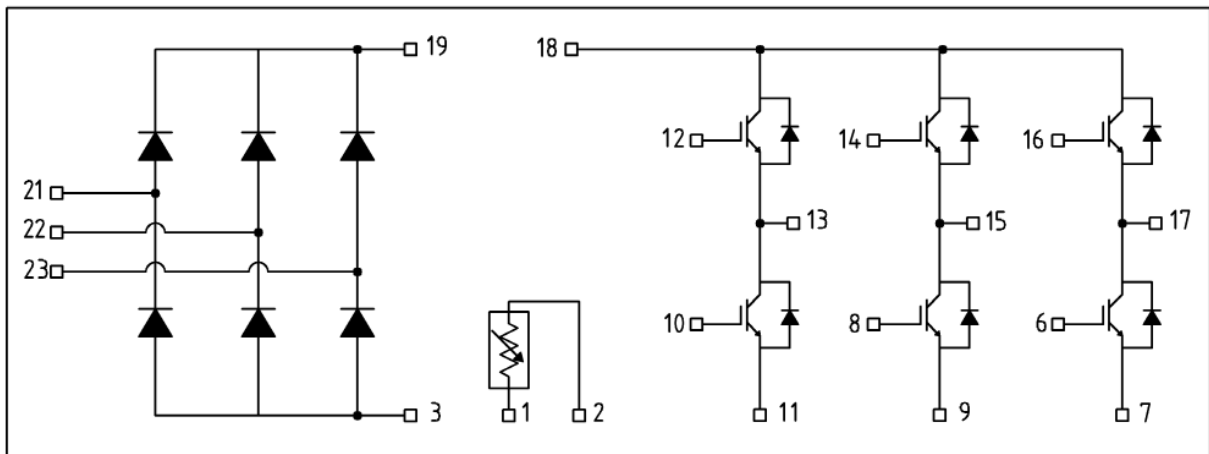
Applications

- Three phase synchronous or asynchronous motor drive

Preliminary Data



Internal Equivalent Circuit



Pin Description

Pin Number	Pin Name	Pin Description
1,2	TH1, TH2	NTC-, NTC+
3	N	Negative DC Link Input
6, 8, 10	GWN, GVN, GUN	Gate Input for Low-side W Phase, V Phase, U Phase
7, 9, 11	DCN	Negative DC Link Output
12, 14, 16	GUP, GVP, GWP	Gate Input for High-side U Phase, V Phase, W Phase
13, 15, 17	U, V, W	Output for U Phase, V Phase, W Phase
18	DCP	Positive DC Link Output
19	P	Positive DC Link Input
21	R	Input for R Phase
22	S	Input for S Phase
23	T	Input for T Phase

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Item	Symbol	Parameter	Conditions	Value	Units
Input Rectifier	V_{RRM}	Repetitive Peak Reverse Voltage		1600	V
	I_{FAV}	Forward Current per Diode	@ $T_j = 150^\circ\text{C}$, $T_C = 80^\circ\text{C}$	15	A
	I_{FSM}	Surge Forward Current (Chip level)	@ $t_p = 10\text{ ms}$, half sine wave	220	A
	I^2t	I^2t - Value (Chip level)	@ $t_p = 10\text{ ms}$, half sine wave	200	A^2s
	P_D	Maximum Power Dissipation	@ $T_j = 150^\circ\text{C}$, $T_C = 80^\circ\text{C}$	40	W
	T_j	Operating Junction Temperature ⁽¹⁾	-	-40 ~ 125	$^\circ\text{C}$
Transistor Inverter	V_{CES}	Collector-Emitter Breakdown Voltage	-	1200	V
	V_{GES}	Gate-Emitter Peak Voltage	-	± 20	V
	I_C	DC Collector Current	@ $T_j = 175^\circ\text{C}$, $T_C = 80^\circ\text{C}$	15	A
	I_{cpulse}	Repetitive Peak Collector Current	@ $t_p = 1\text{ ms}$	30	A
	P_D	Maximum Power Dissipation	@ $T_j = 175^\circ\text{C}$, $T_C = 80^\circ\text{C}$	60	W
	T_{SC}	SC Withstand Time (Chip level)	$V_{GE} = 15\text{ V}$, $V_{CE} = 600\text{ V}$	10	μs
Diode Inverter	T_j	Operating Junction Temperature ⁽²⁾	-	-40 ~ 125	$^\circ\text{C}$
	V_{RRM}	Repetitive Peak Reverse Voltage	-	1200	V
	I_F	DC Forward Current	-	15	A
	I_{FRM}	Repetitive Peak Forward Current	@ $t_p = 1\text{ ms}$	30	A
	P_D	Maximum Power Dissipation	@ $T_j = 150^\circ\text{C}$, $T_C = 80^\circ\text{C}$	30	W
Module	T_j	Operating Junction Temperature ⁽¹⁾	-	-40 ~ 125	$^\circ\text{C}$
	T_{stg}	Storage Temperature	-	-40~125	$^\circ\text{C}$
	V_{iso}	Isolation Voltage	@ AC 1minute	2500	V
	W	Weight	-	30	g

(Note *1) The Maximum junction temperature of chip is 150°C .

(Note *2) The Maximum junction temperature of chip is 175°C .

(Note *3) This value is calculated. In other words, this is not the measured value.

LFP15G1207

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Input Rectifier Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_F	Diode Forward Voltage	$T_C = 25^\circ\text{C}, I_F = 15\text{ A}$	-	1.1	-	V
		$T_C = 125^\circ\text{C}, I_F = 15\text{ A}$	-	1.0	-	V
V_{to}	Threshold Voltage	$T_C = 125^\circ\text{C}$	-	0.83	-	V
I_R	Reverse Current (Chip level)	$T_C = 25^\circ\text{C}, V_R = V_{RRM}$	-	0.1	-	mA
r_t	Slope Resistance	$T_C = 125^\circ\text{C}$	-	20.8	-	m Ω
$R_{th(J-C)}$	Thermal Resistance (IGBT Part) ⁽³⁾	Junction-to-Case	-	-	1.7	$^\circ\text{C/W}$

Transistor-Inverter Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$V_{GE(th)}$	Gate-Emitter threshold Voltage	$V_{CE} = V_{GE}, I_{CE} = 15\text{ mA}$	-	6.5	-	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$T_C = 25^\circ\text{C}, I_{CE} = 15\text{ A}, V_{GE} = 15\text{ V}$	-	2.0	-	V
		$T_C = 125^\circ\text{C}, I_{CE} = 15\text{ A}, V_{GE} = 15\text{ V}$	-	2.4	-	V
I_{CES}	Collector-Emitter Cut-off Current	$V_{GE} = 0\text{ V}, V_{CE} = V_{CES}$	-	-	250	μA
I_{CES}	Gate-Emitter Leakage Current	$V_{GE} = \pm 20\text{ V}, V_{CE} = 0\text{ V}$	-	-	-	A
C_{iss}	Input Capacitance	$V_{CE} = 25\text{ V}, V_{GE} = 0\text{ V}$ $f = 1\text{ MHz}, T_C = 25^\circ\text{C}$ (Chip level)	-	0.9	-	nF
C_{oss}	Output Capacitance		-	0.08	-	nF
C_{riss}	Reverse Transfer Capacitance		-	0.06	-	nF
$t_d(on)$	Turn-On Delay Time	$T_C = 125^\circ\text{C}, R_{G ON} = 40\ \Omega$ $R_{G OFF} = 20\ \Omega, L = 500\ \mu\text{H}$ $V_{CE} = 600\text{ V}, V_{GE} = 0\text{ V} \sim 15\text{ V}$ $I_{CE} = 15\text{ A}$	-	-	-	ns
t_r	Rise Time		-	-	-	ns
$t_d(off)$	Turn-Off Delay Time		-	-	-	ns
t_f	Fall Time		-	-	-	ns
E_{on}	Turn-On Switching Loss		-	-	-	mJ
E_{off}	Turn-Off Switching Loss	-	-	-	mJ	
E_{ts}	Total Switching Loss	-	-	-	mJ	
Q_G	Total Gate Charge	$V_{GE} = 0\text{ V} \sim 15\text{ V}$	-	-	-	nC
Q_{GE}	Gate-Emitter Charge		-	-	-	nC
Q_{GC}	Gate-Collector Charge		-	-	-	nC
$R_{th(J-C)}$	Thermal Resistance (IGBT Part) ⁽³⁾	Junction-to-Case	-	-	1.5	$^\circ\text{C/W}$

Diode-Inverter Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_F	Diode Forward Voltage	$I_F = 15\text{ A}, V_{GE} = 0\text{ V}$ $T_C = 125^\circ\text{C}$	-	1.7	-	V
t_{rr}	Diode Reverse Recovery Time	$R_{G ON} = 40\ \Omega$ $T_C = 125^\circ\text{C}$	-	-	-	ns
I_{RRM}	Diode Peak Reverse Recovery Current	$L = 500\ \mu\text{H}$ $T_C = 125^\circ\text{C}$	-	-	-	A
Q_{rr}	Diode Reverse Recovery Charge	$V_{CE} = 600\text{ V}$ $T_C = 125^\circ\text{C}$	-	-	-	μC
E_{rr}	Diode Reverse Recovery Energy	$V_{GE} = 0\text{ V} \sim 15\text{ V}$ $T_C = 125^\circ\text{C}$	-	-	-	mJ
$R_{th(J-C)}$	Thermal Resistance (IGBT Part) ⁽³⁾	$I_{CE} = 15\text{ A}$ Junction-to-Case	-	-	2.0	$^\circ\text{C/W}$

NTC thermistar Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
R_{25}	Resistance	$T_C = 25^\circ\text{C}$	-	22	-	k Ω
P	Power	$T_C = 25^\circ\text{C}$	-	210	-	mW
$B_{25/100}$	B Constant	$T_C = 25^\circ\text{C}, \pm 3\%$ tolerance	-	4000	-	K

* This specifications may not be considered as an assurance of characteristics and may not have same characteristics in case of using different test systems from @LSIS. We therefore strongly recommend prior consultation of our engineers.

Package Dimension(Dimension in mm)

