LJ024U33

Features

- **Display format:** 1024 (W) × 768 (H) dots
- **Dot pitch ratio:** 1:1
- **Input signal level:** H-CMOS level
- **Drive method:** P-P symmetric drive
- **Structure:** Baseplate
- Detachable DC/DC converter
- **Net weight:** Approx. 1050g (1200g*)
  *Including DC/DC converter

■ Absolute maximum ratings

(Ta=25°C)

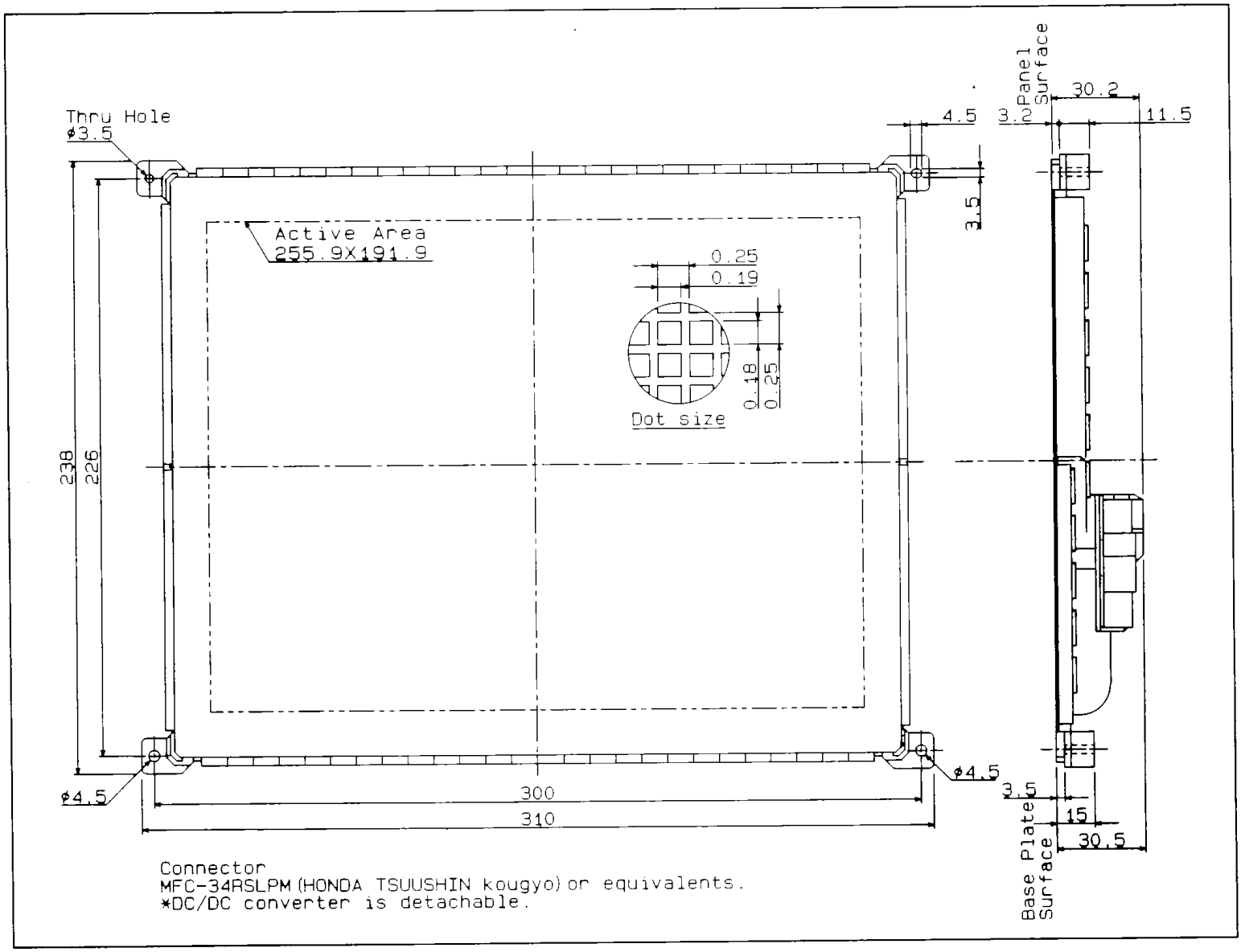
Parameter	Symbol	Rating	Unit
Interface signal (Logic "H")	V_{IH}	V_L +0.5	V
Interface signal (Logic "L")	V_{IL}	−0.5	V
Supply voltage (Logic)	V_L	7	V
Supply voltage (Panel drive)	V_D	27	V
Operating temperature	Topr	0 to +55	°C
Storage temperature	Tstg	−25 to +70	°C

■ Corresponding connector:

MFC-34RPF/MFC-34RPFC or equivalents
(Honda Tsuushin Kougyo)

Outline Dimensions

(Unit:mm)



Electro-optical Characteristics

(Ta=25°C)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Supply voltage (Logic)	V_L	—	4.75	5.0	5.25	V
Supply current (Logic)	I_L	V_L=5V	200	—	700	mA
Supply voltage (Panel drive)	V_D	—	22.8	24.0	25.2	V
Supply current (Panel drive)	I_D	V_D=24V	300	—	1800	mA
Power consumption	P_T	V_L=5V, V_D=24V	—	31	—	W
Luminance	B_{ON}	All dots lit	23	32	—	fL
Off luminance	B_{OFF}	All dots turned off	—	—	1.0	fL
Lumiance distribution	$\triangle B_{DIS}$	All dots lit	—	—	35	%

Interface Timing Ratings

(Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit
Latch period	T_{LC}	35.6	37.0	38.8	μsec
XSCL period	T_{XSC}	125	—	—	nsec
LP pulse width	W_{LP}	70	—	—	nsec
XSCL "L" time	T_{XSL}	—	60	—	nsec
XSCL pulse width	W_{XSC}	—	60	—	nsec
Latch timing	T_{LS1}	200	—	—	nsec
	T_{LS2}	200	—	—	
	T_{LS3}	100	—	—	
	T_{LS}	10	—	—	
Data setup time	T_{DS}	30	—	—	nsec
Data hold time	T_{DH}	30	—	—	nsec
DIN setup time	T_{DIS}	100	—	—	nsec
DIN hold time	T_{DIH}	20	—	—	nsec
Input signal rise time	t_r	—	—	*	nsec
Input signal fall time	t_f	—	—	*	nsec

* $(T_{XSC}-T_{XSC}-W_{XSC})/2$ 30 nsec max.
* $T_{DLS}>31\mu$sec, $T_{DIS}<31\mu$sec

Interface Signals

Pin No.	Symbol	Description
1	V_L	Power supply for logic (+)
2	V_L	Power supply for logic (+)
3	GND	Ground
4	GND	Ground
5	N.C	—
6	DIN	Sync. pulse
7	LP	Latch pulse
8	GND	Ground
9	XSCL	X shift clock
10	GND	Ground
11	UD0	8th display data signal
12	UD1	7th display data signal
13	UD2	6th display data signal
14	UD3	5th display data signal
15	UD4	4th display data signal
16	UD5	3rd display data signal
17	UD6	2nd display data signal
18	UD7	1st display data signal
19	GND	Ground
20	LD0	8th display data signal
21	LD1	7th display data signal
22	LD2	6th display data signal
23	LD3	5th display data signal
24	LD4	4th display data signal
25	LD5	3rd display data signal
26	LD6	2nd display data signal
27	LD7	1st display data signal
28	GND	Ground
29	GND	Ground
30	GND	Ground
31	GND	Ground
32	V_D	Power supply for panel drive
33	V_D	Power supply for panel drive
34	V_D	Power supply for panel drive

Interface Timing Chart