LJ64ZU49

Features

- **Display format:** 640 (W) × 480 (H) dots
- **Dot pitch ratio:** 1:1
- 16-Level gray scale
- **Input signal level:** LS TTL level
- **Drive method:** PWM symmetric drive
- **Structure:** Baseplate
- Detachable DC/DC converter
- **Net weight:** Approx. 620g (700g*)
  *Including DC/DC converter

Absolute maximum ratings

(Ta=25°C)

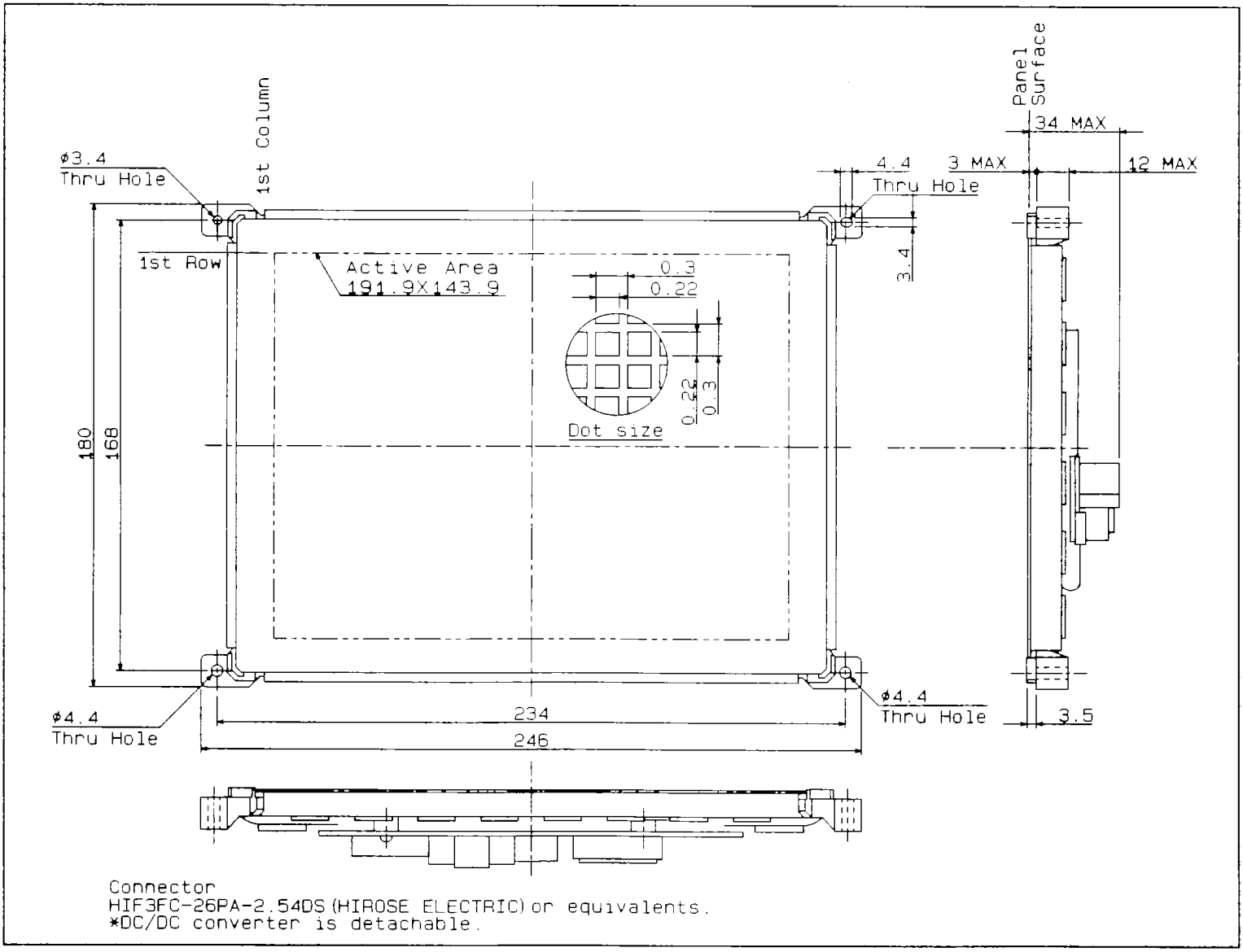
Parameter	Symbol	Rating	Unit
Interface signal (Logic "H")	V_{IH}	5.5	V
Interface signal (Logic "L")	V_{IL}	−0.5	V
Supply voltage (Logic)	V_L	7	V
Supply voltage (Panel drive)	V_D	27	V
Operating temperature	Topr	−5 to +55	°C
Storage temperature	Tstg	−40 to +80	°C

Corresponding connector:

HIF3BA-26D-2.54R (HIROSE) or equivalents

Outline Dimensions

Unit:mm



Electro-optical Characteristics

(Ta=25°C)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Supply voltage (Logic)	V_L	—	4.75	5.0	5.25	V
Supply current (Logic)	I_L	V_L=5V	100	—	350	mA
Supply voltage (Panel drive)	V_D	—	22.8	24.0	25.2	V
Supply current (Panel drive)	I_D	V_D=24V	400	—	1500	mA
Power consumption	P_I	V_L=5V, V_D=24V	—	22	—	W
Luminance	B_{ON}	All dots lit	23	30	—	fL
Off luminance	B_{OFF}	All dots turned off	—	—	1.0	fL
Luminance distribution	$\triangle B_{DIS}$	All dots lit	—	—	35	%

Interface Signals

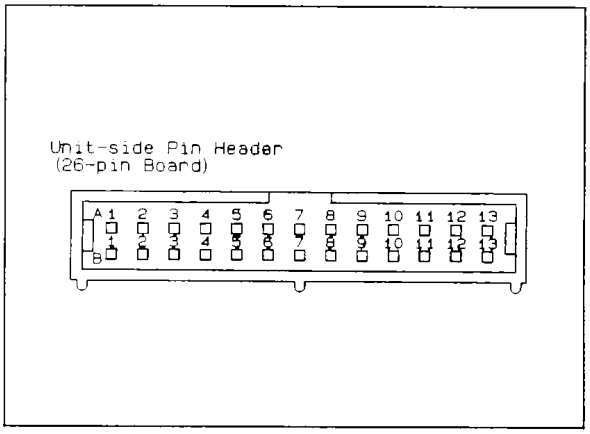
Pin No.	Symbol	Description
A-1	N.C	—
B-1	N.C	—
A-2	D11	Data signal
B-2	D10	
A-3	D13	
B-3	D12	
A-4	D01	
B-4	D00	
A-5	D03	
B-5	D02	
A-6	N.C	—
B-6	N.C	—
A-7	CKD	Data transfer clock
B-7	GND	Ground
A-8	H.D	Horizontal sync. signal
B-8	GND	Ground
A-9	V.D	Vertical sync. signal
B-9	GND	Ground
A-10	GND	Ground
B-10	GND	Ground
A-11	V_D	+24V
B-11	V_D	+24V
A-12	V_L	+5V
B-12	V_L	+5V
A-13	N.C	—
B-13	N.C	—

Interface Timing Ratings

(Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit
Clock frequency	1/t1	8	—	13	MHz
Clock duty	t1(H)/t1 × 100	45	—	55	%
Data setup time	t2	10	—	—	nsec
Data hold time	t3	10	—	—	nsec
H.D hold time	t4	10	—	—	nsec
H.D setup time	t5	10	—	—	nsec
Horizontal sync. signal cycle time	t6	34	—	41.3	μsec
Horizontal sync. signal valid time (Valid time of display data)	t7		320 × t1		μsec
Horizontal sync. signal blanking time (Invalid time of display data)	t8	1.3	—	—	μsec
Frame frequency	1/t9	50	—	60	Hz
Vertical sync. signal valid time	t10	t6	480 × t6	—	μsec
Vertical sync. signal blanking time	t11	1	—	t9 − t6	μsec
Vertical sync. signal rise wait time	t12	4 × 34	—	—	μsec
Vertical sync. rise timing	t13	34	—	t7+29	μsec

Connector



Interface Timing Chart