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	AVC LIQUID CRYSTAL DISPLAY GROUP SHARP CORPORATION **SPECIFICATION**	APPLICABLE GROUP AVC LIQUID CRYSTAL DISPLAY GROUP

DEVICE SPECIFICATION FOR

TFT-LCD module

MODEL No. LK315T3LA6M

CUSTOMER'S APPROVAL

DATE _____

BY _____

PRESENTED

BY T. Suzuki

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SHARP CORPORATION

RECORDS OF REVISION

MODEL No. : LK315T3LA6M

SPEC No. : LD-K21723

DATE	NO.	REVISED No.	PAGE	SUMMARY	NOTE
2009.8.5	LD-K21723	-	-	-	1st Issue

1. Application

This specification applies to the color 31.5" Wide XGA TFT-LCD module LK315T3LA6M.

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2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit, power supply circuit, inverter circuit and back light system etc. Graphics and texts can be displayed on a 1366×RGB×768 dots panel with 16,777,216 colors by using LVDS (Low Voltage Differential Signaling) to interface, +12V of DC supply voltages.

This module also includes the DC/AC inverter to drive the CCFT. (+24V of DC supply voltage)

And in order to improve the response time of LCD, this module applies the Over Shoot driving (O/S driving) technology for the control circuit .In the O/S driving technology, signals are being applied to the Liquid Crystal according to a pre-fixed process as an image signal of the present frame when a difference is found between image signal of the previous frame and that of the current frame after comparing them.

By using the captioned process, the image signals of this LCD module are being set so that image response can be completed within one frame, as a result, image blur can be improved and clear image performance can be realized.

3. Mechanical Specifications

Parameter	Specifications	Unit
Display size	80.039 (Diagonal)	cm
	31.5 (Diagonal)	inch
Active area	697.69 (H) × 392.26 (V)	mm
Pixel Format	1366 (H) × 768 (V) (1pixel = R + G + B dot)	pixel
Pixel pitch	0.51075(H) × 0.51075 (V)	mm
Pixel configuration	R,G, B vertical stripe	
Display mode	Normally black	
Unit Outline Dimensions (*1)	760.0(W) × 450.0(H) × 50.0max(D)	mm
Mass	5.1	kg
Surface treatment	Low-Haze Anti Glare Hard coating: 3H	

(*1) Outline dimensions are shown in Fig.1

4. Input Terminals

4-1. TFT panel driving

CN1 (Interface signals and +12V DC power supply) (Shown in Fig.1)

Using connector : FI-RE51S-HF (Japan Aviation Electronics Ind., Ltd.)

Matching connector : FI-RE51HL, FI-RE51CL (Japan Aviation Electronics Ind., Ltd.) or equivalent connector

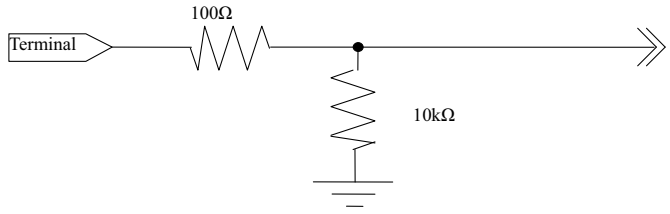
Matching LVDS transmitter : THC63LVD1023 or equivalent device

Pin No.	Symbol	Function	Remark
1	GND		
2	Reserved	It is required to set non-connection(OPEN)	
3	Reserved	It is required to set non-connection(OPEN)	
4	Reserved	It is required to set non-connection(OPEN)	
5	Reserved	It is required to set non-connection(OPEN)	
6	Reserved	It is required to set non-connection(OPEN)	
7	Reserved	It is required to set non-connection(OPEN)	[Note 3]
8	Reserved	It is required to set non-connection(OPEN)	
9	Reserved	It is required to set non-connection(OPEN)	
10	FRAME	Frame frequency setting 1:60Hz 0:50Hz	Pull down : GND [Note 1,2]
11	GND		
12	AIN0-	Aport (-)LVDS CH0 differential data input	LVDS
13	AIN0+	Aport (+)LVDS CH0 differential data input	LVDS
14	AIN1-	Aport (-)LVDS CH1 differential data input	LVDS
15	AIN1+	Aport (+)LVDS CH1 differential data input	LVDS
16	AIN2-	Aport (-)LVDS CH2 differential data input	LVDS
17	AIN2+	Aport (+)LVDS CH2 differential data input	LVDS
18	GND	GND	
19	ACK-	Aport LVDS Clock signal(-)	LVDS
20	ACK+	Aport LVDS Clock signal(+)	LVDS
21	GND	GND	
22	AIN3-	Aport (-)LVDS CH3 differential data input	LVDS
23	AIN3+	Aport (+)LVDS CH3 differential data input	LVDS
24	Open	It is required to set non-connection(OPEN)	
25	Open	It is required to set non-connection(OPEN)	
26	GND	GND	
27	GND	GND	
28	BIN0-	Bport (-)LVDS CH0 differential data input	LVDS
29	BIN0+	Bport (+)LVDS CH0 differential data input	LVDS
30	BIN1-	Bport (-)LVDS CH1 differential data input	LVDS
31	BIN1+	Bport (+)LVDS CH1 differential data input	LVDS
32	BIN2-	Bport (-)LVDS CH2 differential data input	LVDS
33	BIN2+	Bport (+)LVDS CH2 differential data input	LVDS
34	GND	GND	
35	BCK-	Bport LVDS Clock signal(-)	LVDS
36	BCK+	Bport LVDS Clock signal(+)	LVDS
37	GND	GND	
38	BIN3-	Bport (-)LVDS CH3 differential data input	LVDS
39	BIN3+	Bport (+)LVDS CH3 differential data input	LVDS
40	Open	It is required to set non-connection(OPEN)	
41	Open	It is required to set non-connection(OPEN)	
42	GND	GND	
43	GND	GND	
44	GND	GND	
45	GND	GND	
46	GND	GND	
47	VCC	+12V Power Supply	

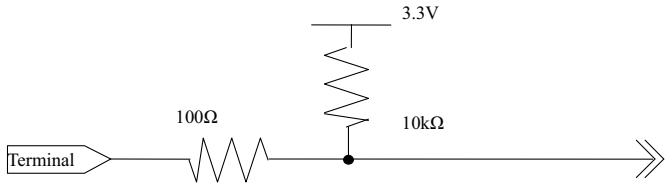
48	VCC	+12V Power Supply	
49	VCC	+12V Power Supply	
50	VCC	+12V Power Supply	
51	VCC	+12V Power Supply	

[note] GND of a liquid crystal panel drive part has connected with a module chassis.

[Note 1] The equivalent circuit figure of the terminal



[Note 2] The equivalent circuit figure of the terminal



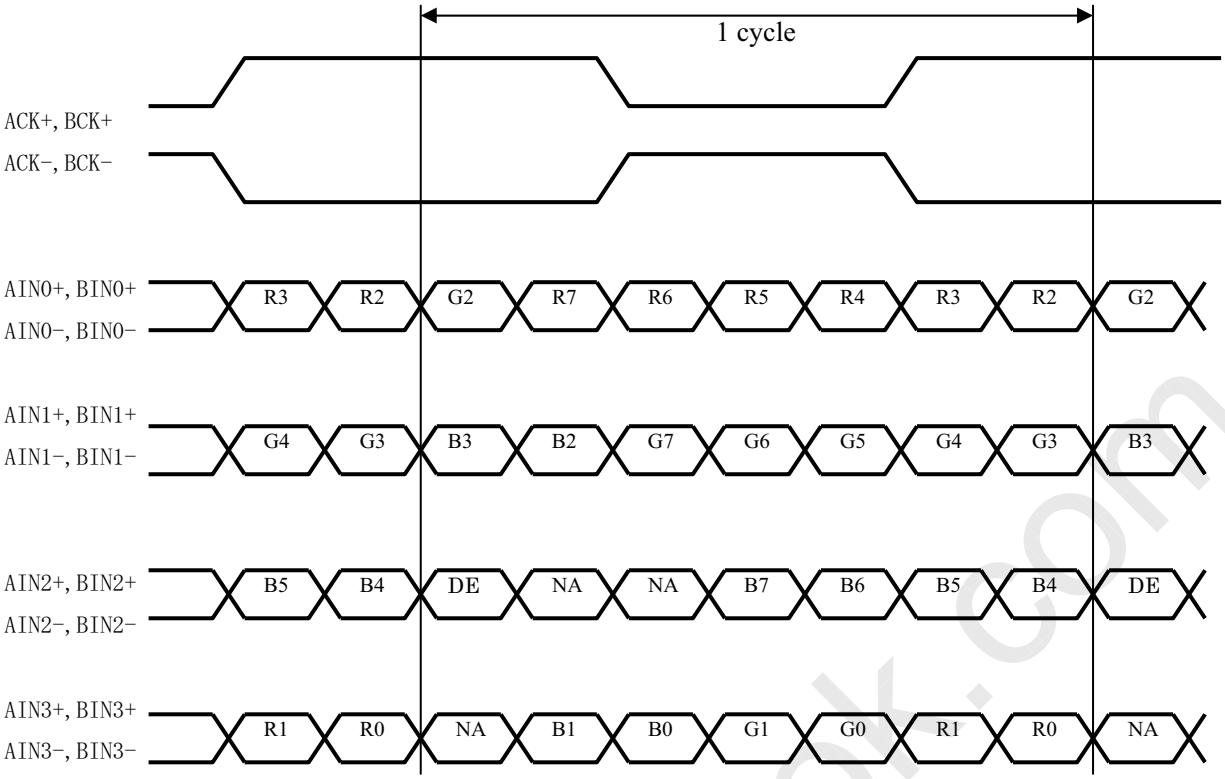
[Note 3] LVDS Data order

SELLVDS	
Data	Open
TA0	R2
TA1	R3
TA2	R4
TA3	R5
TA4	R6
TA5	R7(MSB)
TA6	G2
TB0	G3
TB1	G4
TB2	G5
TB3	G6
TB4	G7(MSB)
TB5	B2
TB6	B3
TC0	B4
TC1	B5
TC2	B6
TC3	B7(MSB)
TC4	NA
TC5	NA
TC6	DE(*)
TD0	R0(LSB)
TD1	R1
TD2	G0(LSB)
TD3	G1
TD4	B0(LSB)
TD5	B1
TD6	NA
TA0	R2

NA: Not Available

(*)Since the display position is prescribed by the rise of DE(Display Enable)signal, please do not fix DE signal during operation at ”High”.

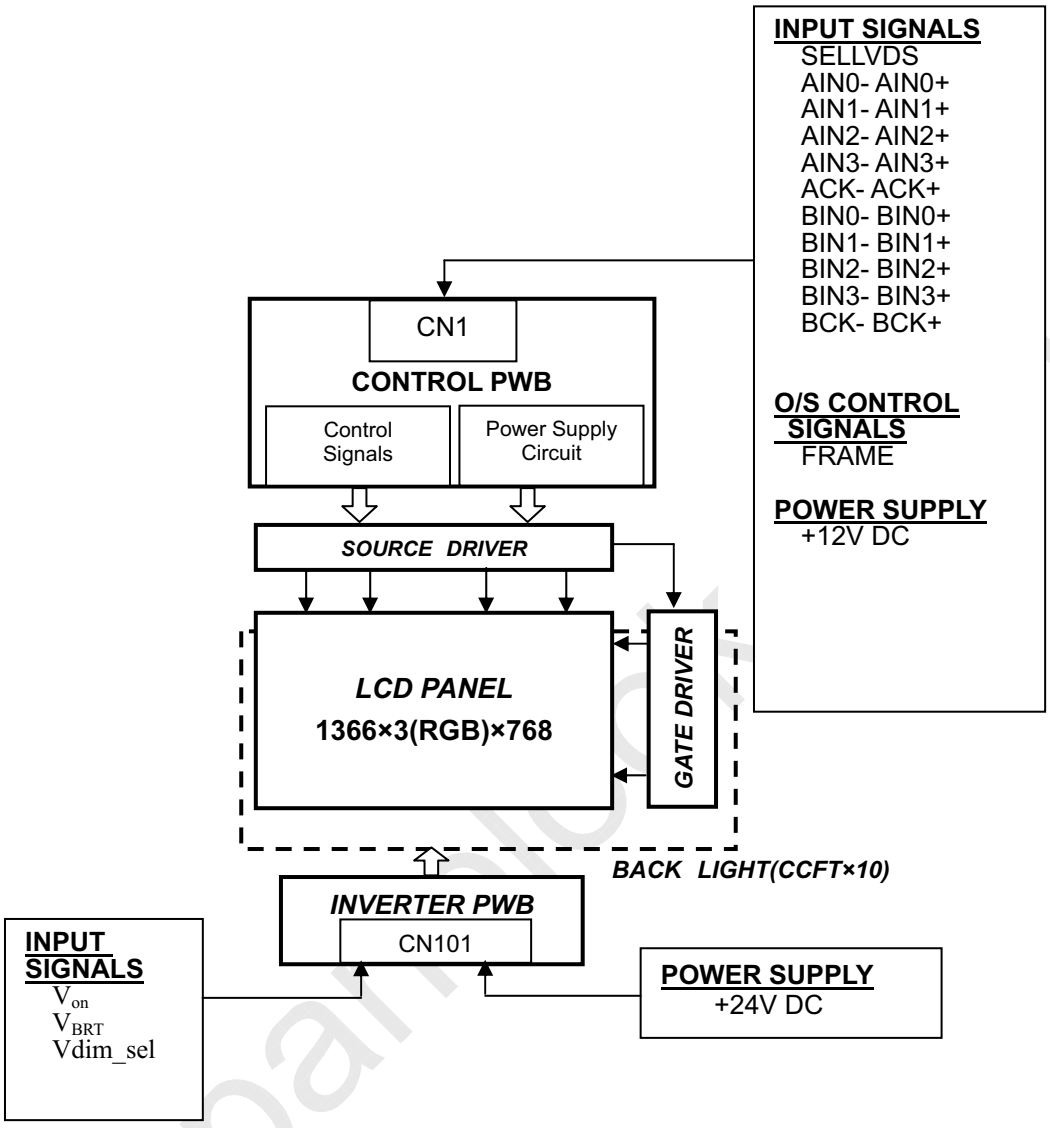
SELLVDS= Pin No.7 Open



DE: Display Enable

NA: Not Available (Fixed Low)

Interface block diagram



4-2. Backlight driving

CN101 (Inverter control)

Using connector: S14B-PHA-SM-TB(JST)

Matching connector: PHAR-14 (JST)

Pin No.	Symbol	Function	Default(OPEN)	Input Impedance	Remark
1	V_{INV}	+24V			
2	V_{INV}	+24V			
3	V_{INV}	+24V			
4	V_{INV}	+24V			
5	V_{INV}	+24V			
6	GND	Ground			
7	GND	Ground			
8	GND	Ground			
9	GND	Ground			
10	GND	Ground			
11	Reserved	-	Non connect (OPEN)	-	
12	Von	Inverter ON/OFF	Inverter OFF	42 kΩ	[Note 1]
13	V_{BRT}	Brightness Control	3.3V : pull up Duty 100%	135 kΩ	[Note 3]
14	Vdim_sel	PWM selection	3.3V : pull up Selected Analog PWM	80 kΩ	[Note 2]

[Note 1] Inverter ON/OFF

Input voltage	Function
3.3V	Inverter: ON
0V	Inverter: OFF (Default)

[Note 2] PWM selection

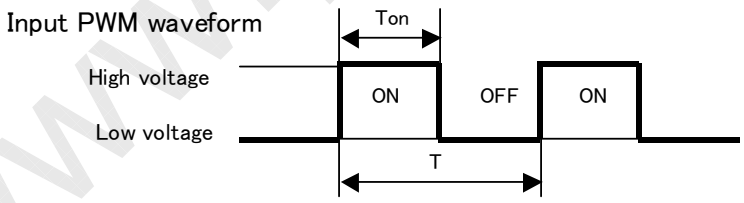
Pin No.14 is used for the selection of dimming control for V_{BRT} pin (Pin No.13).

Input voltage	V_{BRT}
0V	Pulse PWM
3.3V	Analog PWM

[Note 3] Brightness Control (Pulse PWM Dimming)

1. Pulse PWM Dimming

Pin No.13 is used for the control of the PWM duty with input pulse from 100Hz to 200Hz.



High : 2.3~3.6V ／ Low : 0~1.0V

Ta=25°C

		MIN	TYP	MAX	Remark
Pulse signal	[Hz]	100	120	200	
DUTY (T_{ON}/T)	[%]	25	<->	100	
Dimming level (Brightness ratio)	[%]	10	<->	100	Pulse signal=120Hz

[Note] Dimming level is reference value.

2. Analog PWM Dimming

Pin No.13 is used for the dimming control with input voltage from 0 to 3.15V

(when Analog PWM is selected with Pin 14.) Ta=25°C

	MIN	TYP	MAX	Function
Input voltage [V]	0	<->	3.15	0V: Dark - 3.15V: Bright
Brightness ratio [%]	10	<->	100	

[Note] PWM frequency : 165±10Hz

4-3. The back light system characteristics

The back light system is direct type with 10 CCFTs (Cold Cathode Fluorescent Tube).

The characteristics of the lamp are shown in the following table.

The value mentioned below is at the case of one CCFT.

Item	Symbol	Min.	Typ.	Max.	Unit	Remarks
Life time	T_L	-	60000	-	Hour	Duty= 100% [Note]
		25000	-	-		10% of total operation time: 10% dimming 90% of total operation time: more than 20% dimming [Note]

[Note] ・Lamp life time is defined as the time when brightness becomes 50% of the original value in the continuous operation under the condition of Ta=25°C and brightness control (V_{BRT} 100%).

・Above value is applicable when the long side of LCD module is placed horizontally. (Landscape position). (Lamp lifetime may vary if LCD module is in portrait position due to the change of mercury density inside the lamp.)

5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Input voltage (for Control)	V_I	Ta=25°C	-0.3 ~ 3.6	V	[Note 1]
+12V supply voltage (for Control)	V_{CC}	Ta=25°C	0 ~ +15	V	
Input voltage (for Inverter)	V_{BRT} V_{ON}	Ta=25°C	0 ~ +6	V	
+24V supply voltage (for Inverter)	V_{INV}	Ta=25°C	0 ~ +29	V	
Storage temperature	Tstg	-	-25 ~ +60	°C	[Note 2]
Operation temperature (Ambient)	Topa	-	0 ~ +50	°C	

[Note 1] SELLVDS

[Note 2] Humidity 95%RH Max.(Ta ≤ 40°C)

Maximum wet-bulb temperature at 39°C or less.(Ta > 40°C), No condensation.

6. Electrical Characteristics

6-1. Control circuit driving Ta=25°C

Parameter		Symbol	Min.	Typ.	Max.	Uniit	Remark
+12V supply voltage	Supply voltage	Vcc	11.4	12	12.6	V	[Note 1]
	Current dissipation	Icc	-	530	950	mA	[Note 2]
		I_{RUSH}	-	1000	-	mA	[Note 6]
		T_{RUSH}	-	5.0	-	ms	[Note 6]
Permissible input ripple voltage		V_{RP}	-	-	100	mV_{P-P}	Vcc = +12.0V
Differential input threshold voltage	High	V_{TH}	-	-	100	mV	V_{CM} = +1.2V
	Low	V_{TL}	-100	-	-	mV	[Note 5]
Input Low voltage		V_{IL}	-	-	0.7	V	[Note 3] [Note 4]
Input High voltage		V_{IH}	2.6	3.3	3.6	V	
Input leak current (Low)		I_{IL}	-	-	400	µA	V_I = 0V [Note 3]
Input leak current (High)		I_{IH}	-	-	100	µA	V_I =3.3V [Note 3]
Input leak current (Low)		I_{IL}	-	-	100	µA	V_I = 0V [Note 4]
Input leak current (High)		I_{IH}	-	-	400	µA	V_I =3.3V [Note 4]
Terminal resistor		R_T	-	100	-	Ω	Differential input

[Note] V_{CM}: Common mode voltage of LVDS driver.

[Note 1]

Input voltage sequences

0 < t1 ≤ 10ms

0< t2-1 ≤ 20ms

t2-2 ≥ 20ms

0 < t3 ≤ 1s

t4 ≥ 1s

t5 ≥ 1s

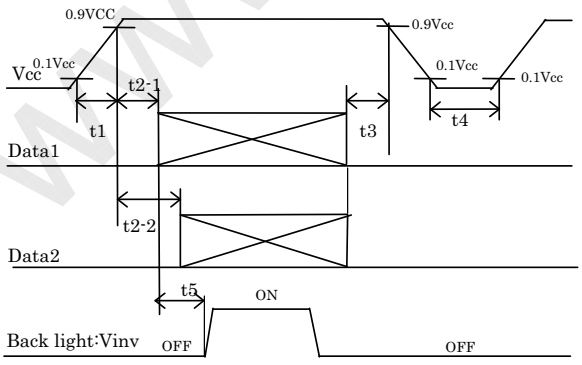
Dip conditions for supply voltage

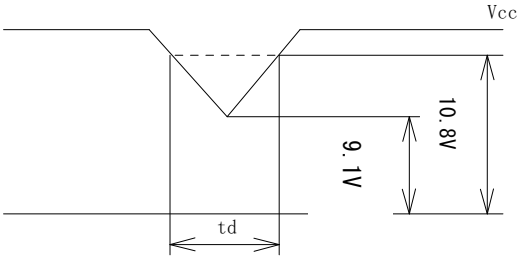
a) 9.1V ≤ Vcc < 10.8V

td ≤ 10ms

b) Vcc < 9.1V

Dip conditions for supply voltage is based on input voltage sequence.





※ Data1: ACK±, AIN0±, AIN1±, AIN2±, AIN3±, BCK±, BIN0±, BIN1±, BIN2±, BIN3±

*V_{CM} voltage pursues the sequence mentioned above

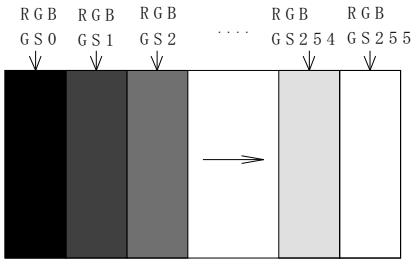
※ Data2: SELLVDS, FRAME

※ About the relation between data input and back light lighting, please base on the above-mentioned input sequence.

When back light is switched on before panel operation or after a panel operation stop, it may not display normally. But this phenomenon is not based on change of an incoming signal, and does not give damage to a liquid crystal display.

[Note 2]Typical current situation: 256 gray-bar pattern (Vcc = +12.0V)

The explanation of RGB gray scale is seen in section 8.



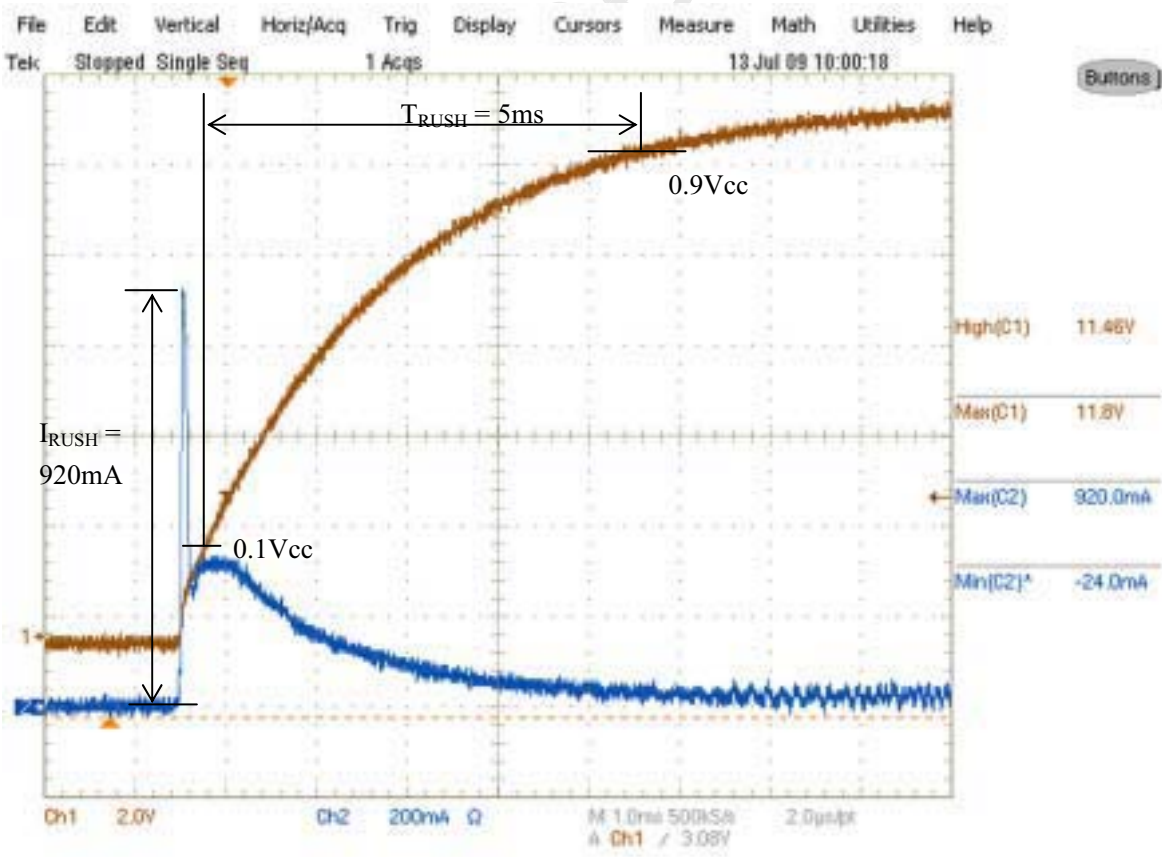
Vcc = +12.0V
CK = 73.3MHz
Th = 10.42μs

[Note 3] SELLVDS

[Note 4] FRAME

[Note 5] ACK±, AIN0±, AIN1±, AIN2±, AIN3±, BCK±, BIN0±, BIN1±, BIN2±, BIN3±

[Note 6] The Rush current corrugation at the time of power on(the power risetime is 5ms.)

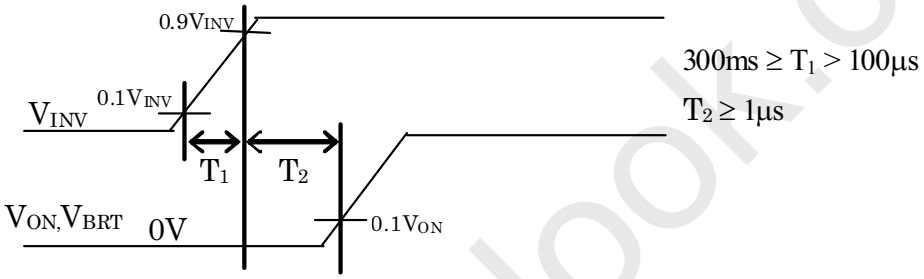


6-2. Inverter driving for back light

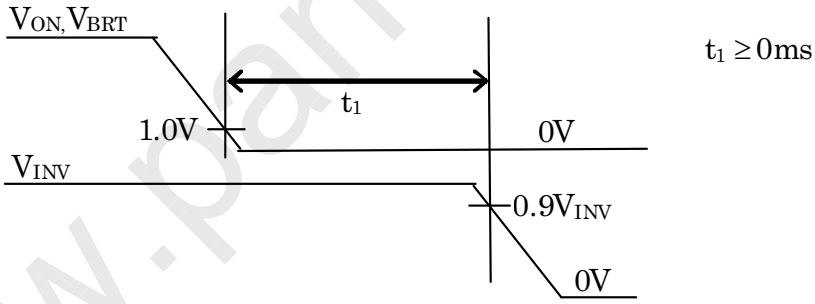
The back light system is direct type with 10 CCFTs (Cold Cathode Fluorescent Tube). Ta=25°C

Parameter		Symbol	Min.	Typ.	Max.	Unit	Remark
+24V	Current dissipation 1	$I_{INV\,1}$	-	4.5	5.0	A	Von = 3.3V V_{INV} = 24V V_{BRT} = 3.15V [Note 1,2]
	Current dissipation 2	$I_{INV\,2}$		3.4	3.74	A	
	Supply voltage	V_{INV}	22.5	24.0	25.5	V	
Permissible input ripple voltage		V_{RF}	-	-	800	mV_{p-p}	V_{INV} = 24V
Input voltage (Low)		V_{ONL}	0	-	1.0	V	Von Impedance = 42 kΩ min
Input voltage (High)		V_{ONH}	2.3	3.3	3.6	V	
Brightness control voltage		V_{BRT}	0	→	3.6	V	V_{BRT} Impedance = 135 kΩ min
Brightness control voltage vs Brightness level (Reference value)			0	→	3.15	V	
			10	→	100	%	

[Note 1] 1) V_{INV}-turn-on condition



2) V_{INV}-turn-off condition



[Note 2] Current dissipation 1 : Definition within 60 minutes after turn on. (Rush current is excluded.)

Current dissipation 2 : Definition more than 60minutes after turn on.

[Note 3] The inverter unit is driving at the following drive frequency.

Lamp driving frequency : 41kHz

Burst dimmer frequency : 165Hz

There is possibility that the display problem of the backlights such as flicker, blinking, etc by the interference of the above inverter driving frequency and the LCD driving frequency will occur.

In setting of a LCD driving frequency, we recommend to set for the no interference with the above frequency to occur.

7. Timing characteristics of input signals

7-1. Timing characteristics

Timing diagrams of input signal are shown in Fig.2

Parameter		Symbol	Min.	Typ.	Max.	Unit
Clock	Frequency	1/Tc	65	73.3	85	MHz
Data enable signal	Horizontal period	TH	720	764	970	clock
			10.33	10.42	—	μs
	Horizontal period (High)	THd	683	683	683	clock
	Vertical period	TV	778	801	972	line
	Vertical period (High)	TVd	768	768	768	line

[Note] When vertical period is very long, flicker may occur.

Please turn off the module after it shows the black screen.

Please make sure that length of vertical period should become of an integral multiple of horizontal length of period. Otherwise, the screen may not display properly.

As for your final setting of driving timing, we will conduct operation check test at our side, please inform your final setting.

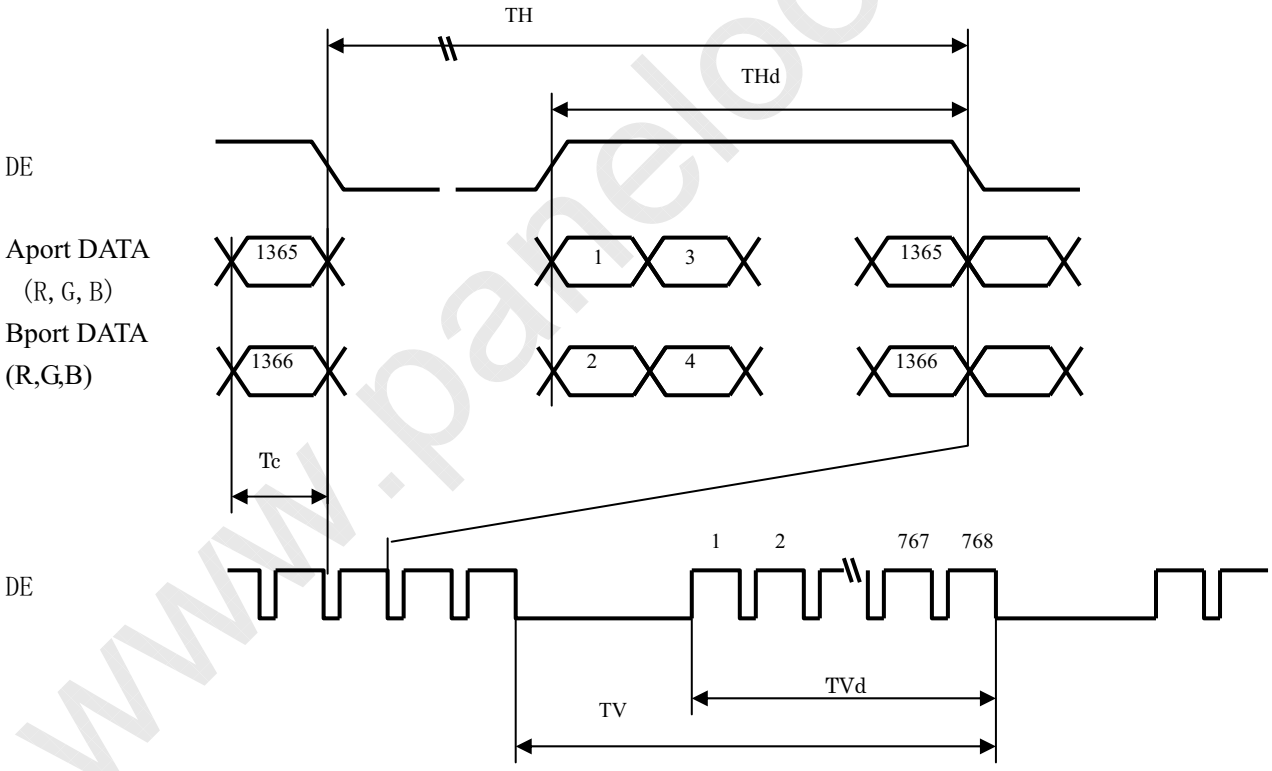
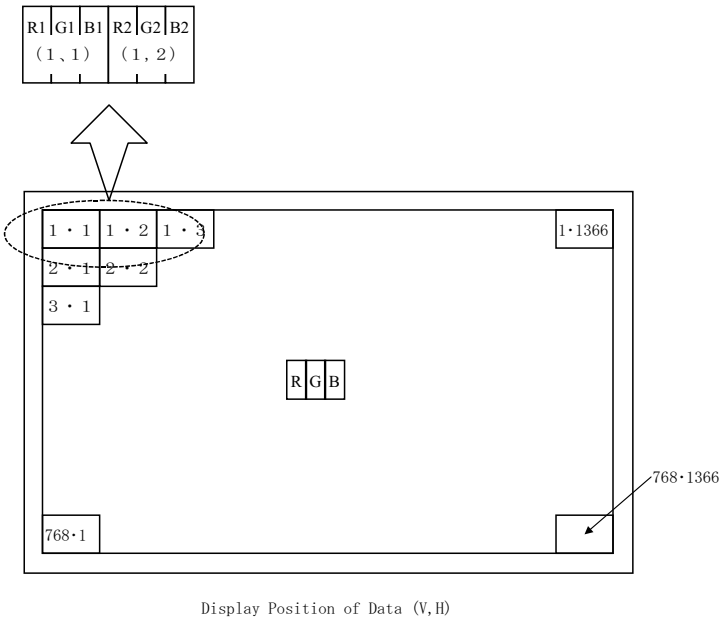


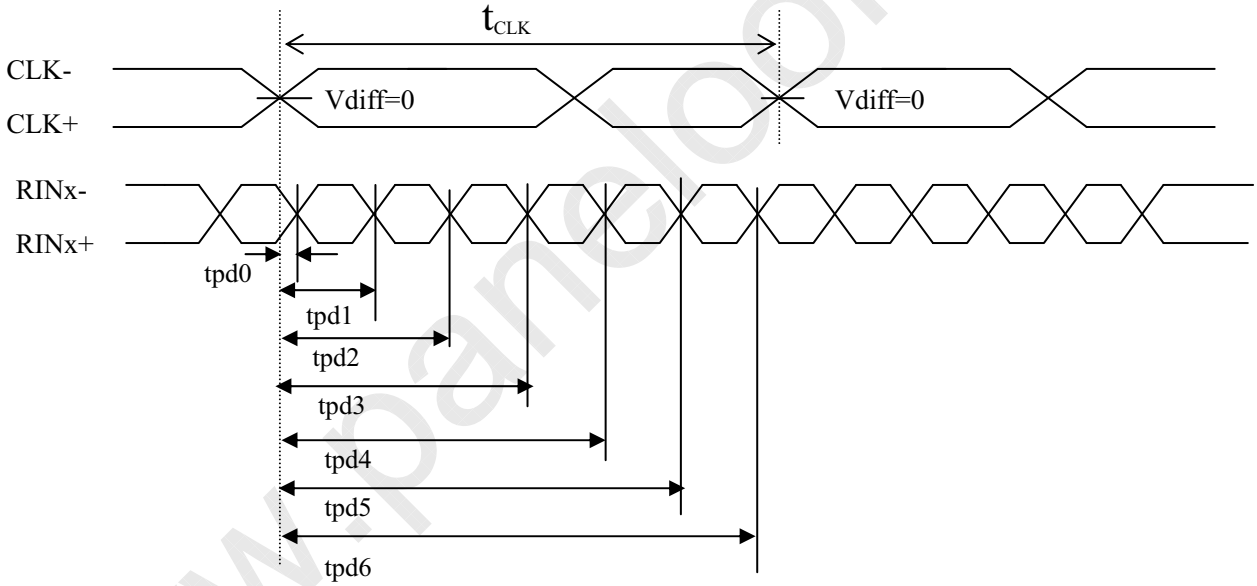
Fig.2 Timing characteristics of input signals

7-2. Input data signal and display position on the screen



Display Position of Data (V,H)

7-3. LVDS signal characteristics



The item		Symbol	min.	typ.	max.	unit
Data position	Delay time, CLK rising edge to serial bit position 0	tpd0	-0.25	0	0.25	ns
	Delay time, CLK rising edge to serial bit position 1	tpd1	1*t_{CLK}/7-0.25	1* t_{CLK} /7	1* t_{CLK} /7+0.25	
	Delay time, CLK rising edge to serial bit position 2	tpd2	2* t_{CLK} /7-0.25	2* t_{CLK} /7	2* t_{CLK} /7+0.25	
	Delay time, CLK rising edge to serial bit position 3	tpd3	3* t_{CLK} /7-0.25	3* t_{CLK} /7	3* t_{CLK} /7+0.25	
	Delay time, CLK rising edge to serial bit position 4	tpd4	4* t_{CLK} /7-0.25	4* t_{CLK} /7	4* t_{CLK} /7+0.25	
	Delay time, CLK rising edge to serial bit position 5	tpd5	5* t_{CLK} /7-0.25	5* t_{CLK} /7	5* t_{CLK} /7+0.25	
	Delay time, CLK rising edge to serial bit position 6	tpd6	6* t_{CLK} 7-0.25	6* t_{CLK} /7	6* t_{CLK} /7+0.25	

8. Input Signal, Basic Display Colors and Gray Scale of Each Color

	Colors & Gray scale	Gray Scale	Data signal																							
			R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7
Basic Color	Black	—	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	—	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	—	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan	—	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red	—	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	—	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	—	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	—	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	↓				↓								↓								↓				
	⇩	↓				↓								↓								↓				
	Brighter	GS253	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇩	GS254	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Green	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	GS1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	↓				↓								↓								↓				
	⇩	↓				↓								↓								↓				
	Brighter	GS253	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	⇩	GS254	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Green	GS255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale of Blue	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	⇧	↓				↓								↓								↓				
	⇩	↓				↓								↓								↓				
	Brighter	GS253	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1
	⇩	GS254	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1
	Blue	GS255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

0 : Low level voltage, 1 : High level voltage.

Each basic color can be displayed in 256 gray scales from 8 bit data signals. According to the combination of total 24 bit data signals, the 16,777,216 colors display can be achieved on the screen.

9. Optical characteristics

Ta = 25°C, Vcc = +12V, V_{INV} = +24V

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing angle range	Horizontal	θ 21 θ 22	CR ≥ 10	70	88	-	Deg.	[Note1,4]
	Vertical	θ 11 θ 12		70	88	-	Deg.	
Contrast ratio		CRn	θ =0 deg.	2000	2500	-	-	[Note2,4] V_{BRT}=3.3V
Response time		τ_{DRV}		-	4	-	ms	[Note3,4,5] V_{BRT}=3.3V
Chromaticity of white		x		0.250	0.280	0.310	-	[Note 4] V_{BRT}=3.3V
		y		0.259	0.289	0.319	-	
Chromaticity of red		x		0.611	0.641	0.671	-	
		y		0.314	0.344	0.374	-	
Chromaticity of green		x		0.251	0.281	0.311	-	
		y		0.575	0.605	0.635	-	
Chromaticity of blue		x		0.114	0.144	0.174	-	
		y		0.043	0.073	0.103	-	
Luminance of white		Y_L		360	450		cd/m^2	[Note 4] V_{BRT}=3.3V
Luminance uniformity		δ w		-	-	1.25		[Note 6]

Measurement condition : Set the value of V_{BRT} to maximum luminance of white.

*The measurement shall be executed 60 minutes after lighting at rating.

[Note] The optical characteristics are measured using the following equipment.

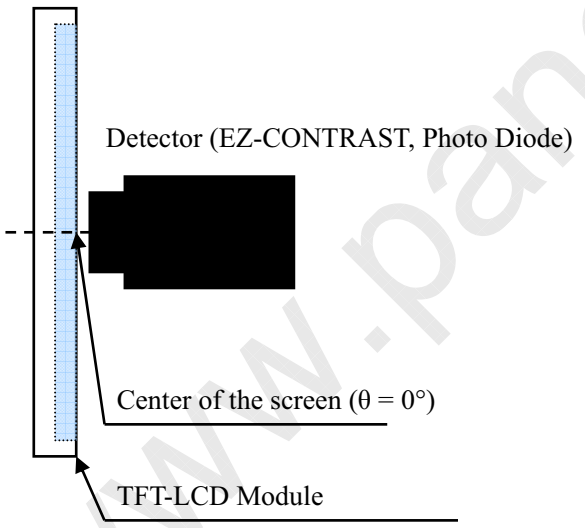


Fig.3-1 Measurement of viewing angle range and response time.
(Viewing angle range: EZ-CONTRAST
Response time: Photo Diode)

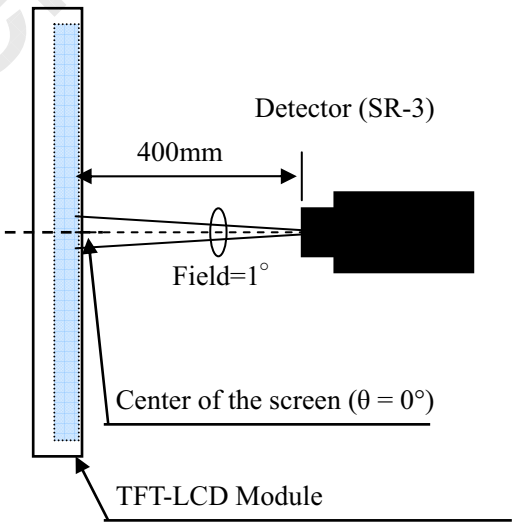
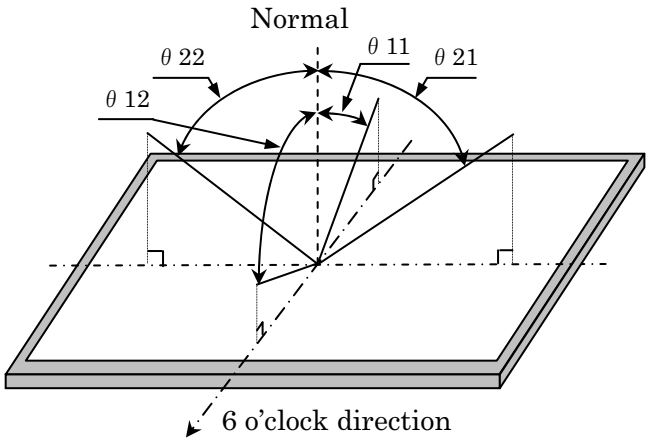


Fig.3-2 Measurement of Contrast, Luminance, and Chromaticity.

[Note 1] Definitions of viewing angle range :



[Note 2] Definition of contrast ratio :

The contrast ratio is defined as the following.

$$\text{Contrast Ratio} = \frac{\text{Luminance (brightness) with all pixels white}}{\text{Luminance (brightness) with all pixels black}}$$

[Note 3] Definition of response time

The response time (τ_{DRV}) is defined as the following figure and shall be measured by switching the input signal for "any level of gray (0%, 25%, 50%, 75% and 100%)" and "any level of gray (0%, 25%, 50%, 75% and 100%)".

	0%	25%	50%	75%	100%
0%		tr: 0%-25%	tr: 0%-50%	tr: 0%-75%	tr: 0%-100%
25%	td: 25%-0%		tr: 25%-50%	tr: 25%-75%	tr: 25%-100%
50%	td: 50%-0%	td: 50%-25%		tr: 50%-75%	tr: 50%-100%
75%	td: 75%-0%	td: 75%-25%	td: 75%-50%		tr: 75%-100%
100%	td: 100%-0%	td: 100%-25%	td: 100%-50%	td: 100%-75%	

t*:x-y...response time from level of gray(x) to level of gray(y)

$\tau_{DRV} = \Sigma(t^*:x\text{-}y)/20$

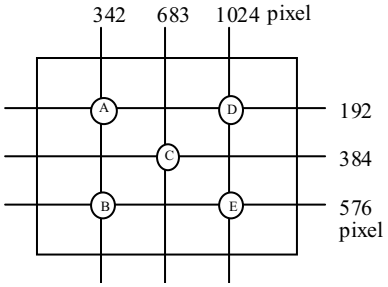
[Note 4] This shall be measured at center of the screen.

[Note 5] This value is valid when O/S driving is used at typical input time value .

[Note 6] Definition of white uniformity ;

White uniformity is defined as the following with five measurements. (A～E)

$$\delta_W = \frac{\text{Maximum luminance of five points (brightness)}}{\text{Minimum luminance of five points (brightness)}}$$



10. Handling Precautions of the module

a) Be sure to turn off the power supply when inserting or disconnecting the cable.

b) This product is using the parts (inverter, CCFT etc), which generate the high voltage. Therefore, during operating, please don't touch these parts.

c) Brightness control voltage is switched for "ON" and "OFF", as shown in Fig.4. Voltage difference generated by this switching, ΔV_{INV}, may affect a sound output, etc. when the power supply is shared between the inverter and its surrounding circuit. So, separate the power supply of the inverter circuit with the one of its surrounding circuit.

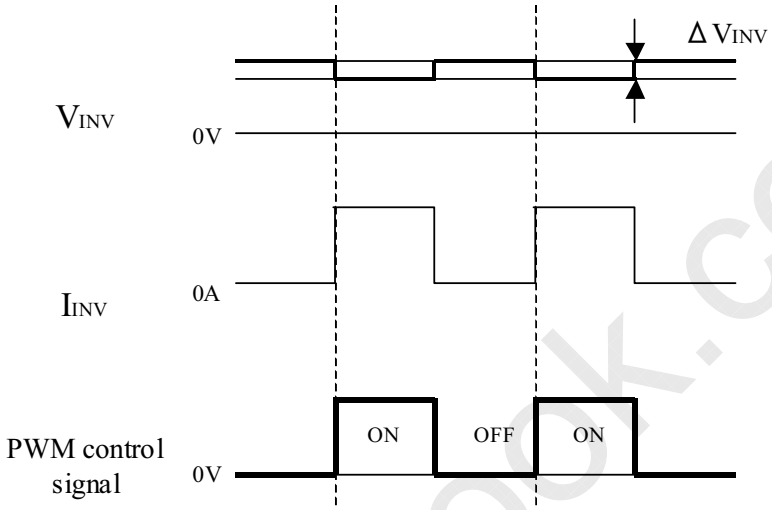


Fig.4 Brightness control voltage.

d) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.

e) Since the front polarizer is easily damaged, pay attention not to scratch it.

f) Since long contact with water may cause discoloration or spots, wipe off water drop immediately.

g) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.

h) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.

i) Since CMOS LSI is used in this module, take care of static electricity and take the human earth into consideration when handling.

j) Please consider to minimize the influence of EMI and the exogenous noise before designing the grounding of LCD module.

k) The module has some printed circuit boards (PCBs) on the back side, take care to keep them form any stress or pressure when handling or installing the module; otherwise some of electronic parts on the PCBs may be damaged.

l) Observe all other precautionary requirements in handling components.

m) When some pressure is added onto the module from rear side constantly, it causes display non-uniformity issue, functional defect, etc.. So, please avoid such design.

n) When handling LCD modules and assembling them into cabinets, please be noted that long-term storage in the environment of oxidization or deoxidization gas and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the LCD modules.

o) Connect a module frame to GND.

11. Packing form

a) Piling number of cartons: (2 packages / 1 palette) ×2 maximum
b) Packing quantity in one carton: (13) pcs.
c) Carton size: 1130 (W)×870 (D)×1158(H)
d) Total mass of one carton filled with full modules: (159) kg(Max)

12. Reliability test item

No.	Test item	Condition
1	High temperature storage test	Ta=60°C 240h
2	Low temperature storage test	Ta=-25°C 240h
3	High temperature and high humidity operation test	Ta=40°C ; 95%RH 240h (No condensation)
4	High temperature operation test	Ta=50°C 240h
5	Low temperature operation test	Ta=0°C 240h
6	Vibration test (non-operation)	Frequency: 10~57Hz/Vibration width (one side): 0.075mm : 58~500Hz/Acceleration: 9.8 m/s^2 Sweep time: 11 minutes Test period: 3 hours (1h for each direction of X, Y, Z)
7	Shock test (non-operation)	Maximum acceleration: 490m/s^2 Pulse width: 11ms, sinusoidal half wave Direction: +/-X, +/-Y, +/-Z, once for each direction.
8	ESD	* At the following conditions, it is a thing without incorrect operation and destruction. (1)Non-operation: Contact electric discharge ±10kV Non-contact electric discharge±20kV (2)Operation Contact electric discharge ±8kV Non-contact electric discharge ±15kV Conditions: 150pF、330ohm

[Result evaluation criteria]

Under the display quality test condition with normal operation state, there shall be no change, which may affect practical display function.

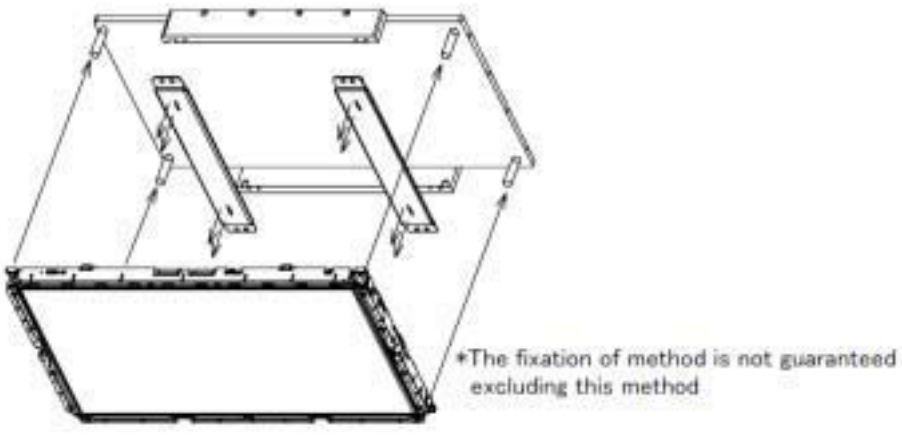


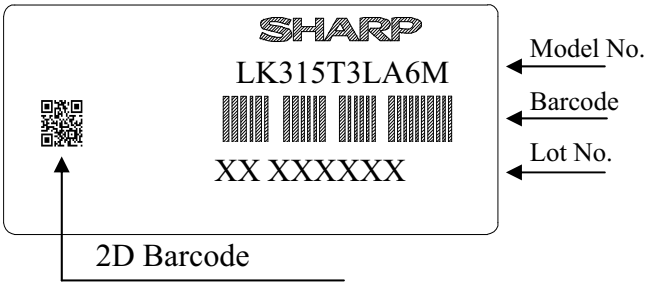
Figure of Shock test's jig
Module fixed position (M4 Bolt × 12)

13. Others

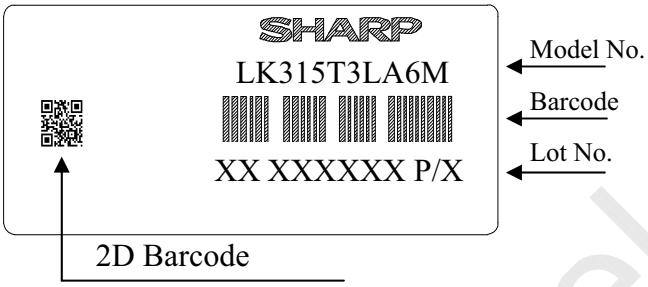
1) Lot No. Label ;

The label that displays SHARP, product model (LK315T3LA6M), a product number is stuck on the back of the module.

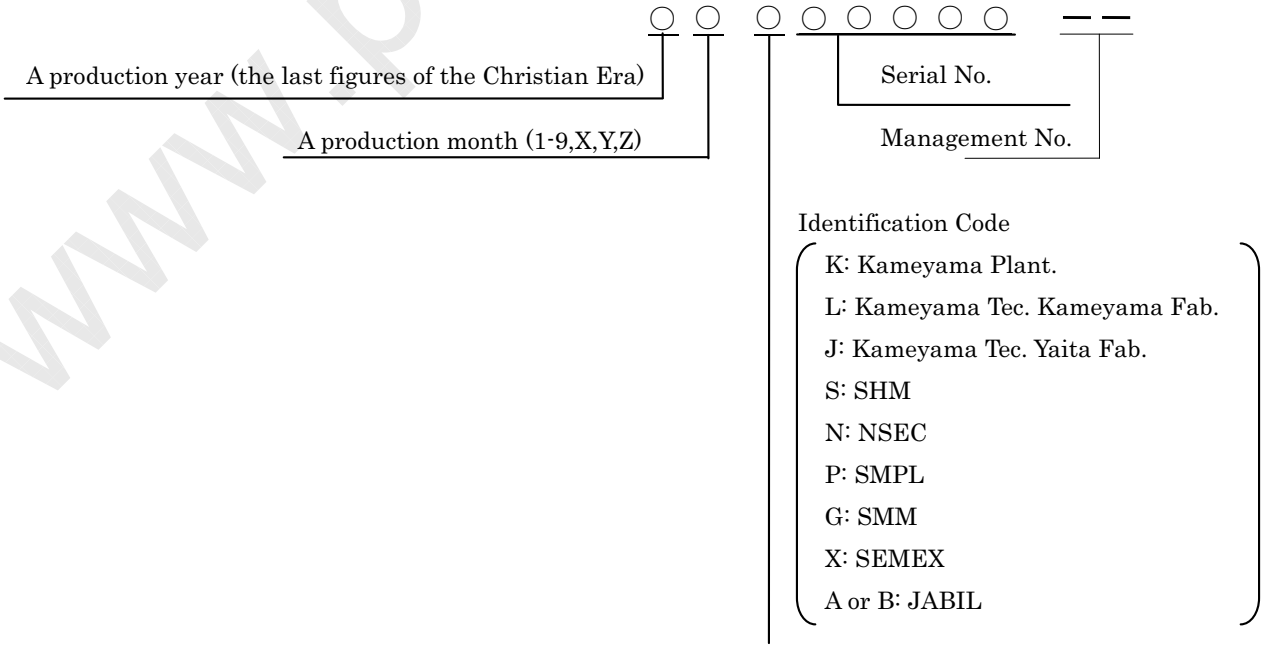
[LK315T3LA6M] JAPAN PRODUCTION



[LK315T3LA6MP/X] NSEC PRODUCTION



How to express Lot No.



2) Packing Label

[LK315T3LA6M] JAPAN PRODUCTION

社内品番 :(４Ｓ) LK315T3LA6M (①)

Bar code

ＬｏｔNO. ・(１Ｔ) ＊＊＊＊. ＊.＊＊ (②)

Bar code

Ｑｕａｎｔｉｔｙ:(Q) 13 ｐｃｓ (③)

Bar code

ユーザ品番

シャープ物流用ラベルです。

[LK315T3LA6MP/X] NSEC PRODUCTION

社内品番 :(４Ｓ) LK315T3LA6MP/X (①)

Bar code

ＬｏｔNO. ・(１Ｔ) ＊＊＊＊. ＊.＊＊ (②)

Bar code

Ｑｕａｎｔｉｔｙ:(Q) 13 ｐｃｓ (③)

Bar code

ユーザ品番

シャープ物流用ラベルです。

① Management No
② Lot No. (Date)
③ Quantity

3) Adjusting volume have been set optimally before shipment, so do not change any adjusted value.
If adjusted value is changed, the specification may not be satisfied.

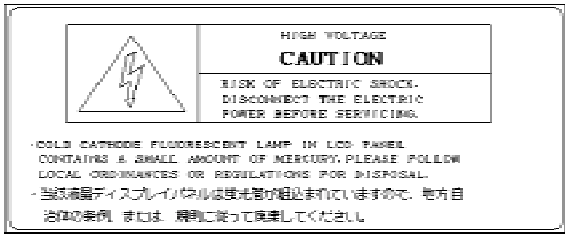
4) Disassembling the module can cause permanent damage and should be strictly avoided.

5) Please be careful since image retention may occur when a fixed pattern is displayed for a long time.

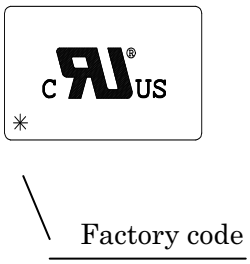
6) The chemical compound, which causes the destruction of ozone layer, is not being used.

7) Label of material information
The optical part material has been described to the module as shown in the figure below.

8) Cold cathode fluorescent lamp in LCD PANEL contains a small amount of mercury. Please follow local ordinances or regulations for disposal. The below figure shows the label.



9) When any question or issue occurs, it shall be solved by mutual discussion.
10) Rust on the module is not taken up a problem.
11) Control-PWB(C-PWB) must be on upper side of LCD module when it is in the TV-set.
*:Please inform SHARP if C-PWB is at bottom side of LCD module when it is in the TV-set
12) This module is corresponded to RoHS.
13) This LCD is appropriate to UL. Below figure shows the UL label.



14) When any question or issue occurs, it shall be solved by mutual discussion.

14. Carton storage condition

Temperature	0°C to 40°C
Humidity	95%RH or less
Reference condition	: 20°C to 35°C, 85%RH or less (summer)
	: 5°C to 15°C, 85%RH or less (winter)
	・the total storage time (40°C,95%RH) : 240H or less
Sunlight	Be sure to shelter a product from the direct sunlight.
Atmosphere	Harmful gas, such as acid and alkali which bites electronic components and/or wires must not be detected.
Notes	Be sure to put cartons on palette or base, don't put it on floor, and store them with removing from wall Please take care of ventilation in storehouse and around cartons, and control changing temperature is within limits of natural environment
Storage life	1 year

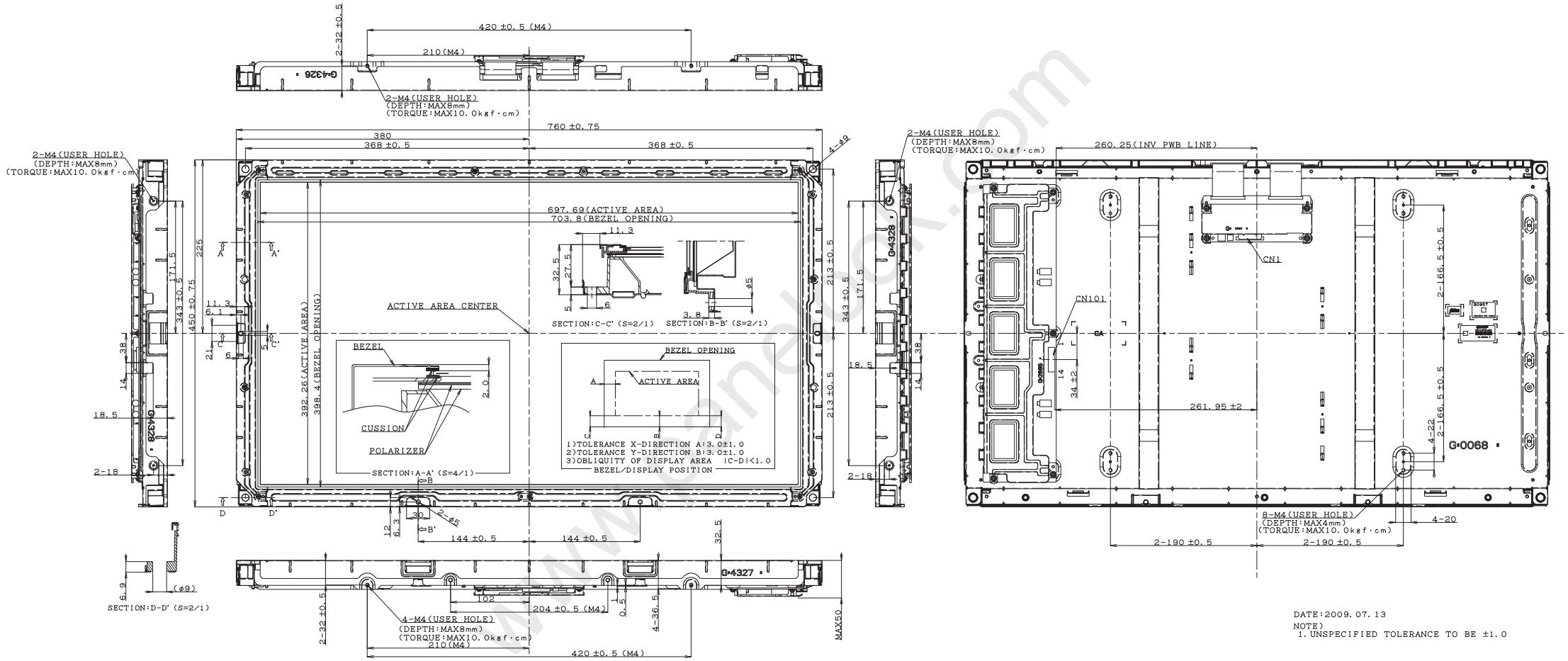
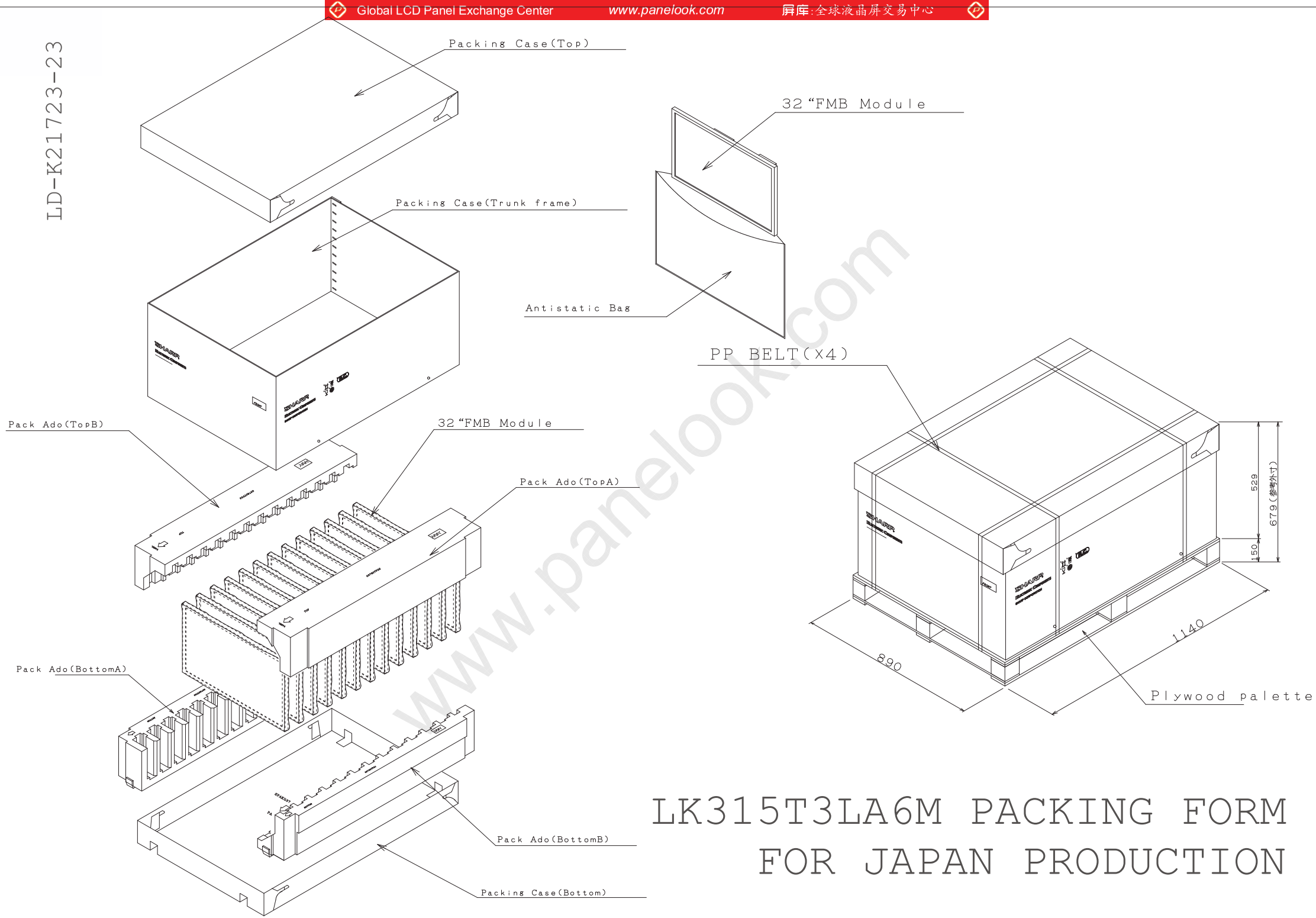


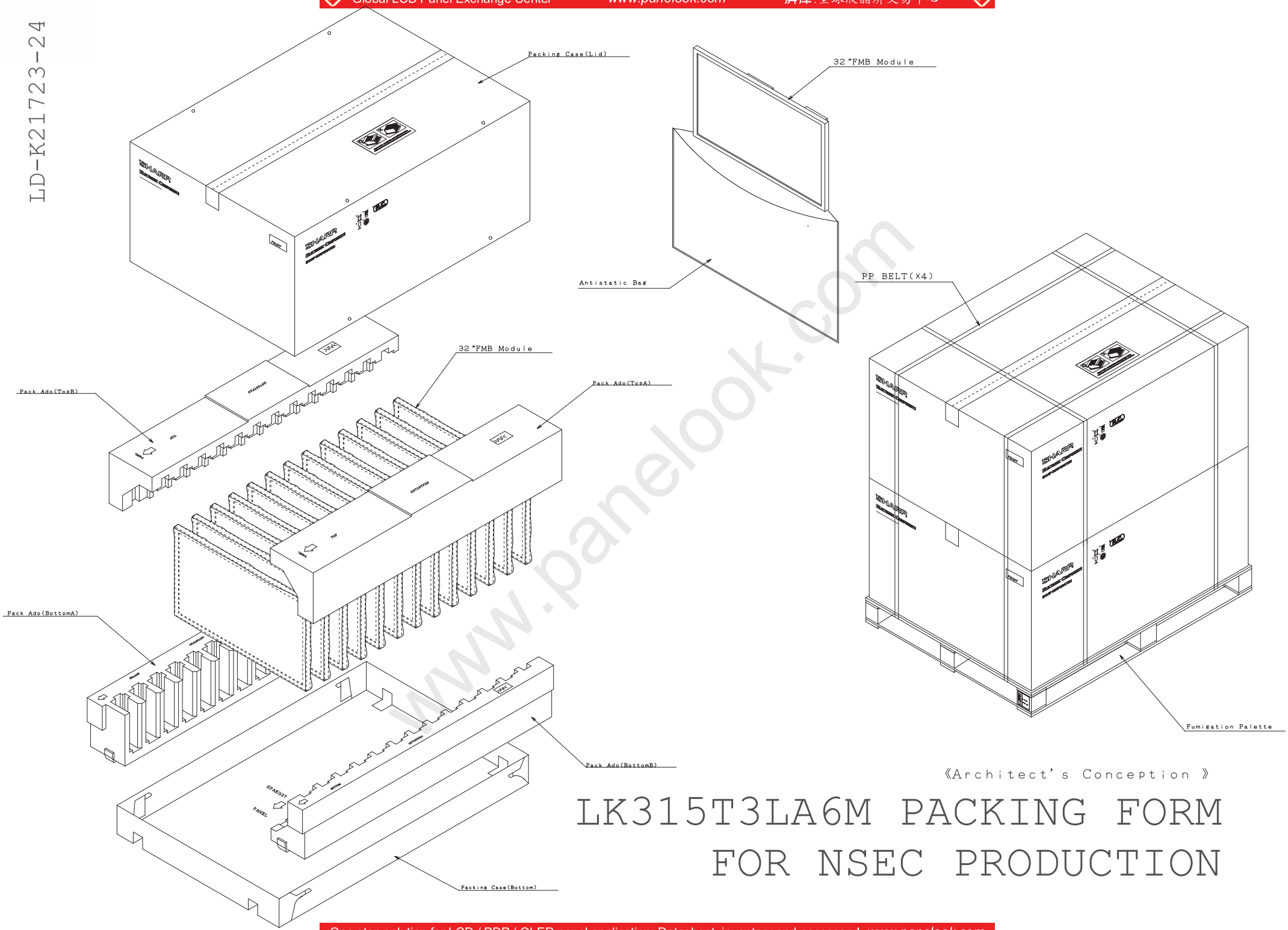
Fig.1 LK315T3LA6M OUTLINE DIMENSIONS

LD-K21723-23



LK315T3LA6M PACKING FORM
FOR JAPAN PRODUCTION

LD-K21723-24



《Architect's Conception》

LK315T3LA6M PACKING FORM FOR NSEC PRODUCTION