NAN YA PLASTICS CORPORATION

SPECIFICATION OF

LCD MODULE

PRODUCT NO. : LKBFBZX61M99S_

SPEC. NO. : LMX61-99-0

CUSTOMER
APPROVED BY
DATE:

EDITED ON : Dec. 21, 2007

LCD DEPARTMENT
ELECTRONIC MATERIALS DIVISION
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Q.C. DEPT.	DESIGN MANAGER	DESIGN CHECK	DESIGNER
			W.R.HSU

RECORDS OF REVISION				SPEC. NO. : LMX61-99	
DATE	REVISED NO.	REF. PAGE /TOTAL PAGE	SUMMARY	DESIGN	CHECK
12.21.07'	0	1~10/26	First Issue	W.R.HSU	

1.MECHANICAL DATA

NO.	ITEM	CONTENTS	UNIT
1	Product No.	LKBFBZX61M99S_	—
2	Module Size	154.6 (W) x 114.8 (H) x 9.0 (D)	mm
3	Dot Size	0.10 (W) x 0.34 (H)	mm
4	Dot Pitch	0.12 (W) x 0.36 (H)	mm
5	Number of Dots	320 RGB (W) x 240 (H)	Dot
6	Duty	1/240	—
7	LCD Display Mode	FSTN, Color STN Module	—
8	Rear Polarizer	Color Transmissive Type	—
9	Viewing Direction	6	O'clock
10	Backlight	CCFL	—
11	Controller	Excluded	—
12	DC/DC Converter	Included	—
13	Touch Panel	Excluded	—
14	Weight	200 (Approx.)	g

NOTE:

L K B F B Z X 6 1 M 99 S

(1) (2) (3) (4) (5) (6)

NO.	ITEM	SYMBOL	DEFINITION
(1)	Backlignt	B	CCFL Backlight
(2)	Reflective/Transmissive	Z	Transmissive
(3)	Mode/View Angle	M	Color STN Module, 6 O'clock
(4)	Option	99	Module Version Number
(5)		S	RoHS Compliance
(6)		T	Testing Sample

RoHS Compliance.

Nan Ya guarantees that this project doesn't include any materials (6 materials) or includes less than specified quantities which are regulated by RoHS Compliance.

2.ABSOLUTE MAXIMUM RATINGS

2-1.ELECTRICAL ABSOLUTE RATINGS VSS=0V

ITEM	SYMBOL	MIN.	MAX.	UNIT	COMMENT
Power Supply for Logic	VDD-VSS	-0.3	6	V	
Contrast Adjustment Voltage	VCONT-VSS	0	VDD	V	
Input Voltage	VI	-0.3	VDD	V	
Static Electricity	—	—	—	—	Note 1

Note 1 LCM should be grounded during handling LCM.

2-2.ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	NORMAL TEMP.			
	OPERATING		STORAGE	
	MIN.	MAX.	MIN.	MAX.
Ambient Temperature(℃)	0	50	-20	70
Humidity (Without Condensation)	Note 2,4		Note 3,4	

Note 2 Ta ≦ 50℃ : 80%RH MAX.

Note 3 Please refer to item of reliability test.

Note 4 Background color will change slightly depending on ambient temperature.
That phenomenon is reversible.

3.ELECTRICAL CHARACTERISTICS

3-1.ELECTRICAL CHARACTERISTICS OF LCI

ITEM	SYMBOL	CONDITION		MIN.	TYP.	MAX.	UNIT
Power Supply for Logic	VDD-VSS	—		3.0	3.3	3.6	V
				4.5	5.0	5.5	
Input Voltage	VIH	H Level		0.8VDD	—	VDD	V
	VIL	L Level		0	—	0.2VDD	
Contrast Adjustment Voltage	VCONT-VSS	Duty = 1/240	0℃ ~ 50℃	1.3	1.8	2.3	V
Power Supply Current (Ta=25℃)	IDD	VDD-VSS=3.3V VCON-VSS=1.8V Pattern: 		—	55.0	80.0	mA
		VDD-VSS=5.0V VCON-VSS=1.8V Pattern: 		—	30.0	50.0	
LCM Surface Luminance (Ta=25℃)	L	VDD-VSS = 3.3V/5.0V VCONT-VSS=1.8V IL=4mArms	Dots All On (White)	280	330	—	cd/m²
			Dots All Off (Black)	—	10	—	
Recommended Frame Frequency for Optimum Contrast	FLM	—		115	120	125	Hz

3-2.ELECTRICAL CHARACTERISTICS OF BACKLIC

Used Lamp Rating Ta=25℃

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	REMARK
Lamp Voltage	VL	—	740	—	Vrms	—
Lamp Current	IL	—	4	—	mArms	—
Lamp Power Consumption	PL	—	3	—	W	(*1)
Starting Voltage	VS	—	—	1020	Vrms	Ta=25℃
Lamp life time	LL	—	50000	—	Hrs	at IL=4 mArms Ta=25℃(*2)

(*1) Power consumption excluded inverter loss.

(*2) Lamp life time is defined as follows : The final brightness is at 50% of original brightness.

(*3) a. Please follow the table of lamp characteristics shown above if not to use the inverter tested by Nan Ya.

b. If customers want to design inverter by themselves, please inform Nan Ya to offer the detail lamp specification.

3-3.ELECTRICAL CHARACTERISTICS OF TESTED INVERTER

TDK TAD250

(If the inverter output "CP2" couldn't mating CCFL connector, please refer to specification "INTERNAL PIN CONNECTION" page to fit it.)

3-3-1 GENERAL SPECIFICATIONS

OPERATION TEMPERATURE : 0℃~50℃

STORAGE TEMPERATURE : -20℃~80℃

DIMENSION : 95.0(L)mm x 19.5(W)mm x MAX. 8.8(H)mm

3-3-2 PIN ASSIGNMENTS

INPUT(CP1) CONNECTOR :

MOLEX 53261-0590

NO.	FUNCTION
1	VIN
2	GND
3	Vrmt ON/OFF CONTROL
4	Vctrl
5	NC

OUTPUT(CN2) CONNECTOR :

MITSUMI M60-04-30-134P

NO.	FUNCTION
1	RTN
2	NC
3	NC
4	HV

3-3-3 RELATIONSHIP BETWEEN VIN & TUBE CURRENT

(1) Backlight measurement brightness based on the TDK TAD250 inverter.

(2) Test condition : Turn on the module CCFL backlight with TAD250.

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	REMARK
Input Voltage	VIN	10	12	15	V	
Control Terminal Input Voltage	Vrmt	3.5	5	10	V	ON State
		-0.5	0	0.4		OFF State
Tube Current Control Voltage	Vctrl	—	1.8	—	V	
Tube Current	IL	—	4	—	mA	

Note 1. Inverter must be used in the range of VIN Input Voltage.
If it doesn't used in this range, the electrical characteristics of backlight would not be to guarantee.

4.OPTICAL CHARACTERISTICS

4-1.Optical Char. of Normal Temp. Mode at Vop

ITEM / MODE		Cr(Contrast Ratio)						θ (Viewing Angle)		φ (Viewing Angle)	
		0 ℃		25 ℃		50 ℃		25 ℃		25 ℃	
		MIN.	TYP.	MIN.	TYP.	MIN.	TYP.	Viewing Directio	TYP.	Viewing Directio	TYP.
Z	M	23	35	25	40	5	8	6 O'Clock	48	9 O'Clock	45
								12 O'Clock	30	3 O'Clock	45
NOTE		NOTE 3,6						NOTE 3,5			

NOTE :

Z : Transmissive

M : Color STN Module, 6 O'clock

at $\varphi=0^{\circ}, \theta=0^{\circ}$

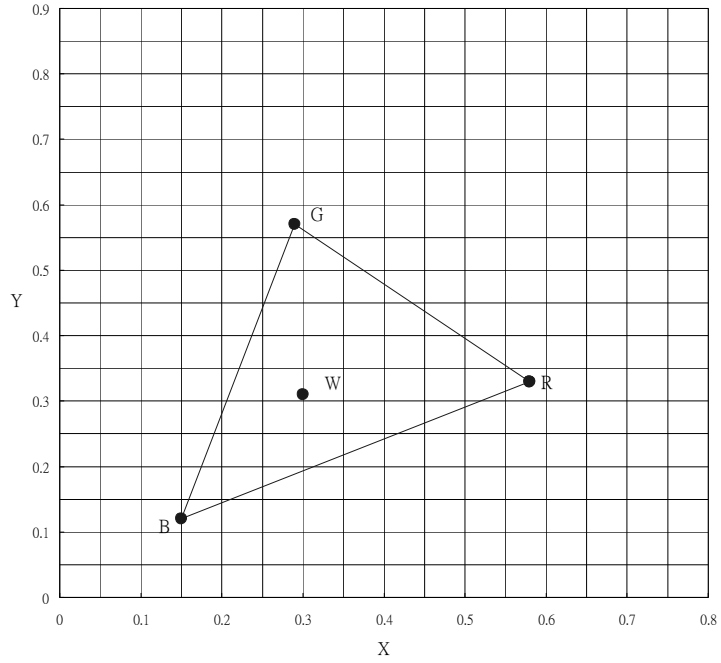
ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
Response Time (rise)	Tr	0 ℃	640	800	1200	ms	NOTE 2,3
		25 ℃	280	350	530		
		50 ℃	96	120	180		
Response Time (fall)	Tf	0 ℃	360	450	680	ms	NOTE 2,3
		25 ℃	96	120	180		
		50 ℃	56	70	110		

4-2.Color of CIE Coordinate Ta=25℃

ITEM		SYMBOL	CONDITION	VALUE			NOTE
				MIN.	TYP.	MAX.	
Color of CIE Coordinate	Red	x	$\varphi=0^{\circ}, \theta=0^{\circ}$	0.53	0.58	0.63	Note※
		y		0.28	0.33	0.38	
	Green	x	$\varphi=0^{\circ}, \theta=0^{\circ}$	0.24	0.29	0.34	
		y		0.52	0.57	0.62	
	Blue	x	$\varphi=0^{\circ}, \theta=0^{\circ}$	0.1	0.15	0.2	
		y		0.07	0.12	0.17	
	White	x	$\varphi=0^{\circ}, \theta=0^{\circ}$	0.25	0.3	0.35	
		y		0.26	0.31	0.36	

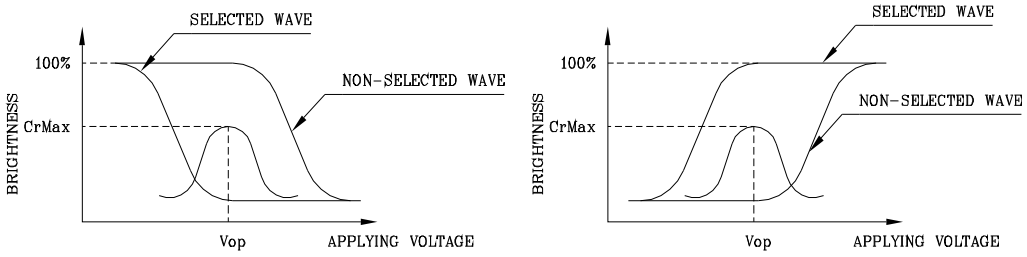
Note※ Measuring at position 3 on Fig.1 CIE chromaticity diagram

Fig.1



(NOTE 1)

Definition of Operation Voltage(Vop)



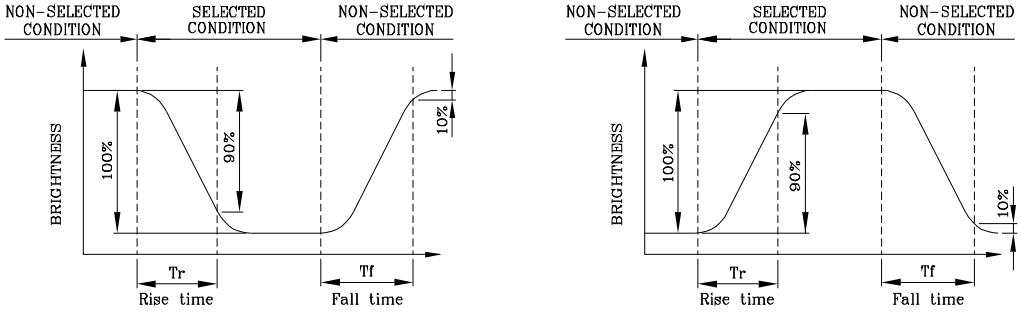
(positive type) (negative type)

*Conditions

Viewing Angle : 0
Frame Frequency : 120Hz
Applying Waveform : 1/N duty 1/a bias

(NOTE 2)

Definition of Response Time(Tr,Tf)



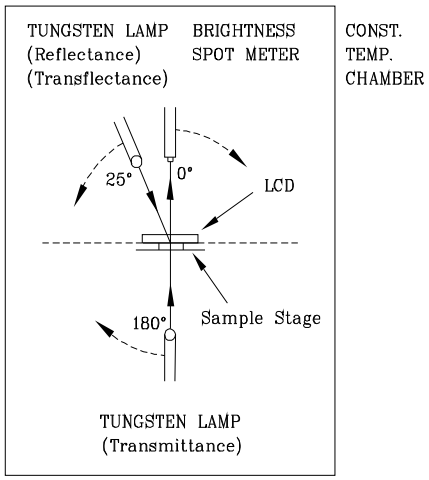
(positive type) (negative type)

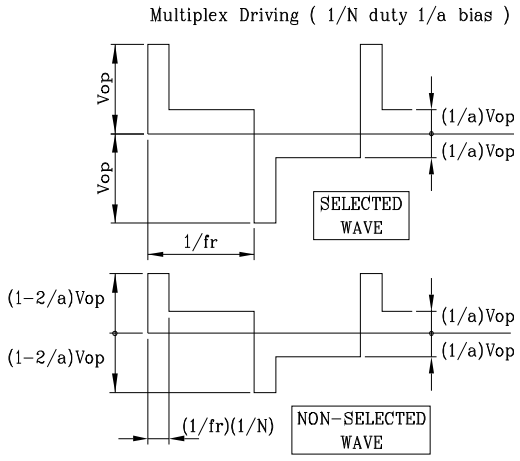
*Conditions

Operating Voltage : Vop
Viewing Angle (θ,ϕ) : (0,0)
Frame Frequency : 120Hz
Applying Waveform : 1/N duty 1/a bias

(NOTE 3)

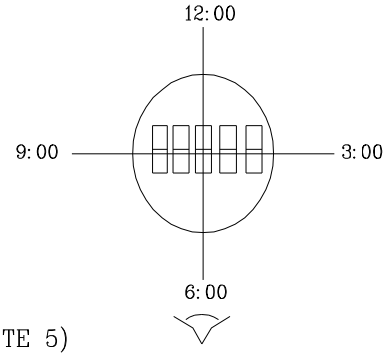
Description of Measuring Equipment and Driving Waveforms





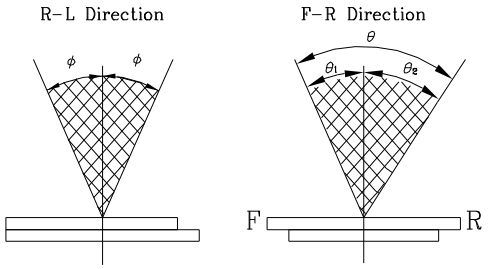
(NOTE 4)

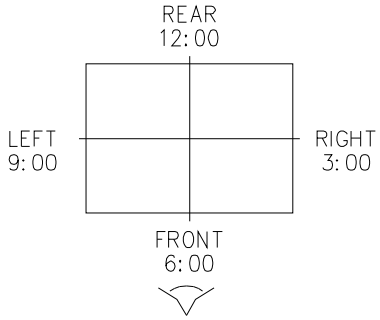
Definition of Viewing Direction



(NOTE 5)

Definition of Viewing Angle





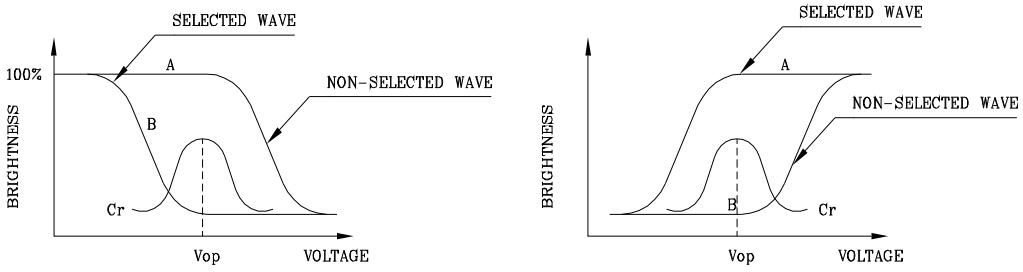
$\theta = \theta_1 + \theta_2$

*Conditions

Operating Voltage : Vop
Frame Frequency : 120Hz
Applying Waveform : 1/N duty 1/a bias
Contrast Ratio : larger than 2

(NOTE 6)

Definition of Contrast Ratio (Cr)



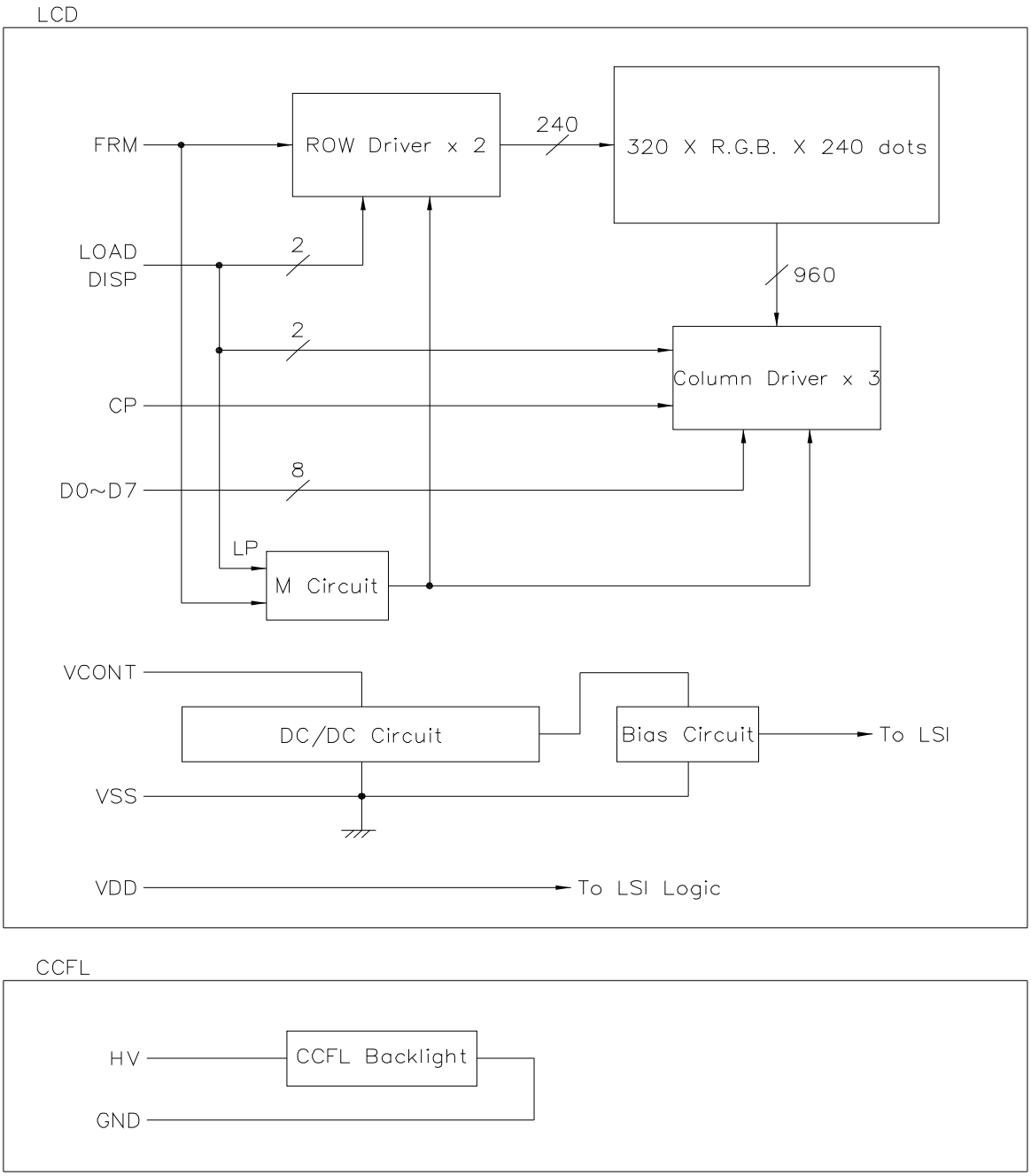
(positive type) (negative type)

Contrast Ratio : Cr=A/B

*Conditions

Viewing Angle : 0
Frame Frequency : 120Hz
Applying Waveform : 1/N duty 1/a bias

5. BLOCK DIAGRAM



6.INTERNAL PIN CONNECTION

LCD

Pin No.	Symbol	Level	Function
1	FRM	H	Synchronous signal for driving scanning line
2	LOAD	H→L	Data signal latch clock
3	CP	H→L	Data signal shift clock
4	DISP	H(ON),L(OFF)	Display control signal
5	VDD	—	Power supply for logic
6	VSS	—	GND
7	VCONT	—	Power supply for LCD
8	D7	H(ON),L(OFF)	Display Data
9	D6	H(ON),L(OFF)	Display Data
10	D5	H(ON),L(OFF)	Display Data
11	D4	H(ON),L(OFF)	Display Data
12	D3	H(ON),L(OFF)	Display Data
13	D2	H(ON),L(OFF)	Display Data
14	D1	H(ON),L(OFF)	Display Data
15	D0	H(ON),L(OFF)	Display Data
16	VDD	H(ON),L(OFF)	Power supply for logic
17	VDD	H(ON),L(OFF)	Power supply for logic
18	VSS	H(ON),L(OFF)	GND
19	VSS	H(ON),L(OFF)	GND
20	VSS	H(ON),L(OFF)	GND

USED LCD CONNECTOR :

ELCO 08-6210-020-340-800+

CORRESPONDABLE LCD FPC or FFC :

Pitch 0.5mm ,width 10.5mm

CCFL

Pin No.	Symbol	Level	Function
1	HV	—	Power supply for CCFL
2	NC	—	No Connection
3	GND	—	Ground line

USED CCFL CONNECTOR : BHR-03VS-1(JST)

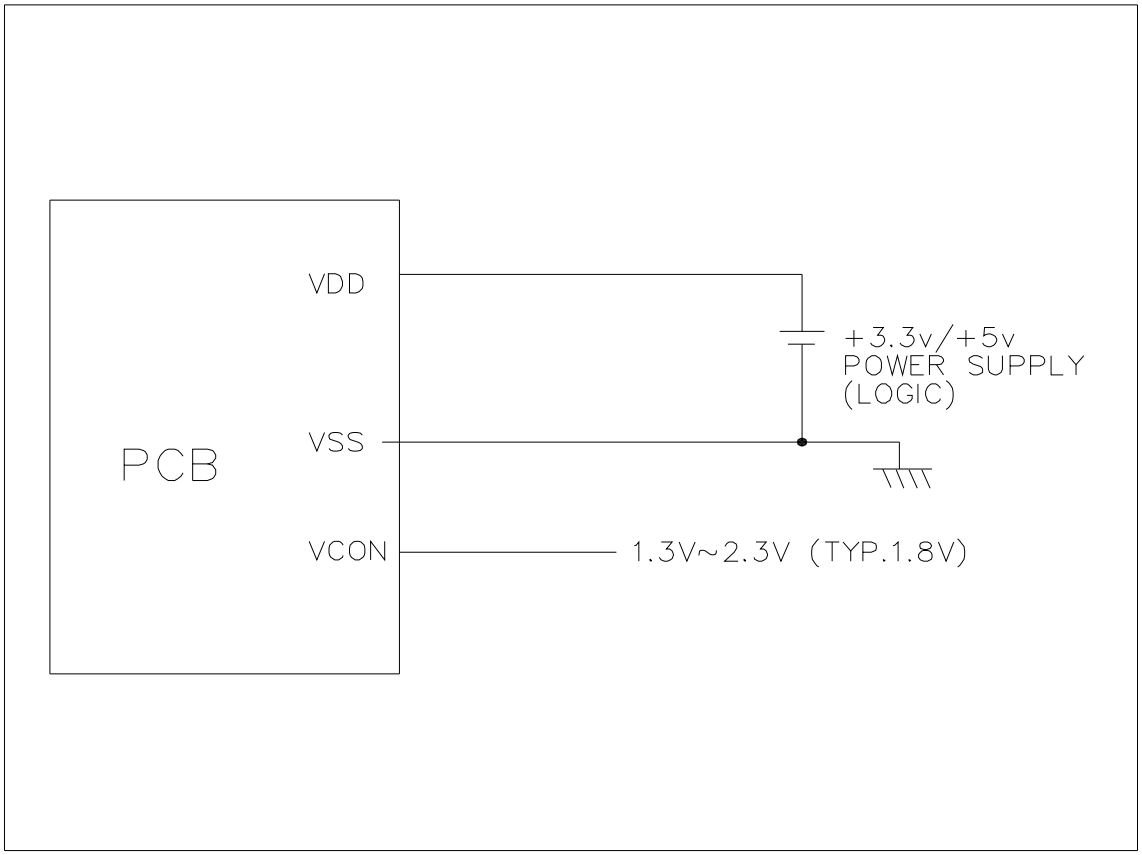
BHR-03VS-1(JST)

CORRESPONDABLE CCFL CONNECTOR :

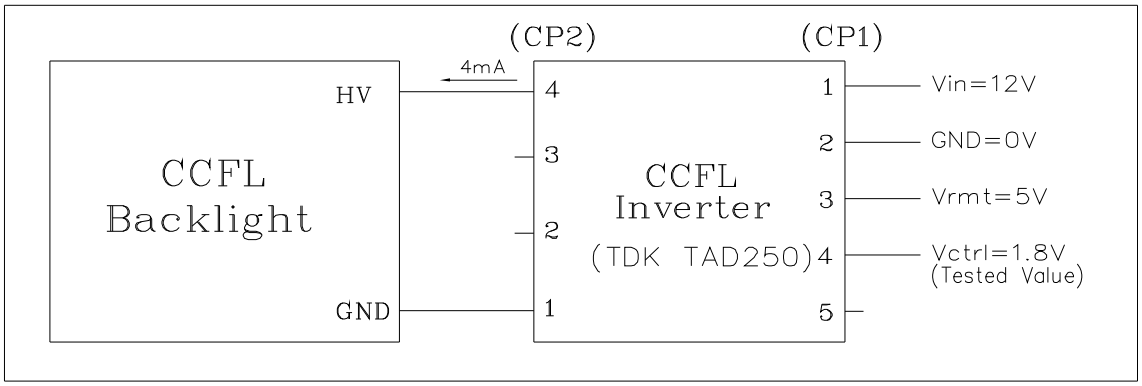
SM02-(8.0)B-BHS-3(JST) or COMPATIBLE

7.POWER SUPPLY

LCD



CCFL

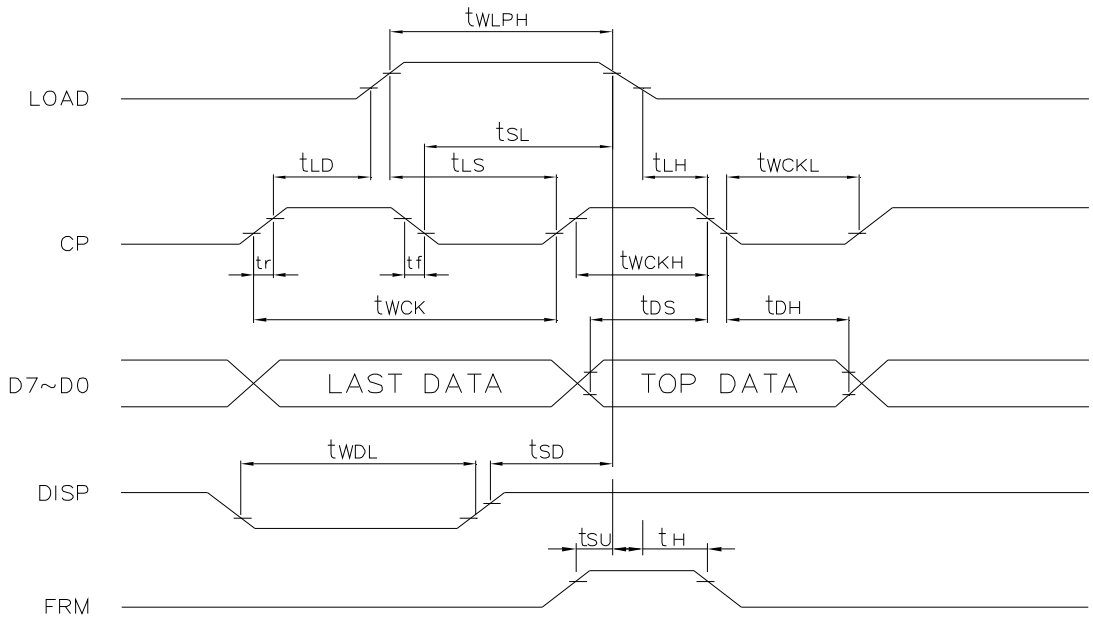


8.TIMING CHARACTERISTICS

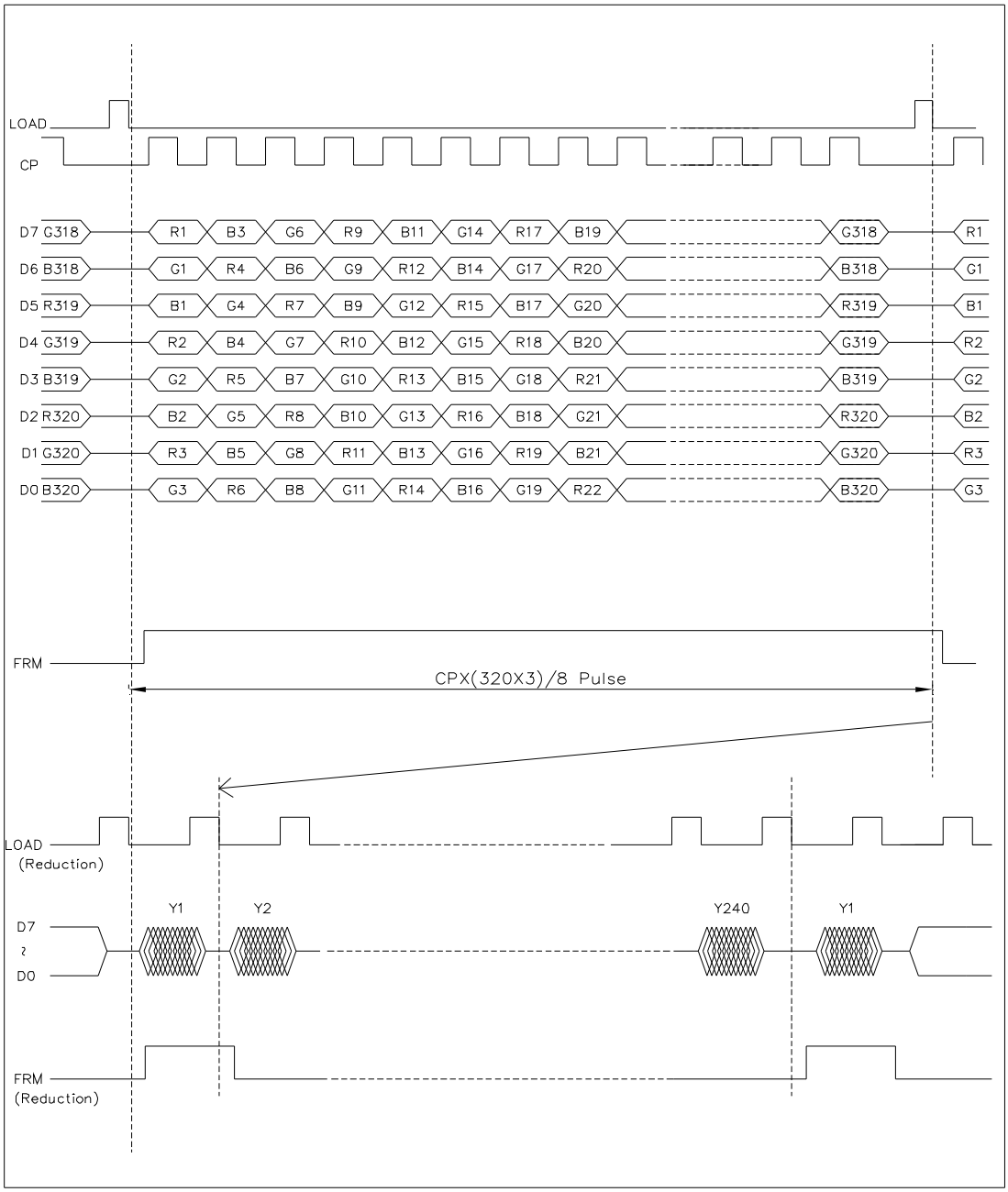
8-1.INTERFACE TIMING

VDD=3.3/5.0V ± 10%

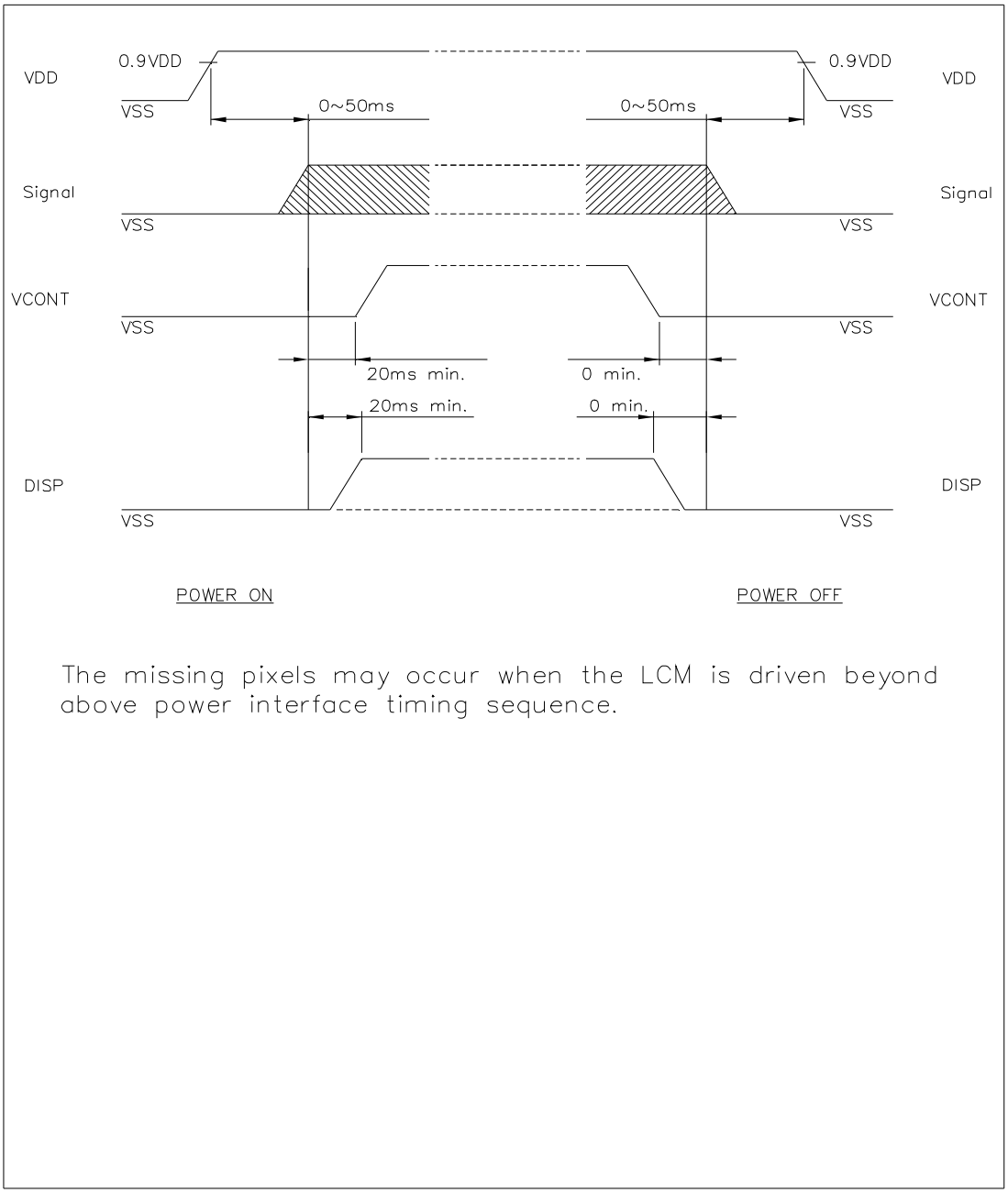
Parameter	SYMBOL	MIN.	MAX.	UNIT
CLOCK PULSE CYCLE TIME	t_{WCK}	66/40	–	ns
CLOCK PULSE HIGH LEVEL WIDTH	t_{WCKH}	23/12	–	ns
CLOCK PULSE LOW LEVEL WIDTH	t_{WCKL}	23/14	–	ns
LATCH PULSE HIGH LEVEL WIDTH	t_{WLPH}	30/15	–	ns
CP→LOAD RISE TIME	t_{LD}	10/5	–	ns
CP→LOAD FALL TIME	t_{SL}	30/25	–	ns
LOAD→CP RISE TIME	t_{LS}	30/25	–	ns
LOAD→CP FALL TIME	t_{LH}	30/25	–	ns
CLOCK PULSE RISE/FALL TIME	t_r , t_f	–	50	ns
DATA SETUP TIME	t_{DS}	10/5	–	ns
DATA HOLD TIME	t_{DH}	25/15	–	ns
DISP LOW LEVEL WIDTH	t_{WDL}	1.2	–	μs
DISP CANCELLATION TIME	t_{SD}	100	–	ns
FRLM SETUP TIME	t_{SU}	30	–	ns
FRM HOLD TIME	t_H	50	–	ns



8-2.TIMING CHART OF INPUT SIGNAL

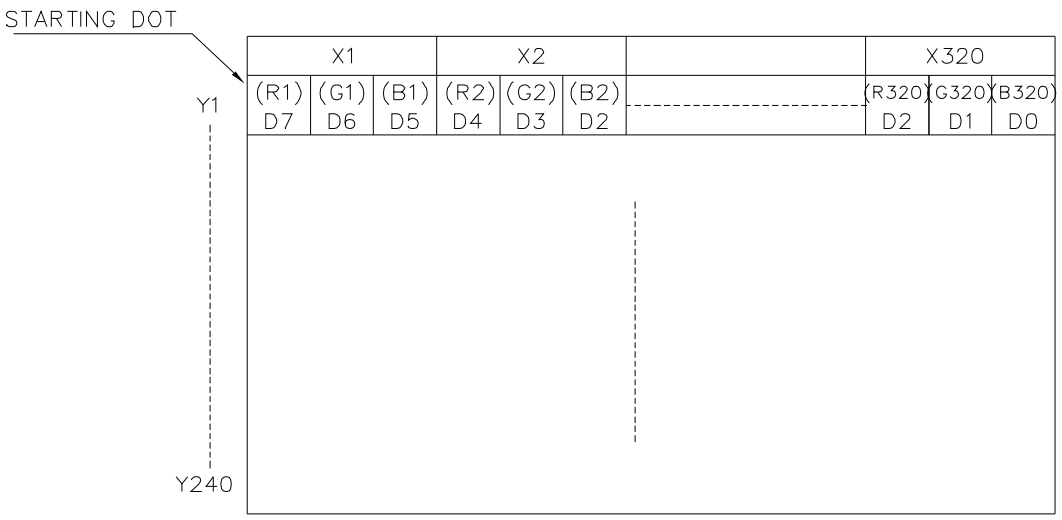


8-3.POWER ON/OFF TIMING



The missing pixels may occur when the LCM is driven beyond above power interface timing sequence.

8-4.DISPLAY PATTERN



D0~D7 are 8 bits transmitted data, where D0 is LSB and D7 is MSB.

9.RELIABILITY TEST

NORMAL TEMPERATURE RELIABILITY TEST

NO.	ITEM	CONDITION			STANDARD	NOTE
1	High Temp. Storage	70 ℃	120 Hrs		Appearance without defect	
2	Low Temp. Storage	-20 ℃	120 Hrs		Appearance without defect	
3	High Temp. & High Humi. Storage	50 ℃ 90%RH	120 Hrs		Appearance without defect	
4	High Temp. Operating Display	50 ℃	120 Hrs		Appearance without defect	
5	Low Temp. Operating Display	0 ℃	120 Hrs		Appearance without defect	
6	Thermal Shock	-20 ℃, 30min. → 70℃, 30min. ↑ (1cycle) ┘			Appearance without defect	10 cycles

Inspection Provision

1.Purpose

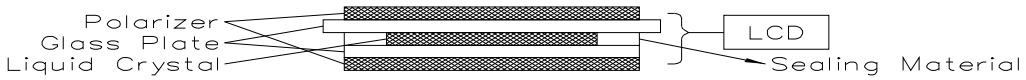
The NAN YA inspection provision provides outgoing inspection provision and its expected quality level based on our outgoing inspection of NAN YA LCD produces.

2.Applicable Scope

The NAN YA inspection provision is applicable to the arrangement in regard to outgoing inspection and quality assurance after outgoing.

3.Technical Terms

3-1 NAN YA Technical Terms



4.Outgoing Inspection

4-1 Inspection Method

MIL-STD-105E Level Ⅱ Regular inspection

4-2 Inspection Standard

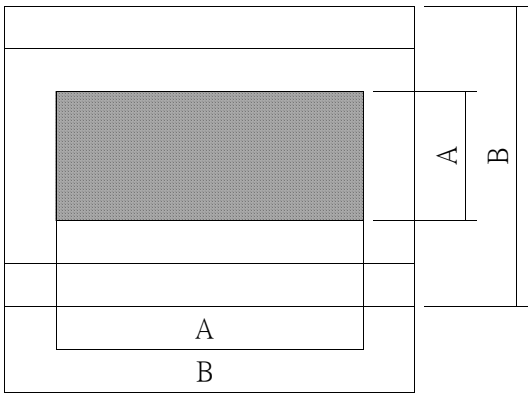
	Item		AQL(%)	Remarks
Major Defect	Dots	Opens Shorts Erroneous operation	0.4	faults which substantially lower the practicality and the initial purpose difficult to achieve
	Solder appearance	Shorts Loose		
	Cracks	Display surface cracks		

	Dimensions	External from Dimensions	0.4	
Minor Defect	Inside the glass	Black spots	0.65	faults which appear to pose almost no obstacle to the practicality, effective use, and operation.
	Polarizing plate	Scratches, foreign Matter, air bubbles, and peeling		
	Dots	Pinhole, deformation		
	Color tone	Color unevenness		
	Solder appearance	Cold solder Solder projections		

4-3 Inspection Provisions

*Viewing Area Definition

Fig. 1



A : Zone Viewing Area

B : Zone Glass Plate Outline

*Inspection place to be 500 to 1000 lux illuminance uniformly without glaring.
The distance between luminous source(daylight fluorescent lamp and cool white fluorescent lamp) and sample to be 30 cm to 50 cm.

*Test and measurement are performed under the following conditions, unless otherwise specified.

Temperature	20 ± 15℃
Humidity	65 ± 20%R.H.
Pressure	860~1060hPa(mmbar)

In case of doubtful judgment, it is performed under the following conditions.

Temperature	20 ± 2℃
Humidity	65 ± 5%R.H.
Pressure	860~1060hPa(mmbar)

5.Specification for quality check

5-1 Electrical characteristics

NO.	Item	Criterion
1	Non operational	Fail
2	Miss operating	Fail
3	Missing dot	Fail
4	Contrast irregular	Fail
5	Response time	Within Specified value
6	Backlight turn on/off	Within Specified value

5-2 External Appearance Defect

<table>
<tr><th>NO.</th><th>Item</th><th>Criterion</th></tr>
<tr><td>1</td><td>Black spots, foreign matter, and white spots (Including light leakage due to pinholes of polarizing plates, etc.)</td><td>(1)-1-Spots

<table><tr><th>Average Diameter (mm):D</th><th>Number of pieces permitted</th><th>Minimum Space</th></tr><tr><td>D≦0.2</td><td>Ignore</td><td>—</td></tr><tr><td>0.2<D≦0.3</td><td>5</td><td>10mm</td></tr><tr><td>0.3<D≦0.4</td><td>2</td><td>30mm</td></tr><tr><td>0.4<D</td><td>0</td><td>—</td></tr></table>Number of total pieces is set to within 5 pieces.

Note that when there are 2 pieces or more, they are not to be concentrated. Set as: Average diameter = (Long diameter + Short diameter)/2

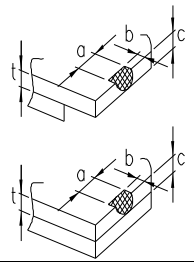
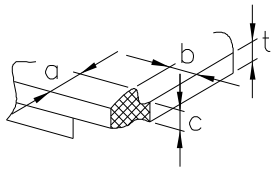
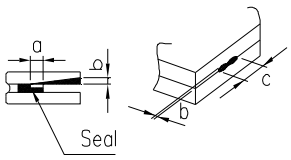
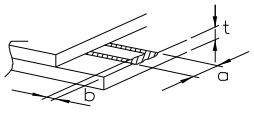
(1)-2-Blurred Spots(At lighting condition)

<table><tr><th>Average Diameter (mm):D</th><th>Number of pieces permitted</th></tr><tr><td>D≦0.3</td><td>Ignore</td></tr><tr><td>0.3<D≦0.75</td><td>5</td></tr><tr><td>0.75<D</td><td>0</td></tr></table>Number of total pieces is set to within 5 pieces.

Note that when there are 2 pieces or more, they are not to be concentrated. Set as: Average diameter = (Long diameter + Short diameter)/2</td></tr>
</table>

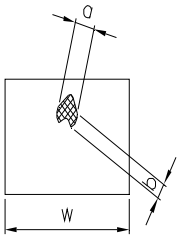
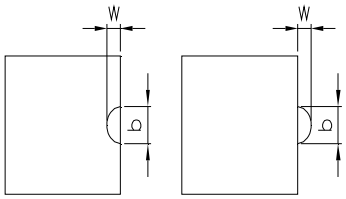
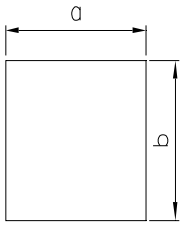
<table>
<tr>
<td>1</td>
<td>Line</td>
<td>(1)-1-Lines
<table>
<tr><th>Width(mm):W</th><th>Length(mm):
L</th><th>Number of pieces permitted</th></tr>
<tr><td>W≦0.03</td><td>Ignore</td><td>Ignore</td></tr>
<tr><td>0.03<W≦0.08</td><td>L≦4</td><td>2</td></tr>
<tr><td>0.08<W≦0.1</td><td>L≦1</td><td>1</td></tr>
</table>
Object exceeding 0.1mm follow the standards of the spots form. Note that when there are 2 pieces or more, they are not to be concentrated.

(1)-2-Blurred Lines(At lighting condition)
<table>
<tr><th>Width(mm):W</th><th>Length(mm):
L</th><th>Number of pieces permitted</th></tr>
<tr><td>W≦0.03</td><td>Ignore</td><td>Ignore</td></tr>
<tr><td>0.03<W≦0.08</td><td>L≦3</td><td>6</td></tr>
<tr><td>0.08<W</td><td>3<L</td><td>None</td></tr>
</table>
Object exceeding 0.1mm follow the standards of the spots form. Note that when there are 2 pieces or more, they are not to be concentrated.</td>
</tr>
<tr>
<td>2</td>
<td>Scratches(Glass, reflection plates, and polarizing plates)</td>
<td>In accordance with black spots.
(At non lighting condition)</td>
</tr>
<tr>
<td>3</td>
<td>Color irregular</td>
<td>Not remarkable color irregular.</td>
</tr>
</table>

<table>
<tr><td>4</td><td>Air bubbles polarizing plates, and reflection plates</td><td colspan="2">
<table>
<tr><th>Average Diameter (mm):D</th><th>Number of pieces permitted</th></tr>
<tr><td>D≦0.3</td><td>Ignore</td></tr>
<tr><td>0.3<D</td><td>0</td></tr>
</table>
Average diameter = (Long diameter + Short diameter)/2
Note that when there are 4 pieces or more, they are not to be concentrated.
</td></tr>
<tr><td rowspan="5">5</td><td rowspan="5">Cracks</td><td>(1)General crack
  </td><td>a≦5
b≦2
c≦t
Where, a and b are ignored when less than or equal to 0.5 . The numbers of pieces are set at up to 5 pieces.</td></tr>
<tr><td>(2)Corner crack
  </td><td>a≦2.5
b≦2.5
c≦t
a+b≦4</td></tr>
<tr><td>(3)Seal portion crack
  </td><td>a≦The seal width x 1/3
b≦t x 2/3
c≦5
The numbers of pieces are set at up to 5 pieces.</td></tr>
<tr><td>(4)ITO Pin crack
  </td><td>a≦5
b≦1/3 pin length
c≦t</td></tr>
<tr><td>(5)Progressive cracks</td><td>All taken to be unacceptable</td></tr>
</table>

6	Outer dimensions	Should be within the tolerance.
7	Soldering	Should be no defective soldering such as shorting, loose terminal cold solder, peeling of printed circuit board pattern, improper mounting position, etc.

5-3 Dot Appearance Defect

NO.	Item	Criteria	
1	Pinhole		Dot display a and b are each ≦ 0.2mm. The overall total is taken be with in 10 units. Note that they are not to be concentrated.
2	Missing		Dot display a and b are each ≦ 0.2mm. The overall total is taken to be with in 10 units.
3	Thick and thin display		Taken to be within ±1.5% of display character width(a) and height(b).

NOTICE:

• SAFETY

1. If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
2. If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

• HANDLING

1. Avoid static electricity which can damage the CMOS LSI.
2. Do not remove the panel or frame from the module.
3. The polarizing plate of the display is very fragile. So, please handle it very carefully.
4. Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
5. Do not use ketonics solvent & Aromatic solvent. Use a soft cloth soaked with a cleaning naphtha solvent.

• STORAGE

1. Store the panel or module in a dark place where the temperature is 25±5℃ and the humidity is below 65% RH.
2. Do not place the module near organics solvents or corrosive gases.
3. Do not crush, shake, or jolt the module.

• TERMS OF WARRANT

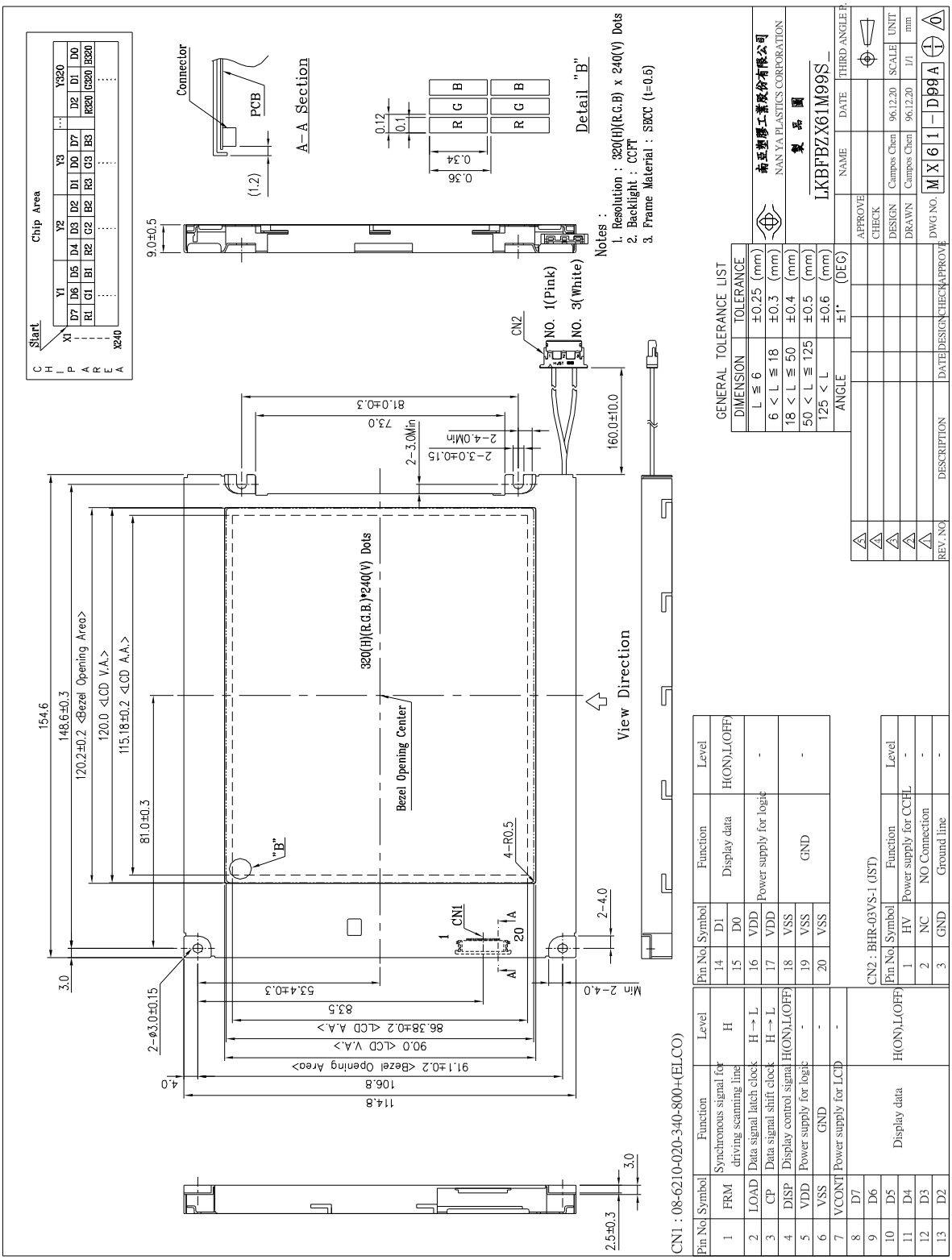
1. Acceptance inspection period

The period is within one month after the arrival of contracted commodity at the buyer's factory site.

2. Applicable warrant period

The period is within twelve months since the date of shipping out under normal using and storage conditions.

10.OUTLINE DRAWING



GENERAL TOLERANCE LIST

DIMENSION	TOLERANCE
L ≦ 6	±0.25 (mm)
6 < L ≦ 18	±0.3 (mm)
18 < L ≦ 50	±0.4 (mm)
50 < L ≦ 125	±0.5 (mm)
125 < L	±0.6 (mm)
ANGLE	±1° (DEG)

南亞塑膠工業股份有限公司
NAN YA PLASTICS CORPORATION

製 品 圖

LKBFBZX61M99S_

	NAME	DATE
APPROVE		
CHECK	Campos Chen	96.12.20
DESIGN	Campos Chen	96.12.20
DRAWN		
DWG NO.	MX61-D99A	

THIRD ANGLE P. | SCALE 1/1 | UNIT mm

REV. NO.	DESCRIPTION	DATE	DESIGN	CHECK	APPROVE
0					

CN1 : 08-6210-020-340-800+(ELCO)

Pin No.	Symbol	Function	Level
1	FRM	Synchronous signal for driving scanning line	H
2	LOAD	Data signal latch clock	H → L
3	CP	Data signal shift clock	H → L
4	DISP	Display control signal	H(ON),L(OFF)
5	VDD	Power supply for logic	-
6	VSS	GND	-
7	VCONT	Power supply for LCD	-
8	D7	Display data	H(ON),L(OFF)
9	D6		
10	D5		
11	D4		
12	D3		
13	D2		
14	D1		
15	D0		
16	VDD	Power supply for logic	-
17	VDD		
18	VSS	GND	-
19	VSS		
20	VSS		

CN2 : BHR-03VS-1 (JST)

Pin No.	Symbol	Function	Level
1	HV	Power supply for CCFL	-
2	NC	NO Connection	-
3	GND	Ground line	-

REV/DATE	R0/ 12.21.07'						BY W.R.HSU