

LM113QML

Reference Diode

General Description

The LM113 are temperature compensated, low voltage reference diodes. They feature extremely-tight regulation over a wide range of operating currents in addition to an unusually-low breakdown voltage and good temperature stability.

The diodes are synthesized using transistors and resistors in a monolithic integrated circuit. As such, they have the same low noise and long term stability as modern IC op amps. Further, output voltage of the reference depends only on highly-predictable properties of components in the IC; so they can be manufactured and supplied to tight tolerances.

Features

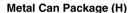
- Low breakdown voltage: 1.220V
- Dynamic impedance of 0.3Ω from 500 µA to 20 mA
- Temperature stability typically 1% over-55°C to 125°C range
- Tight tolerance: ±5% or ±1%

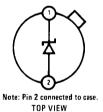
The characteristics of this reference recommend it for use in bias-regulation circuitry, in low-voltage power supplies or in battery powered equipment. The fact that the breakdown voltage is equal to a physical property of silicon—the energy-band gap voltage—makes it useful for many temperature-compensation and temperature-measurement functions.

Ordering Information

NS Part Number	SMD Part Number	NS Package Number	Package Description
LM113H-SMD	5962-8671101XA	H02A	2LD Metal Can
LM113-1H-SMD	5962-8671102XA	H02A	2LD Metal Can
LM113-1H-QMLV	5962-9684302VXA	H02A	2LD Metal Can
LM113WG-QMLV	5962-9684301VZA	WG10A	10LD Ceramic SOIC
LM113-1WG-QMLV	5962-9684302VZA	WG10A	10LD Ceramic SOIC

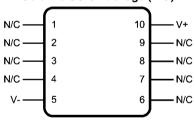
Connection Diagrams





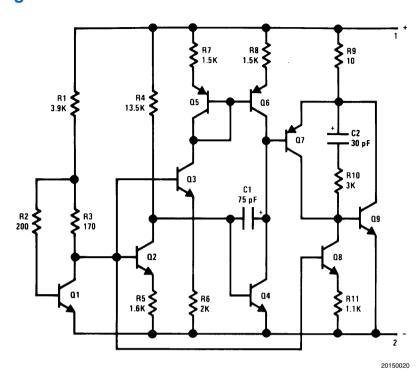
See NS Package Number H02A

Ceramic SOIC Package (WG)



See NS Package Number WG10A

Schematic Diagram



Absolute Maximum Ratings (Note 1)

Power Dissipation (Note 2) 100 mW
Reverse Current 50 mA
Forward Current 50 mA

Storage Temperature Range $-65^{\circ}\text{C} \le T_{\text{A}} \le +150^{\circ}\text{C}$

Operating Temperature Range $-55^{\circ}\text{C} \le T_{A} \le +125^{\circ}\text{C}$

Thermal Resistance

 θ_{JA}

 Metal Can (Still Air)
 440°C/W

 Metal Can (500LF / Min Air Flow)
 TBD

 Ceramic SOIC (Still Air)
 218°C/W

 Ceramic SOIC (500LF / Min Air Flow)
 140°C/W

 θ_{JC}

 Metal Can
 80°C/W

 Ceramic SOIC
 27°C/W

Package Weight

 Metal Can
 275mg

 Ceramic SOIC
 220mg

 ESD Tolerance (*Note 3*)
 4000V

Quality Conformance

Mil-Std-883, Method 5005 - Group A

Subgroup	Description	Temp (°C)
1	Static tests at	+25
2	Static tests at	+125
3	Static tests at	-55
4	Dynamic tests at	+25
5	Dynamic tests at	+125
6	Dynamic tests at	-55
7	Functional tests at	+25
8A	Functional tests at	+125
8B	Functional tests at	-55
9	Switching tests at	+25
10	Switching tests at	+125
11	Switching tests at	-55
12	Settling time at	+25
13	Settling time at	+125
14	Settling time at	-55

LM113 Electrical Characteristics

DC Parameters

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
V	Zener Voltage	I _B = 1 mA		1.16	1.28	V	1
V _{ZR}	Zerier voltage	IR = I IIIA		1.157	1.283	V	2, 3
ΔV_{ZR}	Delta Zener Voltage	$0.5\text{mA} \le I_R \le 20\text{mA}$			15	mV	1
		0.5mA ≤ I _R ≤ 10mA			15	mV	2, 3
V _F	Forward Voltage Drop	I _F = 1mA			1.0	V	1, 2, 3
R _R	Davis Davis Davis Land davis d	I _R = 1mA	(Note 4)		1.0	Ω	4
	Reverse Dynamic Impedance	I _R = 10mA			0.8	Ω	4

DC Drift Parameters

Delta Calculations performed on QMLV devices at Group B, Subgroup 5, only.

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
V _{ZR}	Zener Voltage	I _R = 1mA		-0.02	0.02	V	1

LM113-1 Electrical Characteristics

DC Parameters

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
V	Zonor Voltago	I = 1 mA		1.210	1.232	V	1
V _{ZR}	Zener Voltage	I _R = 1 mA		1.206	1.234	V	2, 3
ΔV_{ZR}	Delta Zener Voltage	0.5mA ≤ I _R ≤ 20mA			15	mV	1
		0.5mA ≤ I _R ≤ 10mA			15	mV	2, 3
V _F	Forward Voltage Drop	I _F = 1mA			1.0	V	1, 2, 3
R _R	Davida Dividada Incidada da	I _R = 1mA	(Note 4)		1.0	Ω	4
	Reverse Dynamic Impedance	I _R = 10mA			0.8	Ω	4

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V_{ZR}	Zener Voltage	I _R = 1mA		-0.02	0.02	V	1

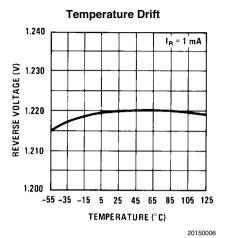
Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

Note 2: The maximum power dissipation must be derated at elevated temperatures and is dictated by T_{Jmax} (maximum junction temperature), θ_{JA} (package junction to ambient thermal resistance), and T_A (ambient temperature). The maximum allowable power dissipation at any temperature is $P_{Dmax} = (T_{Jmax} - T_A)/\theta_{JA}$ or the number given in the Absolute Maximum Ratings, whichever is lower.

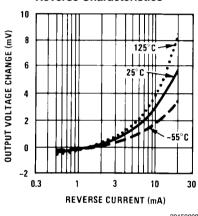
Note 3: Human body model, $1.5 \text{K}\Omega$ in series with 100pF.

Note 4: Guaranteed parameter, not tested.

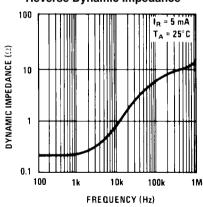
Typical Performance Characteristics



Reverse Characteristics

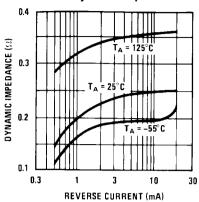


Reverse Dynamic Impedance



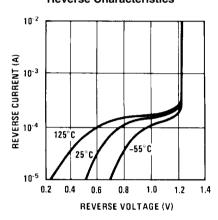
20150010

Reverse Dynamic Impedance



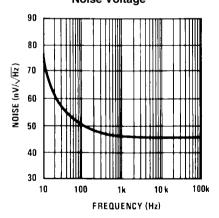
20150007

Reverse Characteristics

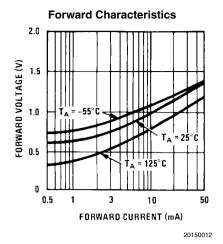


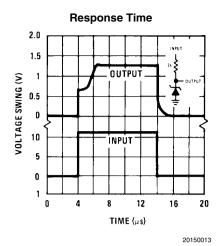
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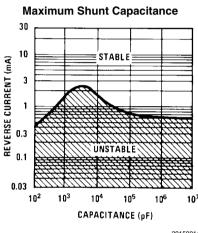
Noise Voltage



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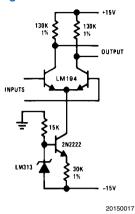




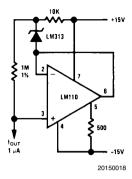
REVERSE CURRENT (mA) 10⁷ 20150014

Typical Applications

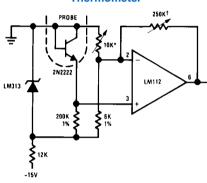
Amplifier Biasing for Constant Gain with Temperature



Constant Current Source

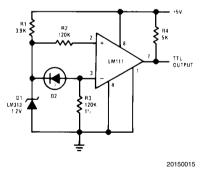


Thermometer



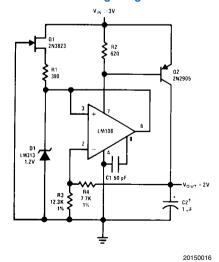
Adjust for 0V at 0°C Adjust for 100 mV/°C

Level Detector for Photodiode



Low Voltage Regulator

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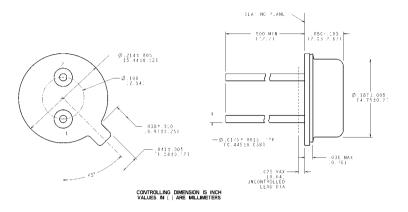


†Solid tantalum.

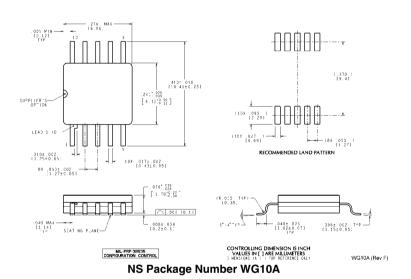
Revision History

Released	Revision	Section	Changes
12/16/2010	А	·	2 MDS data sheets converted into one Corp. data sheet format. MDSs MNLM113-X Rev 1C1 and MNLM113-1-X Rev. 2A1 will be archived.

Physical Dimensions inches (millimeters) unless otherwise noted



NS Package Number H02A



9 www.national.com

H02A (Rev F)

Notes

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