

LM140QML

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SNVS382B-FEBRUARY 2006-REVISED MAY 2013

LM140QML Three Terminal Positive Regulators

Check for Samples: LM140QML

FEATURES

- Complete Specifications at 1.0A and 0.5A Loads
- No External Components
- Internal Thermal Overload Protection
- Internal Short Circuit Current-Limiting
- Output Transistor Safe-Area Compensation

DESCRIPTION

The monolithic 3-terminal positive voltage regulators employ internal current-limiting, thermal shutdown and safe-area compensation. making them essentially indestructible. If adequate heat sinking is provided, they can deliver over 0.5A output current. They are intended as fixed voltage regulators in a wide range of applications including local (on-card) regulation for elimination of noise and distribution problems associated with single-point regulation. In addition to use as fixed voltage regulators, these devices can be used with external components to obtain adjustable output voltages and currents.

Considerable effort was expended to make the entire series of regulators easy to use and minimize the number of external components. It is not necessary to bypass the output, although this does improve transient response. Input bypassing is needed only if the regulator is located far from the filter capacitor of the power supply.

Connection Diagram

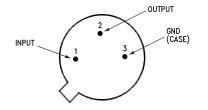
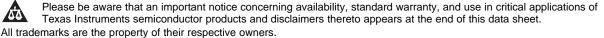


Figure 1. Steel Metal Can TO-39 Package (NDT) Bottom View See Package Number NDT0003A



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.



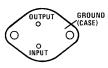


Figure 2. TO-3 Metal Can (K) Bottom View See Package Number K0002C



Absolute Maximum Ratings⁽¹⁾

DC Input Voltage			35V
Internal Power Dissipation ⁽²⁾			Internally Limited
Maximum Junction Temperature	(T _{Jmax)}		150°C
Storage Temperature Range			$-65^{\circ}\text{C} \leq \text{T}_{\text{A}} \leq +150^{\circ}\text{C}$
Operating Temperature Range			$-55^{\circ}\text{C} \leq \text{T}_{\text{A}} \leq +125^{\circ}\text{C}$
Lead Temperature (Soldering 10	300°C		
		T0-39 (Still Air)	232°C/W
	0	T0-39 (500 LF/Min Air Flow)	77°C/W
Thermal Resistance	θ_{JA}	T0-3 (Still Air)	35°C/W
Thermal Resistance		T0-3 (500 LF/Min Air Flow)	TBD
	0	T0-39	15°C/W
	θ _{JC}	T0-3	4°C/W
ESD Susceptibility ⁽³⁾			2KV

(1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not specify specific performance limits. For ensured specifications and test conditions, see the Electrical Characteristics. The ensured specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

The maximum power dissipation must be derated at elevated temperatures and is dictated by T_{Jmax} (maximum junction temperature), (2) θ_{JA} (package junction to ambient thermal resistance), and T_A (ambient temperature). The maximum allowable power dissipation at any temperature is $P_{Dmax} = (T_{Jmax} - T_A)/\theta_{JA}$ or the number given in the Absolute Maximum Ratings, whichever is lower. Human body model, 100pF discharged through 1.5K Ω

(3)

Quality Conformance Inspection

MIL-Std-883, Method 5005 - Group A

Subgroup	Description	Temp °C
1	Static tests at	+25
2	Static tests at	+125
3	Static tests at	-55
4	Dynamic tests at	+25
5	Dynamic tests at	+125
6	Dynamic tests at	-55
7	Functional tests at	+25
8A	Functional tests at	+125
8B	Functional tests at	-55
9	Switching tests at	+25
10	Switching tests at	+125
11	Switching tests at	-55
12	Settling time at	+25
13	Settling time at	+125
14	Settling time at	-55



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LM140H–5.0 Electrical Characteristics DC Parameters

The following conditions apply, unless otherwise specified. DC: V_I = 10V, I_L =350mA

Symbol	Parameter	Conditions	Notes	Min	Мах	Unit	Sub- groups
Vo		$V_{I} = 35V, I_{L} = 5mA$		4.75	5.75	V	1
				4.80	5.20	V	1
		V ₁ = 8V		4.70	5.30	V	1, 2, 3
	Output Voltage	$V_{I} = 8V, I_{L} = 5mA$		4.70	5.30	V	1, 2, 3
		$V_{I} = 20V, I_{L} = 5mA$		4.70	5.30	V	1, 2, 3
		V _I = 20V		4.70	5.30	V	1, 2, 3
R _{Line}		$7V \le V_I \le 25V, I_L = 200 \text{mA}$		-50	50	mV	1
	Line Develotion	$8V \le V_{I} \le 25V, I_{L} = 200mA$		-50	50	mV	2, 3
	Line Regulation	$8V \le V_I \le 20V, I_L = 200mA$		-25	25	mV	1
				-40	40	mV	2, 3
R _{Load}		$5mA \le I_L \le 500mA$		-50	50	mV	1
	Las d Danida Gan			-100	100	mV	2, 3
	Load Regulation			-25	25	mV	1
		$5mA \le I_L \le 200mA$		-50	50	mV	2, 3
l _Q	Quiescent Current				7.0	mA	1, 2, 3
Δl _Q		$8V \le V_I \le 25V, I_L = 200mA$		-0.8	0.8	mA	1, 2, 3
	Quiescent Current Change	$5mA \le I_L \le 350mA$		-0.5	0.5	mA	1, 2, 3
I _{Pk}	Peak Current	$V_{I} - V_{O} = 7V$	See ⁽¹⁾	0.4	2.0	А	1, 2, 3
V _{DO}	Dropout Voltage		See ⁽²⁾		2.5	V	1
I _{OS}	Short Circuit Current	V ₁ = 35V			1.0	А	1, 2, 3

 $\begin{array}{ll} \mbox{(1)} & V_O \mbox{ is set to } 90\% \ V_{Ref} \\ \mbox{(2)} & V_{DO} = V_I - V_O \ \mbox{when } V_O \ \mbox{is } 95\% \ \mbox{of } V_{Ref}. \end{array}$

LM140H–5.0 Electrical Characteristics AC Parameters

The following conditions apply, unless otherwise specified.

AC: $V_1 = 10V$, $I_L = 350mA$

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
RR	Ripple Rejection	$I_L = 125$ mA, $e_I = 1V_{RMS}$, $f = 2.4$ KHz, $V_I = 10V$		62		dB	4, 5, 6



LM140H–12 Electrical Characteristics DC Parameters

The following conditions apply, unless otherwise specified. DC: V_I = 19V, I_L =350mA

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
Vo		$V_{I} = 35V, I_{L} = 5mA$		11.4	12.6	V	1
				11.5	12.5	V	1
		V _I = 15.5V		11.4	12.6	V	1, 2, 3
	Output Voltage	$V_{I} = 15.5V, I_{L} = 5mA$		11.4	12.6	V	1, 2, 3
		$V_{I} = 27V, I_{L} = 5mA$		11.4	12.6	V	1, 2, 3
		V ₁ = 27V		11.4	12.6	V	1, 2, 3
R _{Line}		$14.5V \le V_{I} \le 30V, I_{L} = 200mA$		-60	60	mV	1
	Line Deculation	$15.0V \le V_{I} \le 30V, I_{L} = 200mA$		-120	120	mV	2, 3
	Line Regulation	$16V \le V_I \le 25V, I_L = 200$ mA		-30	30	mV	1
				-60	60	mV	2, 3
R _{Load}		$5mA \le I_L \le 500mA$		-120	120	mV	1
	Las d Danida (fau			-240	240	mV	2, 3
	Load Regulation			-60	60	mV	1
		$5mA \le I_L \le 200mA$		-120	120	mV	2, 3
l _Q	Quiescent Current				7.0	mA	1, 2, 3
Δl _Q		$14.5V \le V_{I} \le 30V, I_{L} = 200mA$		-0.8	0.8	mA	1, 2, 3
	Quiescent Current Change	$5mA \le I_L \le 350mA$		-0.5	0.5	mA	1, 2, 3
I _{Pk}	Peak Current	V ₁ - V _O = 7V	See ⁽¹⁾	0.4	2.0	А	1, 2, 3
V _{DO}	Dropout Voltage		See ⁽²⁾		2.5	V	1
los	Short Circuit Current	V _I = 35V			1.0	А	1, 2, 3

 $\begin{array}{ll} \mbox{(1)} & V_O \mbox{ is set to } 90\% \ V_{Ref} \\ \mbox{(2)} & V_{DO} = V_I - V_O \ \mbox{when } V_O \ \mbox{is } 95\% \ \mbox{of } V_{Ref}. \end{array}$

LM140H–12 Electrical Characteristics AC Parameters

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
RR	Ripple Rejection	$V_I = 17V$, $I_L = 125mA$, $e_I = 1V_{RMS}$, $f = 2.4KHz$		55		dB	4, 5, 6



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LM140H–15 Electrical Characteristics DC Parameters

The following conditions apply, unless otherwise specified. DC: V_I = 23V, I_L =350mA

Symbol	Parameter	Conditions	Notes	Min	Мах	Unit	Sub- groups
Vo		$V_{I} = 35V, I_{L} = 5mA$		14.25	15.75	V	1
				14.40	15.60	V	1
	Output Maltana	V _I = 18.5V		14.25	15.75	V	1, 2, 3
	Output Voltage	$V_{I} = 18.5V, I_{L} = 5mA$		14.25	15.75	V	1, 2, 3
		$V_{I} = 30V, I_{L} = 5mA$		14.25	15.75	V	1, 2, 3
		V _I = 30V		14.25	15.75	V	1, 2, 3
R _{Line}		$17.5V \le V_{I} \le 30V, I_{L} = 200mA$		-60	60	mV	1
	Line Develotion	$18.5V \le V_{I} \le 30V, I_{L} = 200mA$		-120	120	mV	2, 3
	Line Regulation	$20V \le V_I \le 30V, I_L = 200mA$		-30	30	mV	1
				-60	60	mV	2, 3
R _{Load}		$5mA \le I_L \le 500mA$		-150	150	mV	1
	Logid Devider for			-300	300	mV	2, 3
	Load Regulation			-75	75	mV	1
		$5mA \le I_L \le 200mA$		-150	150	mV	2, 3
l _Q	Quiescent Current				7.0	mA	1, 2, 3
Δl _Q		$17.5V \le V_{I} \le 30V, I_{L} = 200mA$		-0.8	0.8	mA	1, 2, 3
	Quiescent Current Change	$5mA \le I_L \le 350mA$		-0.5	0.5	mA	1, 2, 3
I _{Pk}	Peak Current	V ₁ - V ₀ = 7V	See ⁽¹⁾	0.4	2.0	А	1, 2, 3
V _{DO}	Dropout Voltage		See ⁽²⁾		2.5	V	1
I _{OS}	Short Circuit Current	V ₁ = 35V			1.0	А	1, 2, 3

(1) V_O is set to 90% V_{Ref} (2) V_{DO} = V_I - V_O when V_O is 95% of V_{Ref}.

LM140H–15 Electrical Characteristics AC Parameters

The following conditions apply, unless otherwise specified.

AC: V_I = 23V, I_L =350mA

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
RR	Ripple Rejection	V _I =20V, I _L =125mA, e _I =1V _{RMS} , <i>f</i> = 2.4KHz		54		dB	4, 5, 6



LM140K–5.0 Electrical Characteristics DC Parameters

The following conditions apply, unless otherwise specified. DC: V_I = 10V, I_L = 5mA

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
l _Q	Quiescent Current	1 10			6.0	mA	1
	Quescent Current	$I_{L} = 1A$			7.0	mA	2, 3
ΔQ		$I_L = 1A, 8V \le V_I \le 20mA$		-0.8	0.8	mA	1
	Quiescent Current Change	$I_L \le 500 \text{mA}, 8 \text{V} \le \text{V}_1 \le 25 \text{V}$		-0.8	0.8	mA	1, 2, 3
		5mA, ≤ I _L ≤ 1.0A		-0.5	0.5	mA	1, 2, 3
Vo				4.80	5.20	V	1
		$V_1 = 8V$		4.75	5.25	V	1, 2, 3
	Output Voltage	$V_I = 8V, I_L = 1A$		4.75	5.25	V	1, 2, 3
		$V_{I} = 20V$		4.75	5.25	V	1, 2, 3
		$V_{I} = 20V, I_{L} = 1A$		4.75	5.25	V	1, 2, 3
R _{Line}		$I_{L} = 500 \text{mA}, 7 \text{V} \le \text{V}_{I} \le 25 \text{V}$		-50	50	mV	1, 2, 3
		$I_L = 1A, 7.3V \le V_I \le 20V$		-50	50	mV	1
	Line Regulation	$I_{L} = 1A, 8.0V \le V_{I} \le 20V$		-50	50	mV	2, 3
		$I_L = 1A, 8V \le V_I \le 12V$		-25	25	mV	1, 2, 3
R _{Load}		$5mA \le I_L \le 1.5A$		-50	50	mV	1
	Load Regulation	$5mA \le I_L \le 1.0A$		-50	50	mV	2, 3
		$250\text{mA} \le \text{I}_{\text{L}} \le 750\text{mA}$		-25	25	mV	1
I _{OS}	Current Limit			-4.0	-0.02	А	1
	Current Limit	V _I = 35V		-2.0	-0.02	А	1

LM140K–5.0 Electrical Characteristics AC Parameters

The following conditions apply, unless otherwise specified.

AC: $V_1 = 10V$, $I_L = 5mA$

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
RR	Ripple Rejection	f = 120Hz, I _L = 350mA, e _I = 1V _{RMS}		68		dB	4



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LM140K–12 Electrical Characteristics DC Parameters

The following conditions apply, unless otherwise specified. DC: V_I = 19V, I_L = 5mA

Symbol	Parameter	Conditions	Notes	Min	Мах	Unit	Sub- groups
l _Q	Quieseent Current	1 10			6.0	mA	1
	Quiescent Current	$I_{L} = 1A$			7.0	mA	2, 3
Δl _Q		$I_L = 1A, 15.5V \le V_I \le 27V$		-0.8	0.8	mA	1
	Quiescent Current Change	$I_L = 500 \text{mA} 15 \text{V} \le \text{V}_1 \le 30 \text{V}$		-0.8	0.8	mA	1, 2, 3
		$5mA \le I_L \le 1A$		-0.5	0.5	mA	1, 2, 3
Vo				11.5	12.5	V	1
		V ₁ = 15.5V		11.4	12.6	V	1, 2, 3
	Output Voltage	$V_{I} = 15.5V, I_{L} = 1A$		11.4	12.6	V	1, 2, 3
		$V_1 = 27V$		11.4	12.6	V	1, 2, 3
		$V_{I} = 27V, I_{L} = 1A$		11.4	12.6	V	1, 2, 3
R _{Line}		$I_L = 500 \text{mA}, \ 14.5 \text{V} \le \text{V}_I \le 25 \text{V}$		-120	120	mV	1, 2, 3
	Line Devulation	$I_L = 1A, 14.6V \le V_I \le 27V$		-120	120	mV	1
	Line Regulation	$I_L = 1A, 15.0V \le V_I \le 27V$		-120	120	mV	2, 3
		$I_{L} = 1A, 16V \le V_{I} \le 22V$		-60	60	mV	1, 2, 3
R _{Load}		$5mA \le I_L \le 1.5A$		-120	120	mV	1
	Load Regulation	$5mA \le I_L \le 1.0A$		-120	120	mV	2, 3
		$250mA \le I_L \le 750mA$		-60	60	mV	1
I _{OS}	Ourseast Linsit	V ₁ = 17V		-3.5	-0.02	А	1
	Current Limit	V ₁ = 35V		-2.0	-0.02	А	1

LM140K–12 Electrical Characteristics AC Parameters

The following conditions apply, unless otherwise specified.

AC: $V_I = 19V$, $I_L = 5mA$

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
RR	Ripple Rejection	f = 120Hz, I _L = 350mA, e _I = 1V _{RMS}		61		dB	4



LM140K–15 Electrical Characteristics DC Parameters

The following conditions apply, unless otherwise specified. DC: V_I = 23V, I_L = 5mA

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups	
l _Q	Ouissesset Current				6.0	mA	1	
	Quiescent Current	$I_{L} = 1A$			7.0	mA	2, 3	
Δl _Q		$I_{L} = 1A, 18.5V \le V_{I} \le 30V$		-0.8	0.8	mA	1	
	Quiescent Current Change	$I_L = 500 \text{mA}, \ 18.5 \text{V} \le \text{V}_{\text{I}} \le 30 \text{V}$	-0.8	0.8	mA	2, 3		
		$5mA \le I_L \le 1A$		-0.5	0.5	mA	1, 2, 3	
Vo				14.4	15.6	V	1	
		V _I = 18.5V		14.25	15.75	V	1, 2, 3	
	Output Voltage	$V_{I} = 18.5V, I_{L} = 1A$		14.25	15.75	V	1, 2, 3	
		$V_1 = 30V$		14.25	15.75	V	1, 2, 3	
		$V_{I} = 30V, I_{L} = 1A$		14.25	15.75	V	1, 2, 3	
R _{Line}		$I_L = 500 \text{mA}, 17.5 \text{V} \le \text{V}_I \le 30 \text{V}$		-150	150	mV	1	
	Line Degulation	$I_L = 500 \text{mA}, \ 18.5 \text{V} \le \text{V}_{\text{I}} \le 30 \text{V}$		-150	150	mV	2, 3	
	Line Regulation	$I_{L} = 1A, 17.7V \le V_{I} \le 30V$		-75	75	mV	1	
		$I_L = 1A, 20V \le V_I \le 26V$		-75	75	mV	1, 2, 3	
R _{Load}		$5mA \le I_L \le 1.5A$		-150	150	mV	1	
	Load Regulation	$5\text{mA} \le \text{I}_{\text{L}} \le 1.0\text{A}$		-150	150	mV	2, 3	
		$250\text{mA} \le \text{I}_{\text{L}} \le 750\text{mA}$		-75	75	mV	1	
I _{OS}	Ourseast Linsit	V ₁ = 20V		-3.5	-0.02	А	1	
	Current Limit	V ₁ =35V		-2.0	-0.02	А	1	

LM140K–15 Electrical Characteristics AC Parameters

The following conditions apply, unless otherwise specified.

AC: $V_1 = 23V$, $I_L = 5mA$

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
RR	Ripple Rejection	f = 120Hz, I _L = 350mA, e _I = 1V _{RMS}		60		dB	4



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REVISION HISTORY SECTION

Released	Revision	Section	Originator	Changes
02/21/06	A	New Release, Corporate format	L. Lytle	6 MDS data sheets converted into one Corp. data sheet format. The drift tables were eliminated from the 883 section since it did not apply. MDS data sheets MNLM140-05H Rev 0B0, MNLM140-05-K Rev. 0C0, MNLM140-12H Rev 0A0, MNLM140-12K Rev 0B0, MNLM140- 15H Rev 0A0, and MNLM140-15K Rev 0B0 will be archived.
05/02/13	В			Changed layout of National Data Sheet to TI format.



10-Dec-2020

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
LM140H-12/883	ACTIVE	то	NDT	3	20	Non-RoHS & Non-Green	Call TI	Call TI	-55 to 125	LM140H-12/883 Q AC O LM140H-12/883 Q >T	Samples
LM140H-15/883	ACTIVE	то	NDT	3	20	Non-RoHS & Non-Green	Call TI	Call TI	-55 to 125	LM140H-15/883 Q AC O LM140H-15/883 Q >T	Samples
LM140H-5.0/883	ACTIVE	то	NDT	3	20	Non-RoHS & Non-Green	Call TI	Call TI	-55 to 125	LM140H-5.0/883 Q A CO LM140H-5.0/883 Q > T	Samples
LM140K-12/883	ACTIVE	то	К	2	50	Non-RoHS & Non-Green	Call TI	Call TI	-55 to 125	(LM120H-15P+, LM14 0K-12) /883 Q ACO /883 Q >T	Samples
LM140KG-12 MD8	ACTIVE	DIESALE	Y	0	221	RoHS & Green	Call TI	Level-1-NA-UNLIM	-55 to 125		Samples
LM140KG-5 MD8	ACTIVE	DIESALE	Y	0	221	RoHS & Green	Call TI	Level-1-NA-UNLIM	-55 to 125		Samples

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (CI) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <= 1000ppm threshold. Antimony trioxide based flame retardants must also meet the <= 1000ppm threshold requirement.

⁽³⁾ MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

⁽⁴⁾ There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.



PACKAGE OPTION ADDENDUM

10-Dec-2020

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

⁽⁶⁾ Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

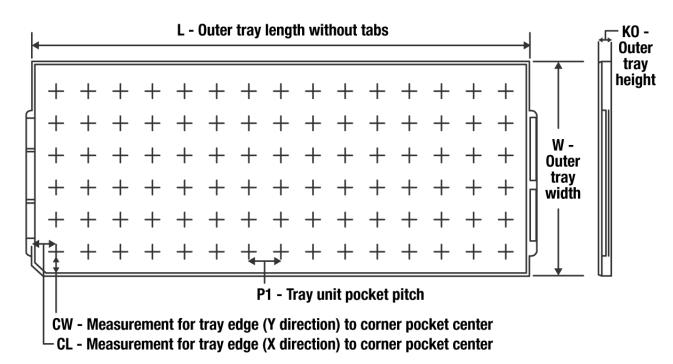
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PACKAGE MATERIALS INFORMATION

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TRAY



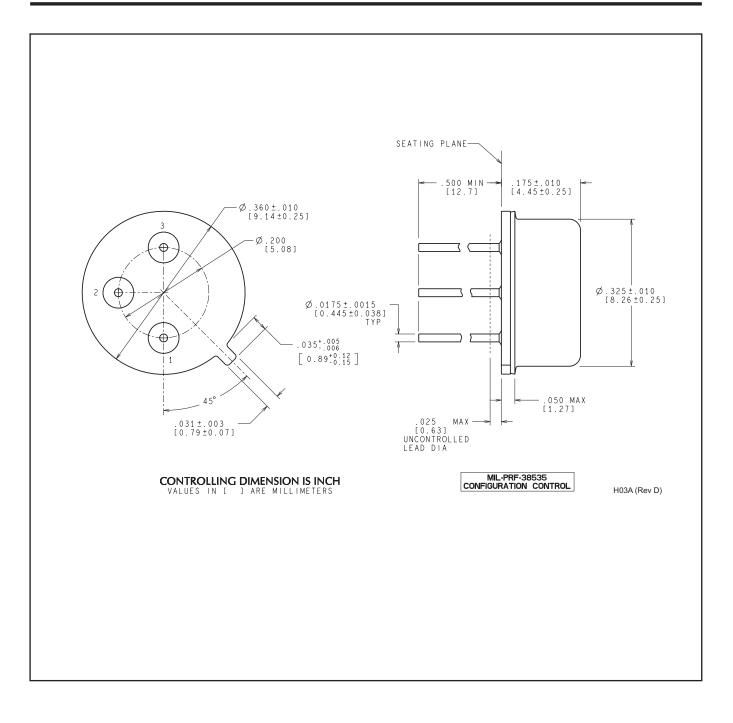
Chamfer on Tray corner indicates Pin 1 orientation of packed units.

Device	Package Name	Package Type	Pins	SPQ	Unit array matrix	Max temperature (°C)	L (mm)	W (mm)	K0 (µm)	P1 (mm)	CL (mm)	CW (mm)
LM140H-12/883	NDT	TO-CAN	3	20	2 X 10	150	126.49	61.98	8890	11.18	12.95	18.54
LM140H-15/883	NDT	TO-CAN	3	20	2 X 10	150	126.49	61.98	8890	11.18	12.95	18.54
LM140H-5.0/883	NDT	TO-CAN	3	20	2 X 10	150	126.49	61.98	8890	11.18	12.95	18.54
LM140K-12/883	К	TO-CAN	2	50	9 X 6	NA	292.1	215.9	25654	3.87	22.3	25.4

*All dimensions are nominal

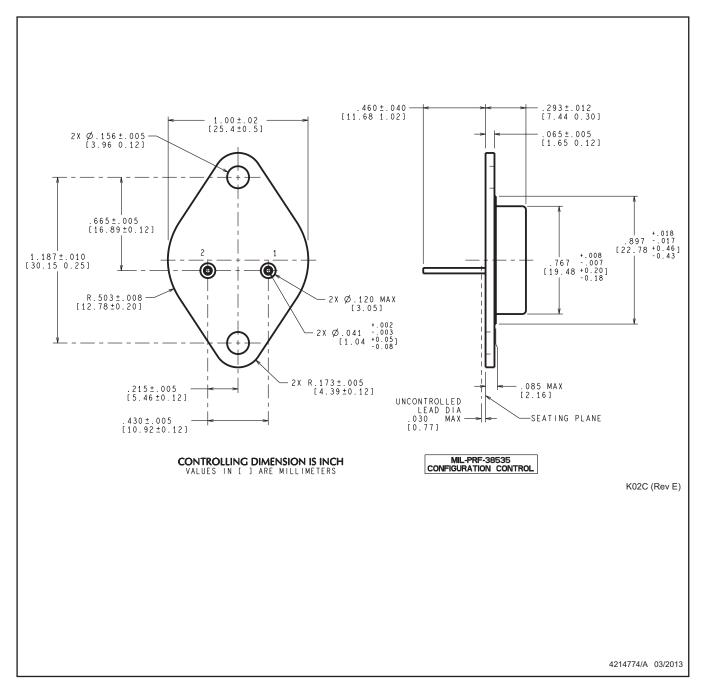


5-Jan-2022





K0002C



NOTES:

1. All linear dimensions are in millimeters. Dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.

2. This drawing is subject to change without notice.

3. Leads not to be bent greater than 15°



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