

Product Specification

SPECIFICATION FOR APPROVAL

() Final Specification

Title	15.0" XGA TFT LCD
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BUYER	-
MODEL	-

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LM150X08
SUFFIX	A4K3

*When you obtain standard approval,
please use the above model name without suffix

APPROVED BY SIGNATURE DATE / / /	APPROVED BY SIGNATURE DATE S.G.Hong / G.Manager REVIEWED BY P.Y.Kim / Manager PREPARED BY S.H.Kim / Engineer
Please return 1 copy for your confirmation with your signature and comments.	MNT Products Engineering Dept. LG. Philips LCD Co., Ltd



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RECORD OF REVISIONS

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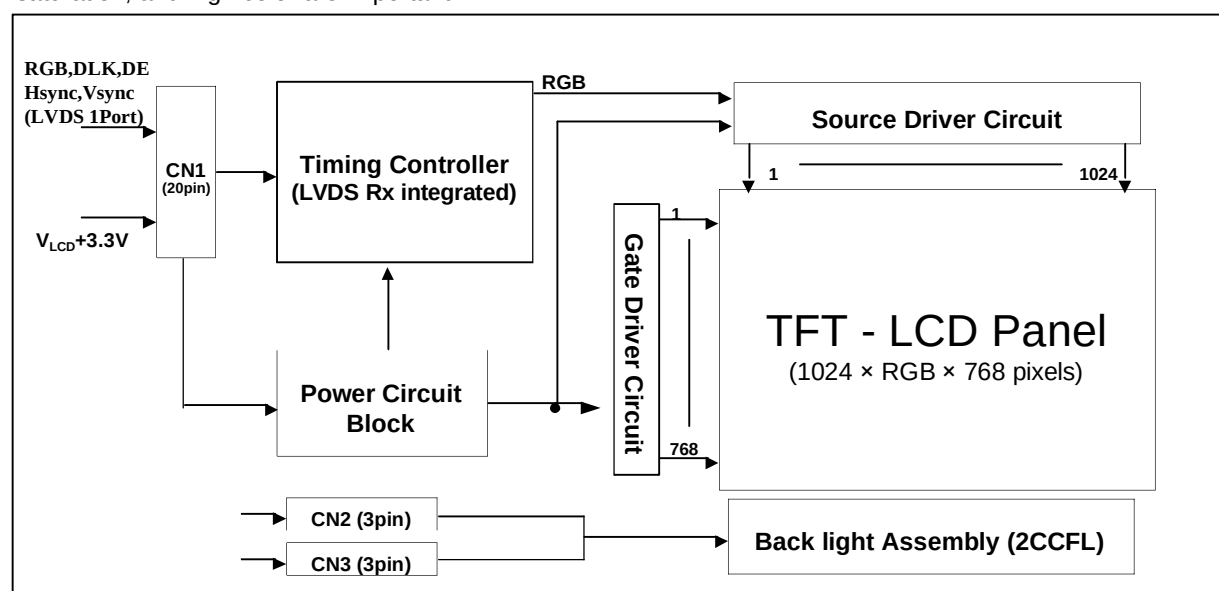
1. General Description

LM150X08 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp(CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. It has a 15.0 inch diagonally measured active display area with XGA resolution (768 vertical by 1024 horizontal pixel array).

Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus, presenting a palette of more than 16M colors.

It has been designed to apply the 8-bit 1 port LVDS interface.

It is intended to support applications where high brightness, super wide viewing angle, high color saturation, and high color are important.



General Features

Active Screen Size	15.0 inches(38.016cm) diagonal (Aspect ratio 4:3)
Outline Dimension	326.5(H) x 253.5 (V) x 11.2(D) mm (Typ.)
Pixel Pitch	0.297mm x 0.297mm
Pixel Format	1024 horiz. By 768 vert. Pixels RGB strip arrangement
Color Depth	16M colors (6-bit with FRC)
Luminance, White	250 cd/m ² (Center 1 points Typ.)
Viewing Angle (CR>10)	R/L 130(Typ.), U/D 100(Typ)
Power Consumption	Total 10.41 Watt(Typ.) (1.45Watt@V _{LCD} , 8.96 Watt@250cd/[Lamp=8mA])
Weight	1,000 g (Typ.)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Hard coating(3H) & Anti-glare (Haze 13%) treatment of the front polarizer



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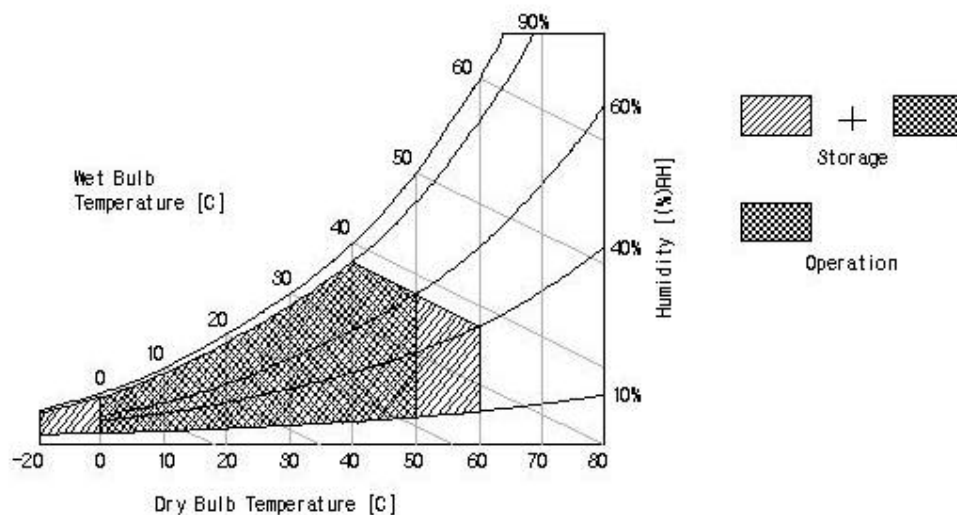
2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Values		Units	Notes
		Min	Max		
Power Input Voltage	V _{LCD}	-0.3	+3.6	Vdc	at 25 ± 2 °C
Operating Temperature	T _{OP}	0	50	°C	1
Storage Temperature	T _{ST}	-20	60	°C	1
Operating Ambient Humidity	H _{OP}	10	90	%RH	1
Storage Humidity	H _{ST}	10	90	%RH	1

Note : 1. Temperature and relative humidity range are shown in the figure below.
 Wet bulb temperature should be 39 °C Max, and no condensation of water.





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3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCDs.

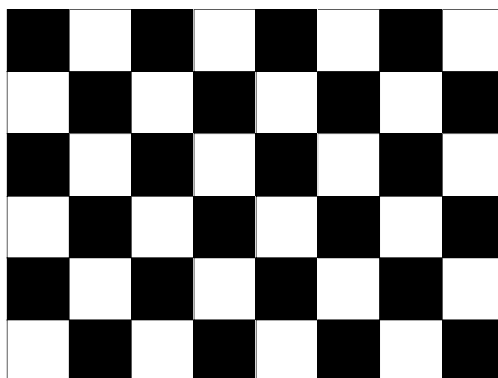
Table 2_1. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Values			Unit	Notes
		Min	Typ	Max		
MODULE :						
Power Supply Input Voltage	V _{LCD}	3.15	3.3	3.45	Vdc	
Power Supply Input Current	I _{LCD}	-	440	505	mA	1
		-	520	675	mA	2
Power Consumption	P _{LCD}	-	1.45	1.66	Watt	1
Rush current	I _{RUSH}	-	-	2.0	A	3

Note :

1. The specified current and power consumption are under the $V_{LCD}=3.3V$, $25 \pm 2^{\circ}C$, $f_v=60Hz$ condition whereas mosaic pattern(8 x 6) is displayed and f_v is the frame frequency.
2. The current is specified at the maximum current pattern.
3. The duration of rush current is about 2ms and rising time of power Input is 1ms(min.).

White : 255Gray
 Black : 0Gray



Mosaic Pattern(8 x 6)



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Table 2_2. ELECTRICAL CHARACTERISTICS

Parameter		Symbol	Values			Unit	Notes
			Min	Typ	Max		
LAMP :							
Operating Voltage		V _{BL}	550(9mA)	560(8mA)	650(3mA)	V _{RMS}	1, 2
Operating Current		I _{BL}	3.0	8.0	9.0	mA _{RMS}	1
Established Starting Voltage		V _S					1, 3
	at 25 °C				850	V _{RMS}	
	at 0 °C				1,100	V _{RMS}	
Operating Frequency		f _{BL}	45	60	80	kHz	4
Discharge Stabilization Time		T _S			3	Min	1, 5
Power Consumption		P _{BL}		8.96	9.86	Watt	6
Life Time			40,000			Hrs	1, 7

Note : The design of the inverter must have specifications for the lamp in LCD Assembly.

The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter.

When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter (no lighting, flicker, etc) never occurs. When you confirm it, the LCD-Assembly should be operated in the same condition as installed in you instrument.

? Do not attach a conducting tape to lamp connecting wire.

If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.

1. Specified values are for a single lamp.

2. Operating voltage is measured at 25 ± 2°C. The variance of the voltage is ± 10%.

3. The voltage above V_S should be applied to the lamps for more than 1 second for start-up.

(Inverter open voltage must be more than lamp starting voltage.)

Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.

4. Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.

5. Let's define the brightness of the lamp after being lighted for 5 minutes as 100%.

T_S is the time required for the brightness of the center of the lamp to be not less than 95%.

6. The lamp power consumption shown above does not include loss of external inverter.

The used lamp current is the lamp typical current. (P_{BL} = V_{BL} × I_{BL} × N_{Lamp})

7. The life is determined as the time at which brightness of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at 25 ± 2°C.

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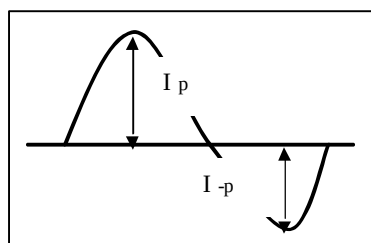
8. The output of the inverter must have symmetrical(negative and positive) voltage waveform and symmetrical current waveform (Unsymmetrical ratio is less than 10%). Please do not use the inverter which has unsymmetrical voltage and unsymmetrical current and spike wave.

Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following.

It shall help increase the lamp lifetime and reduce leakage current.

- The asymmetry rate of the inverter waveform should be less than 10%.
- The distortion rate of the waveform should be within $\sqrt{2} \pm 10\%$.

* Inverter output waveform had better be more similar to ideal sine wave.



* Asymmetry rate:

$$|I_p - I_{-p}| / I_{rms} \times 100\%$$

* Distortion rate

$$I_p \text{ (or } I_{-p}) / I_{rms}$$

9. The inverter which is combined with this LCM, is highly recommended to connect coupling(ballast) condenser at the high voltage output side. When you use the inverter which has not coupling(ballast) condenser, it may cause abnormal lamp lighting because of biased mercury as time goes.
10. In case of edgy type back light with over 4 parallel lamps, input current and voltage wave form should be synchronized



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3-2. Interface Connections

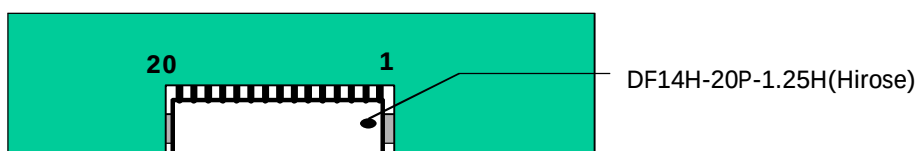
- LCD Connector(CN1):DF14H-20P-1.25H (Manufactured by Hirose) or Equivalent
- Mating Connector : DF14H-20S-1.25C (Manufactured by Hirose) or Equivalent

Table 3. MODULE CONNECTOR(CN1) PIN CONFIGURATION

Pin No	Symbol	Description	Output Pin # (LVDS Tx)
1	VLCD	Power Supply +3.3V	
2	VLCD	Power Supply +3.3V	
3	GND	Power Ground.	
4	GND	Power Ground	
5	RXIN0-	LVDS Receiver Signal(-)	PIN#48
6	RXIN0+	LVDS Receiver Signal(+)	PIN#47
7	GND	Ground	
8	RXIN1-	LVDS Receiver Signal(-)	PIN#46
9	RXIN1+	LVDS Receiver Signal(+)	PIN#45
10	GND	Ground	
11	RXIN2-	LVDS Receiver Signal(-)	PIN#42
12	RXIN2+	LVDS Receiver Signal(+)	PIN#41
13	GND	Ground	
14	RXCLK IN-	LVDS Receiver Clock Signal(-)	PIN#40
15	RXCLK IN+	LVDS Receiver Clock Signal(+)	PIN#39
16	GND	Ground	
17	RXIN3-	LVDS Receiver Signal(-)	PIN#38
18	RXIN3+	LVDS Receiver Signal(+)	PIN#37
19	GND	Ground	
20	GND	Ground	Note 1

- Note: 1. 20nd Pin should be ground.
 2. All GND(ground) pins should be connected together and to Vss which should also be connected to the LCD's metal frame.
 3. All V_{LCD} (power input) pins should be connected together.
 4. Input Level of LVDS signal is based on the IEA 664 Standard.

Rear view of LCM





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Table 4. REQUIRED SIGNAL ASSIGNMENT FOR LVDS Transmitter (DS90C385 or Compatible)

Pin #	Pin Name	Symbol	Description	Output
51	TxIN0	Red0 [LSB]	Red Pixel Data	Rx0- Rx0+
52	TxIN1	Red1	Red Pixel Data	
54	TxIN2	Red2	Red Pixel Data	
55	TxIN3	Red3	Red Pixel Data	
56	TxIN4	Red4	Red Pixel Data	
2	TxIN5	Red7 [MSB]	Red Pixel Data	Rx3- Rx3+
3	TxIN6	Red5	Red Pixel Data	Rx0- Rx0+
4	TxIN7	Green0 [LSB]	Green Pixel Data	
6	TxIN8	Green1	Green Pixel Data	Rx1- Rx1+
7	TxIN9	Green2	Green Pixel Data	Rx3- Rx3+
8	TxIN10	Green6	Green Pixel Data	
10	TxIN11	Green7 [MSB]	Green Pixel Data	
11	TxIN12	Green3	Green Pixel Data	Rx1- Rx1+
12	TxIN13	Green4	Green Pixel Data	
14	TxIN14	Green5	Green Pixel Data	
15	TxIN15	Blue0 [LSB]	Blue Pixel Data	Rx3- Rx3+
16	TxIN16	Blue6	Blue Pixel Data	
18	TxIN17	Blue7 [MSB]	Blue Pixel Data	Rx1- Rx1+
19	TxIN18	Blue1	Blue Pixel Data	Rx2- Rx2+
20	TxIN19	Blue2	Blue Pixel Data	
22	TxIN20	Blue3	Blue Pixel Data	
23	TxIN21	Blue4	Blue Pixel Data	
24	TxIN22	Blue5	Blue Pixel Data	Rx3- Rx3+
25	TxIN23	RES		
27	TxIN24	Hsync.	No connection, If unnecessary	Rx2- Rx2+
28	TxIN25	Vsync.	No connection, If unnecessary	
30	TxIN26	EN	Data Enable	Rx3- Rx3+
50	TxIN27	Red6	Red Pixel Data	

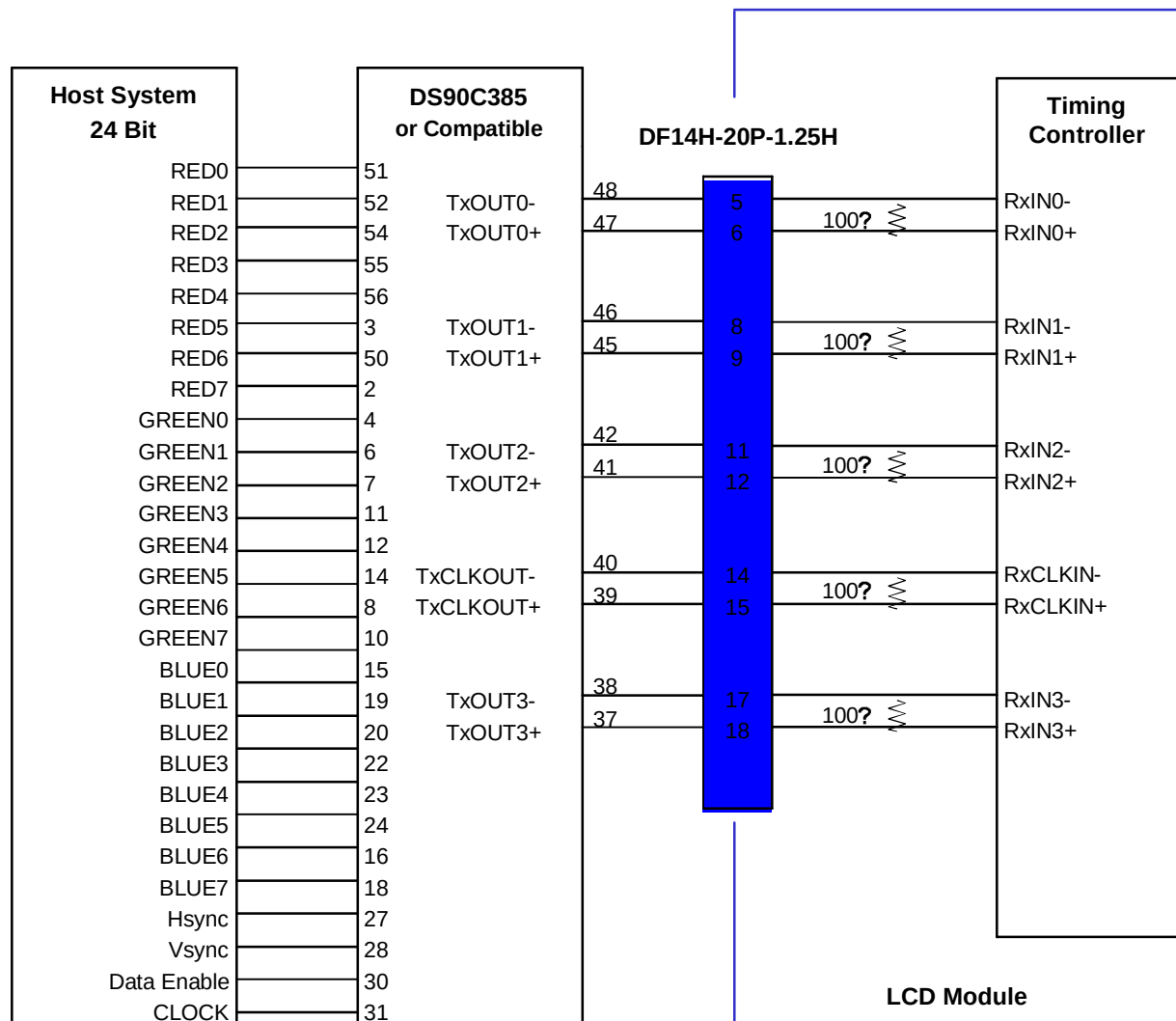
Notes : 1. Refer to LVDS Transmitter Data Sheet for detail descriptions.
2. 7 means MSB and 0 means LSB at R,G,B pixel data.



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Table 5. REQUIRED SIGNAL ASSIGNMENT FOR LVDS TRANSMITTER (DS90C385 or Compatible)



Note : 1. The LCD Module uses a 100Ohm resistor between positive and negative lines of each receiver input.



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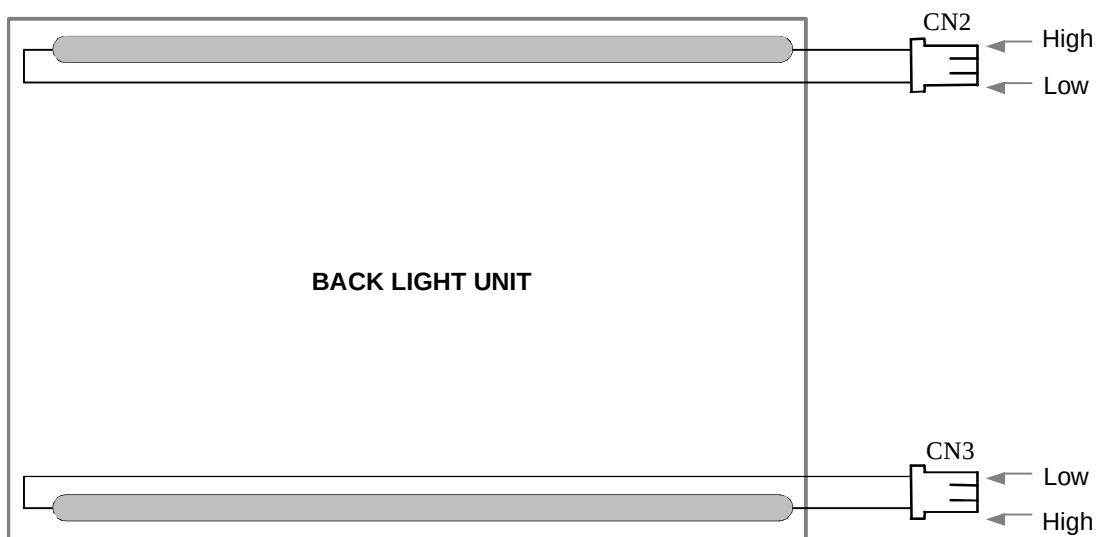
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Table 6. BACKLIGHT CONNECTOR PIN CONFIGURATION(CN2,CN3)

The backlight interface connector is a model BHR-03VS-1(CN2/CN3), manufactured by JST. The mating connector part number is SM02(8.0)B-BHS-1-TB or equivalent. The pin configuration for the connector is shown in the table below.

No	Pin	Symbol	Description	Notes
CN2	1	H1	Power supply for lamp 1(High voltage side), Pink	1
	2	NC	No Connect	
	3	L1	Power supply for lamp 1 (Low voltage side), Black	2
CN3	1	H2	Power supply for lamp 2 (High voltage side), Pink	1
	2	NC	No Connect	
	3	L2	Power supply for lamp 2 (Low voltage side), Black	2

Notes: 1. The high voltage power terminal is colored Pink.
2. The low voltage pin is colored Black.





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3-3. Signal Timing Specifications

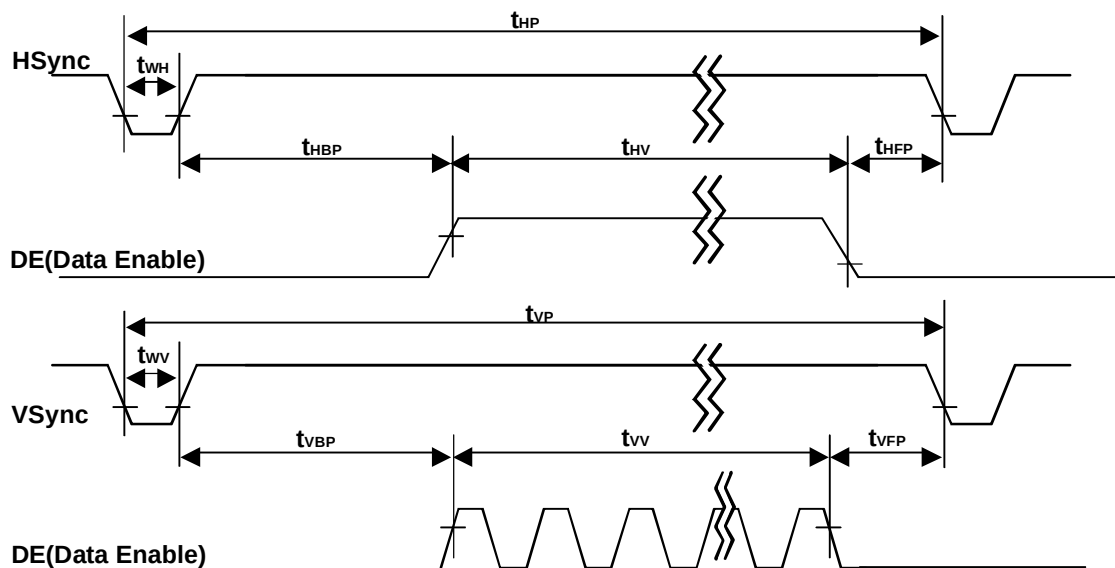
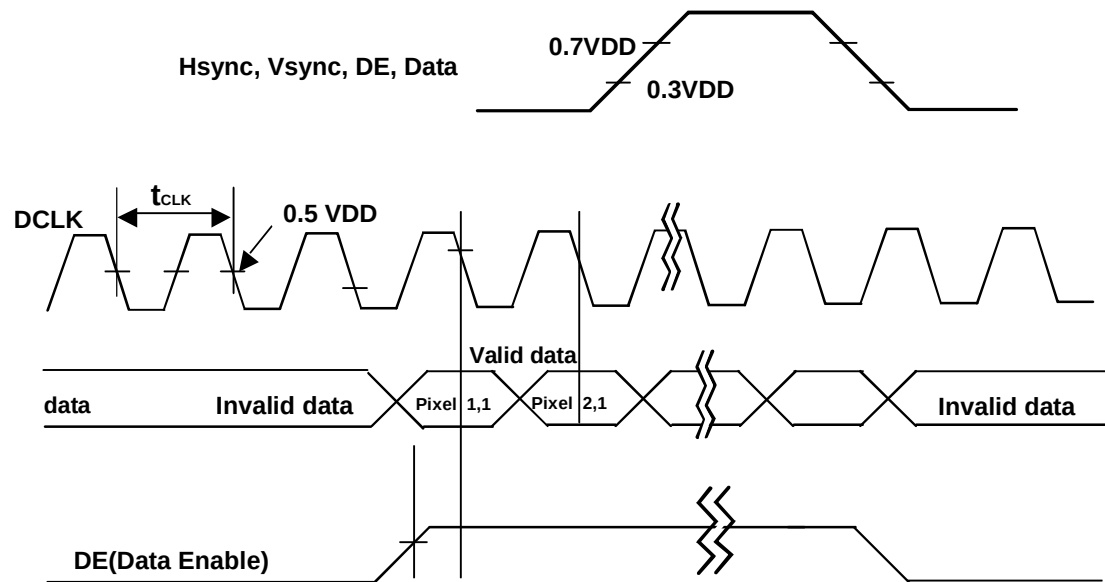
This is the signal timing required at the input of the LVDS Transmitter. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Table 7. Timing Table

ITEM	Symbol		Min	Typ	Max	Unit	Note
DCLK	Period	tCLK	12.70	15.38	20.00	ns	
	Frequency	-	50	65	79	MHz	
Hsync	Period	tHP	1096	1344	1360	tCLK	
	Width	tWH	8	136	-	tCLK	
Vsync	Period	tVP	800	806	862	tHP	
	Frequency	f _V	50	60	75	Hz	
	Width	tWV	2	6	-	tHP	
DE (Data Enable)	Horizontal Valid	tHV	1024	1024	1024	tCLK	
	Horizontal Back Porch	tHBP	8	160	-		
	Horizontal Front Porch	tHFP	8	24	-		
	-	-	-	-	-		
	Vertical Valid	tV _V	768	768	768	tHP	
	Vertical Back Porch	tVBP	4	29	-		
	Vertical Front Porch	tVFP	2	3	-		
	-	-	-	-	-		

Note: Hsync period and Hsync width-active should be even number times of tCLK. If the value is odd number times of tCLK, display control signal can be asynchronous. In order to operate this LCM a Hsync, Vsyn, and DE(data enable) signals should be used.

1. : The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rates.
2. Vsync and Hsync should be keep the above specification.
3. Hsync Period, Hsync Width, and Horizontal Back Porch should be any times of of character number(8).

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3-4. Signal Timing Waveforms




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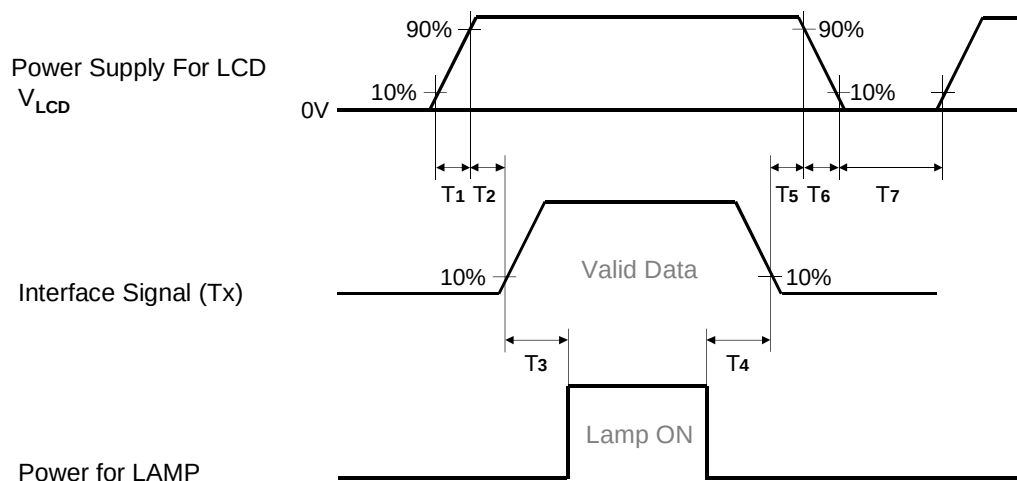
3-5. Color Data Reference

The Brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 8. COLOR DATA REFERENCE

Color		Input Color Data																							
		RED								GREEN								BLUE							
		MSB						LSB		MSB						LSB		MSB						LSB	
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	...	...								...								...							
	RED (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	...	...								...								...							
	GREEN (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
BLUE	BLUE (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
	...	...								...								...							
	BLUE (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note : Users should be input true 8 Bit data streams via LVDS transmitter.


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3-6. Power Sequence

Table 9. POWER SEQUENCE

Parameter	Values			Units
	Min	Typ	Max	
T1	0.5	-	10	ms
T2	0.01	-	50	ms
T3	200	-	-	ms
T4	200	-	-	ms
T5	0.5	-	50	ms
T6	0.5	-	10	ms
T7	500	-	-	ms

- Notes :
1. Please avoid floating state of interface signal at invalid period.
 2. When the interface signal is invalid, be sure to pull down the power supply for LCD V_{LCD} to 0V.
 3. Lamp power must be turn on after power supply for LCD an interface signal are valid.

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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at $25 \pm 2^\circ\text{C}$. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of $?$ and $?$ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.

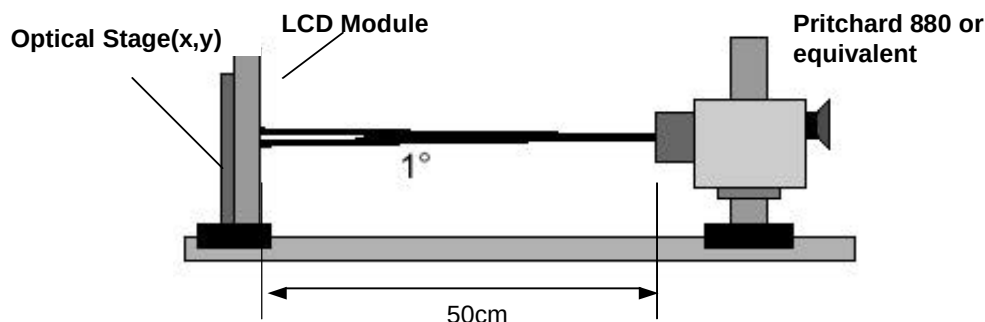


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 10. OPTICAL CHARACTERISTICS

$T_a = 25 \pm 2^\circ\text{C}$, $V_{\text{LCD}} = 3.3\text{V}$, $f_v = 60\text{Hz}$, $D_{\text{clk}} = 65\text{MHz}$, $I_{\text{Lamp}} = 8\text{mA}$

Parameter		Symbol	Values			Units	Notes
			Min	Typ	Max		
Contrast Ratio		CR	300	400	-		1
Surface Luminance, white		L_{WH}	200	250	-	cd/m^2	2
Luminance Variation		$?$ WHITE 9P	-		1.33	-	3
Response Time	Rise Time	Tr_R	-	5	10	ms	4
	Decay Time	Tr_D	-	11	22	ms	
Color Coordinates [CIE1931]	RED	R_x	Typ -0.03	0.631	Typ +0.03		
		R_y		0.347			
	GREEN	G_x		0.306			
		G_y		0.590			
	BLUE	B_x		0.150			
		B_y		0.088			
	WHITE	W_x		0.313			
		W_y		0.329			
Viewing Angle (CR>10)							
	x axis, right ($?$ = 0°)	$?r$	55	65	-	degree	5
	x axis, left ($?$ = 180°)	$?l$	55	65	-		
	y axis, up ($?$ = 90°)	$?u$	40	45	-		
	y axis, down ($?$ = 270°)	$?d$	50	55	-		
Gray Scale			-	2.2	-		6
Crosstalk			-	-	2.0	%	



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Notes 1. Contrast Ratio(CR) is defined mathematically as :

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels at center point (1)}}$$

- Surface luminance is luminance value at center point (1) across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 2.
- The variation in surface luminance , ? WHITE is defined as :

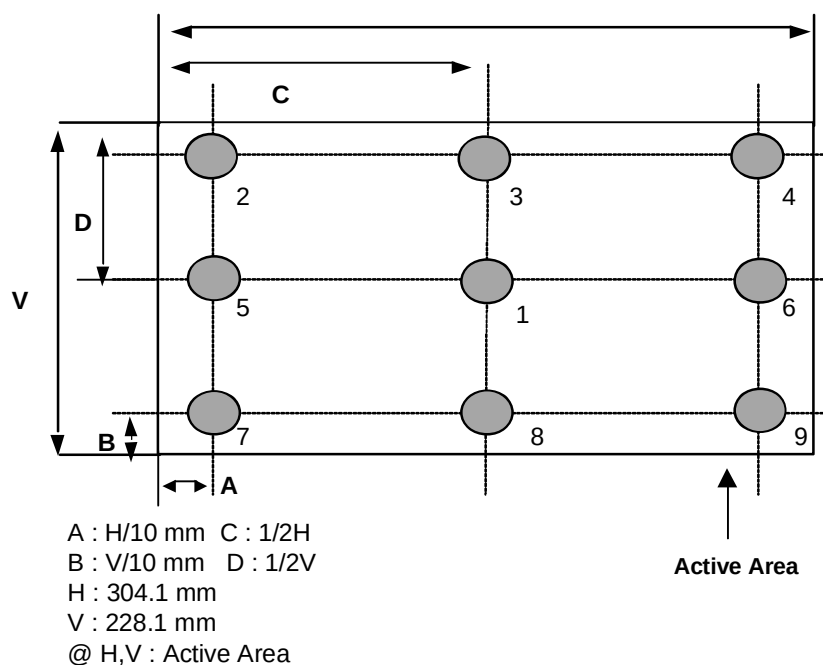
$$\text{? WHITE(9P)} = \frac{\text{Maximum}(L_{on1}, L_{on2}, L_{on3}, \dots, L_{on9})}{\text{Minimum}(L_{on1}, L_{on2}, L_{on3}, \dots, L_{on9})}$$
 Where L_{on1} to L_{on9} are the luminance with all pixels displaying white at 9 locations .
 For more information see FIG 2.
- Response time is the time required for the display to transition from white to black(Rise Time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- Gray scale specification
 Gamma Value is approximately 2.2. For more information see Table 11.

Table 11. Gray Scale Specification

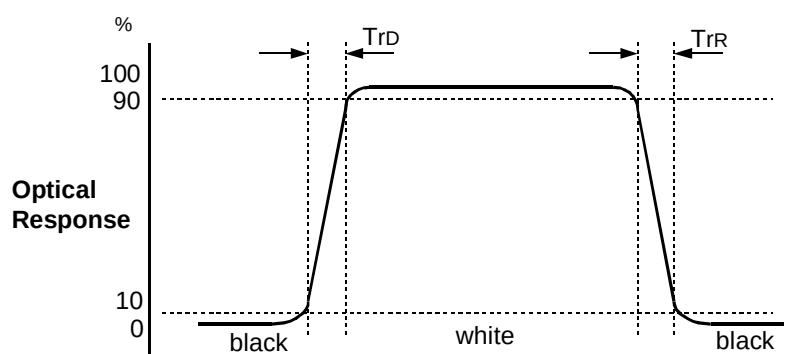
Gray Level	Luminance [%] (Typ)
L0	0.22
L15	0.34
L31	0.81
L47	2.10
L63	4.29
L79	7.46
L95	11.4
L111	16.4
L127	22.1
L143	28.7
L159	36.4
L175	45.1
L191	55.4
L207	66.2
L223	78.0
L239	90.4
L255	100

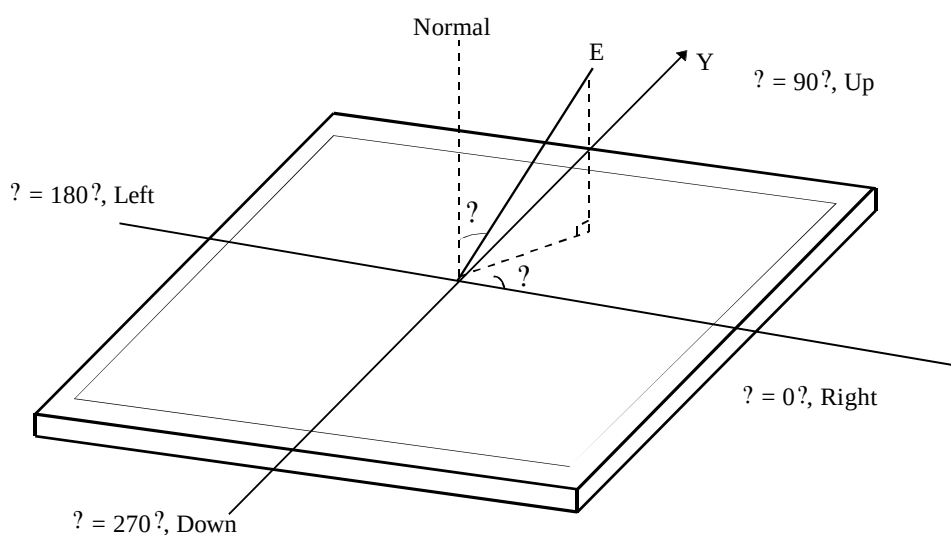
Product Specification

Measuring point for surface luminance & measuring point for luminance variation


FIG. 2 Measure Point for Luminance

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

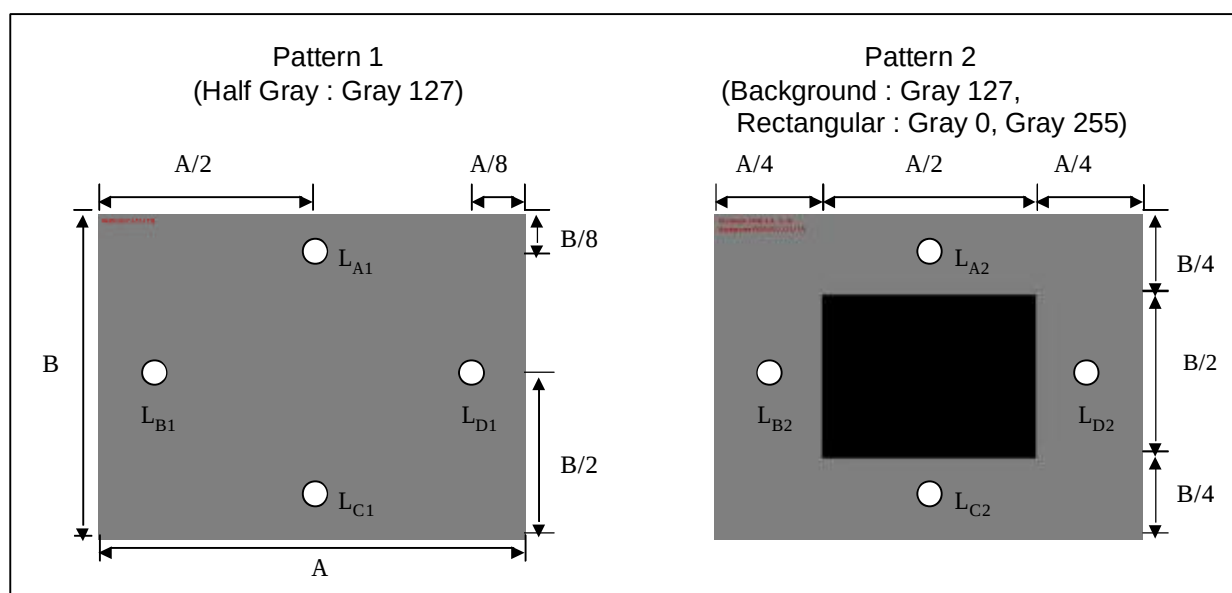

FIG. 3 Response Time

Product Specification
Dimension of viewing angle range

FIG. 4 Viewing angle

Crosstalk is defined as:

$$\left(\frac{L_{A[or C]2} - L_{A[or C]1}}{L_{A[or C]1}} \right) \times 100(\%) [\text{Vertical}],$$

$$\left(\frac{L_{B[or D]2} - L_{B[or D]1}}{L_{B[or D]1}} \right) \times 100(\%) [\text{Horizontal}]$$


FIG. 5 Crosstalk



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5. Mechanical Characteristics

The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

Outline Dimension	Horizontal	326.5mm
	Vertical	253.5mm
	Depth	11.2mm
Bezel Area	Horizontal	307.4 mm
	Vertical	231.3 mm
Active Display Area	Horizontal	304.128mm
	Vertical	228.096mm
Weight	1,000 g(Typ.), 1,050 g(Max.)	
Surface Treatment	Hard coating(3H) Anti-glare(13%) treatment of the front polarizer	

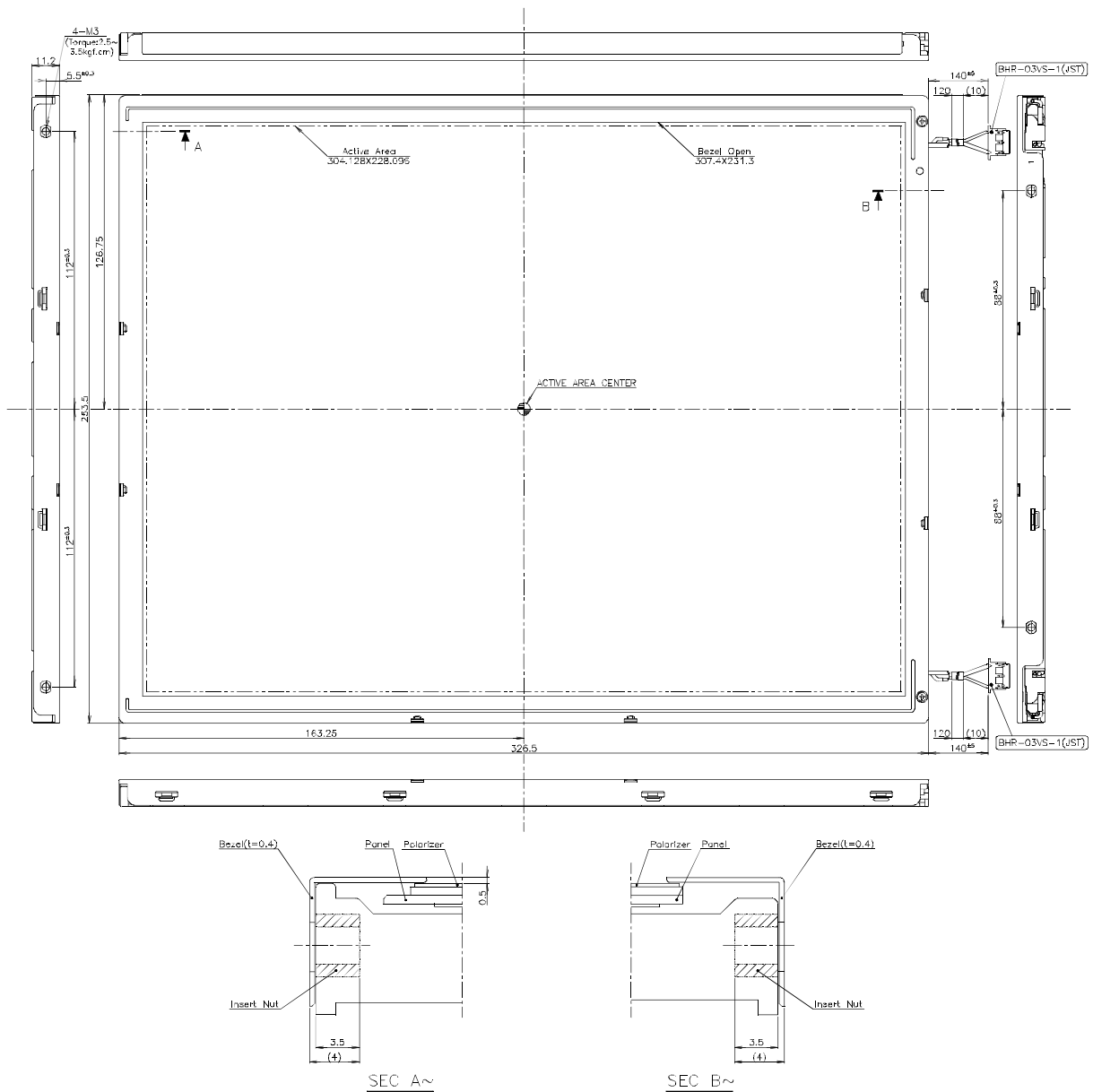
Notes : Please refer to a mechanic drawing in terms of tolerance at the next page.



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<FRONT VIEW>



Ver. 0.0

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Figure 1 is a detailed technical drawing of the back of the 3300L-0206A LCD panel. It shows the internal structure, including the source TCP (REA) and the backlight assembly (BL14H-20P-1.25H-3Hincase). The drawing includes various dimensions and labels for components and features.

Dimensions (mm):

- Top edge: 151.1, 113.3, 75.5, 37.7, 6, 37.7, 75.5, 113.3, 151.1
- Bottom edge: 151.1, 113.3, 75.5, 37.7, 6, 37.7, 75.5, 113.3, 151.1
- Left edge: 151.1, 113.3, 75.5, 37.7, 6, 37.7, 75.5, 113.3, 151.1
- Right edge: 151.1, 113.3, 75.5, 37.7, 6, 37.7, 75.5, 113.3, 151.1
- Internal dimensions: 160.15, 28.85±1, 6.45±1, 4.4, 2-ø2.4 (Depth:B)

Labels and Components:

- Supported Area (Back) By User System (9 Places)
- Source TCP (REA)
- BL14H-20P-1.25H-3Hincase
- LM15CX08 (-1)(V3) US PATENT : 5,838,139
- 6091L-0000 000000 00
- 3300L-0206A
- 3300L-0206A 1
- 1 2 3 4 5 1680-1 1203M VPC-METEC 03 04 05 06 07
- CAUTION: HANDLE WITH CARE. DO NOT TOUCH THE SURFACE OF THE LCD PANEL. IF THE SURFACE IS DAMAGED, IT MAY CAUSE DISCOLORATION OR OTHER DEFECTS. HANDLE WITH CARE.

Diagram illustrating the forces acting on a square frame (likely a bezel) during a bezel open operation. The frame is shown with four points of interest: A (top), B (bottom), C (left), and D (right). Arrows indicate the direction of forces: A downward arrow at A, an upward arrow at B, a rightward arrow at C, and a leftward arrow at D. The right side of the frame is labeled "Bezel Open" and "Active Area".

-
- A diagram showing the front of a cable jacket being marked with an 'X' pattern. A label 'Mark' points to the 'X'.

- www.DataSheet4U.com



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6. Reliability

Environment test condition

No	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0G RMS Bandwidth : 10-500Hz Duration : X,Y,Z, 20 min One time each direction
6	Shock test (non-operating)	Shock level : 120G Waveform : half sine wave, 2ms Direction : $\pm X$, $\pm Y$, $\pm Z$ One time each direction
7	Altitude operating storage / shipment	0 - 10,000 feet(3048m) 0 - 40,000 feet(12,192m)



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7. International Standards

7-1. Safety

- a) UL 60950, Third Edition, Underwriters Laboratories, Inc., Dated Dec. 11, 2000.
Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.
- b) CAN/CSA C22.2, No. 60950, Third Edition, Canadian Standards Association, Dec. 1, 2000.
Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.
- c) EN 60950 : 2000, Third Edition
IEC 60950 : 1999, Third Edition
European Committee for Electrotechnical Standardization(CENELEC)
EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998
(Including A1: 2000)



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8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A	B	C	D	E	F	G	H	I	J	K	L	M
---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C : Inch

D : Year

E : Month

F : Panel Code

G : Factory Code

H : Assembly Code

I,J,K,L,M : Serial No

Note

1. Year

Year	97	98	99	2000	2001	2002	2003	2004	2005	2006	2007
Mark	7	8	9	0	1	2	3	4	5	6	7

2. Month

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	A	B	C

3. Panel Code

Panel Code	P1 Factory	P2 Factory	P3 Factory	P4 Factory	P5 Factory	Hydis Panel
Mark	1	2	3	4	5	H

4. Factory Code

Factory Code	LPL Gumi	LPL Nanjing
Mark	K	C

5. Serial No

Serial No.	1 ~ 99,999	100,000 ~
Mark	00001 ~ 99999	A0001 ~ A9999, - - - , Z9999

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module.
This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box : 8 pcs

b) Box size : 344mm X 315mm X 410mm.



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9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
 $V = \pm 200\text{mV}$ (Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can not be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw (if not, it causes metallic foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.



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9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape.
When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.