

LM19501

LCD Module User Manual

Shenzhen TOPWAY Technology Co., Ltd.

| Rev. | Descriptions | Release Date |
|------|--------------|--------------|
| 0.1 | New release | 2006-09-28 |
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1. Basic Specifications

1.1 Display Specifications

- 1) LCD Display Mode : FSTN, Positive, Transflective
- 2) Display Color : Display Data = "1" : Dark Gray (*1)
: Display Data = "0" : Light Gray (*2)
- 3) Viewing Angle : 6 H
- 4) Driving Method : 1/128 duty, 1/12 bias
- 5) Backlight : White LED backlight

Note:

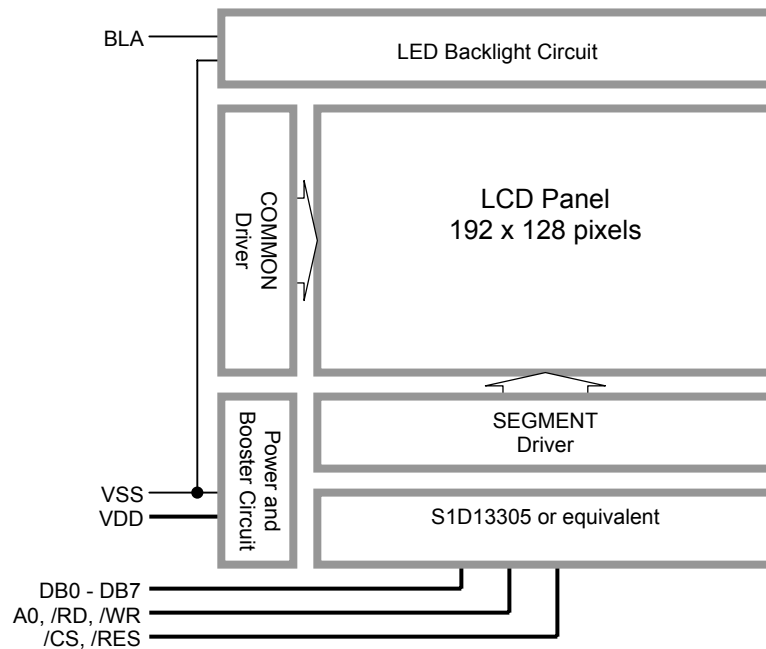
*1. Color tone may slightly change by Temperature and Driving Condition.

*2. The Color is defined as the inactive / background color

1.2 Mechanical Specifications

- 1) Outline Dimension : 98.0 x 86.0 x 12.7MAX
see attached Outline Drawing for details

1.3 Block Diagram



1.4 Terminal Functions

| Pin No. | Pin Name | I/O | Descriptions |
|---------|----------|-------|---|
| 1 | FG | Power | Frame Ground |
| 2 | VSS | Power | 0V Power Supply, GND |
| 3 | VDD | Power | Positive Power Supply |
| 4 | NC | - | No connection (leave open) |
| 5 | /RES | Input | Reset Signal: /RES = L, Reset the LCD Module /RES = H, Normal Running |
| 6 | /RD | Input | Read enable input, active LOW |
| 7 | /WR | Input | Write enable input, active LOW |
| 8 | /CS | Input | Chip Select Signal /CS=LOW: Data IO is enabled |
| 9 | A0 | Input | Data Type Select A0=H: command write, display data or cursor add read A0=L: status flag read, display data or parameter write |
| 10 | DB0 | I/O | 8-bit bi-directional data bus |
| : | : | | |
| 17 | DB7 | | |
| 18 | BLA | Power | Positive Power Supply for LED backlight |
| 19 | NC | - | No connection (leave open) |
| 20 | NC | - | No connection (leave open) |

2. Absolute Maximum Ratings

| Items | Symbol | Min. | Max. | Unit | Condition |
|-----------------------|----------|--------------|--------------|------|-----------------|
| Supply Voltage | V_{DD} | 0 | +7.0 | V | $V_{SS} = 0V$ |
| Input Voltage | V_{IN} | $V_{SS}-0.3$ | $V_{DD}+0.3$ | V | $V_{SS} = 0V$ |
| Operating Temperature | T_{OP} | -20 | +70 | °C | No Condensation |
| Storage Temperature | T_{ST} | -30 | +80 | °C | No Condensation |

Cautions:

Any Stresses exceeding the Absolute Maximum Ratings may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

3. Electrical Characteristics

3.1 DC Characteristics

$V_{SS}=0V, V_{DD}=5.0V, T_{OP}=25^{\circ}C$

| Items | Symbol | MIN. | TYP. | MAX. | Unit | Applicable Pin |
|--------------------|----------|---------------------|------|----------------------|------|----------------------------------|
| Operating Voltage | V_{DD} | 4.5 | 5.0 | 5.5 | V | VDD |
| Input High Voltage | V_{IN} | $0.6 \times V_{DD}$ | - | VDD | V | DB0~DB7, /WR, /RD, /CS, A0, /RES |
| Input Low Voltage | V_{IN} | VSS | - | $0.15 \times V_{DD}$ | V | DB0~DB7, /WR, /RD, /CS, A0, /RES |
| Operating Current | I_{DD} | - | 15.0 | 40.0 | mA | VDD |

$V_{SS}=0V, V_{DD}=3.3V, T_{OP}=25^{\circ}C$

| Items | Symbol | MIN. | TYP. | MAX. | Unit | Applicable Pin |
|--------------------|----------|---------------------|------|----------------------|------|----------------------------------|
| Operating Voltage | V_{DD} | 3.0 | 3.3 | 3.6 | V | VDD |
| Input High Voltage | V_{IN} | $0.6 \times V_{DD}$ | - | VDD | V | DB0~DB7, /WR, /RD, /CS, A0, /RES |
| Input Low Voltage | V_{IN} | VSS | - | $0.15 \times V_{DD}$ | V | DB0~DB7, /WR, /RD, /CS, A0, /RES |
| Operating Current | I_{DD} | - | 22.0 | 62.0 | mA | VDD |

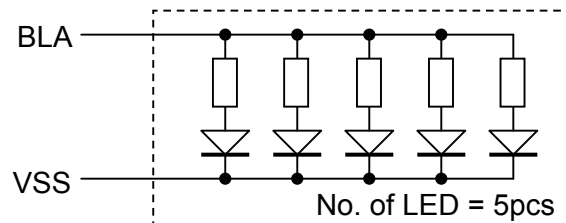
3.2 LED Backlight Circuit Characteristics

$V_{SS}=0V, I_{f_{BLA}}=85mA, T_{OP}=25^{\circ}C$

| Items | Symbol | MIN. | TYP. | MAX. | Unit | Applicable Pin |
|-----------------|---------------|------|------|------|------|----------------|
| Forward Voltage | $V_{f_{BLA}}$ | - | 5.0 | - | V | BLA |
| Forward Current | $I_{f_{BLA}}$ | - | 85 | 110 | mA | BLA |

Cautions:

Exceeding the recommended driving current could cause substantial damage to the backlight and shorten its lifetime.



3.3 AC Characteristics

3.3.1 8080 Mode

$V_{SS}=0V, V_{DD}=5.0V, T_{OP}=25^{\circ}C$

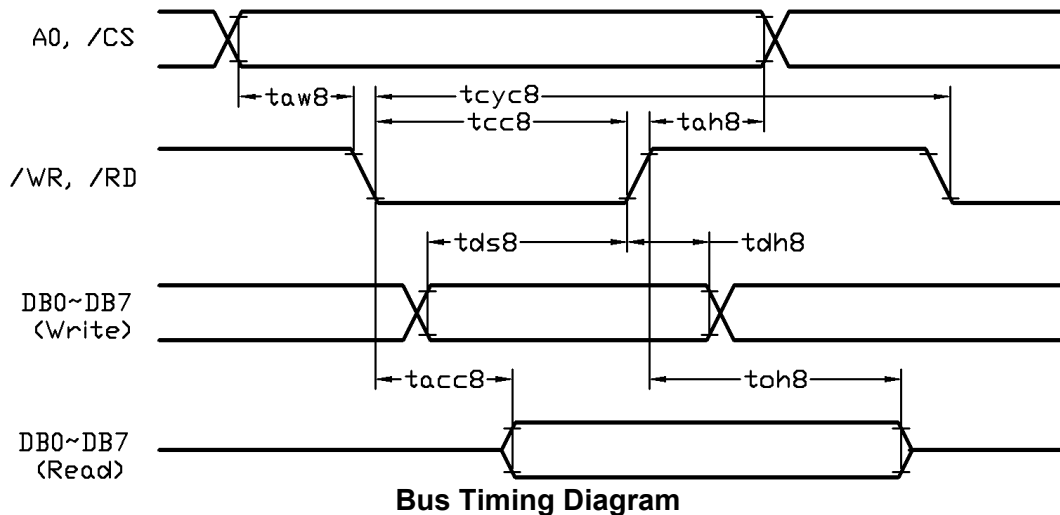
| Item | Symbol | MIN. | TYP. | MAX. | Unit |
|---------------------|--------|------|------|------|------|
| Address Hold Time | tah8 | 13 | - | - | ns |
| Address Setup Time | taw8 | 5 | - | - | ns |
| System Cycle Time | tcyc8 | (*2) | - | - | ns |
| Strobe Pulse Width | tcc8 | 150 | - | - | ns |
| Data Setup Time | tds8 | 150 | - | - | ns |
| Data Hold Time | tdh8 | 7 | - | - | ns |
| Data Access Time | tacc8 | - | - | 65 | ns |
| Output disable Time | toh8 | 13 | - | 65 | ns |

$V_{SS}=0V, V_{DD}=3.3V, T_{OP}=25^{\circ}C$

| Item | Symbol | MIN. | TYP. | MAX. | Unit |
|---------------------|--------|------|------|------|------|
| Address Hold Time | tah8 | 13 | - | - | ns |
| Address Setup Time | taw8 | 5 | - | - | ns |
| System Cycle Time | tcyc8 | (*2) | - | - | ns |
| Strobe Pulse Width | tcc8 | 190 | - | - | ns |
| Data Setup Time | tds8 | 190 | - | - | ns |
| Data Hold Time | tdh8 | 7 | - | - | ns |
| Data Access Time | tacc8 | - | - | 100 | ns |
| Output disable Time | toh8 | 13 | - | 70 | ns |

Note:

- *1. Input signal rise/fall time should be less than 20ns
- *2. For memory control and system control commands: $tcyc8=2tc+tcc8+tcea+75>tacv+245$
For all other command: $tcyc8=4tc+tcc8+30$
- *3. Please see the S1D13305 data sheet for details



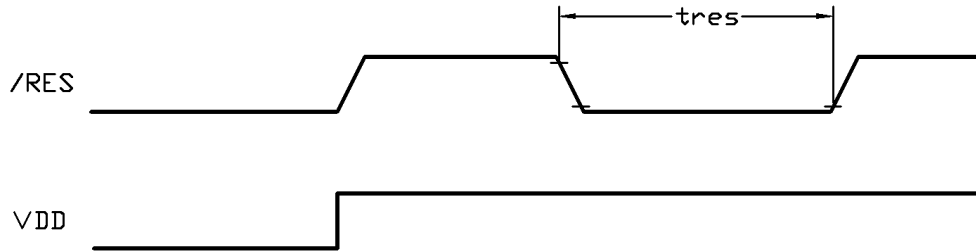
3.4 Reset Timing

$V_{SS}=0V, V_{DD}=5.0V, T_{OP}=25^{\circ}C$

| Item | Symbol | MIN. | TYP. | MAX. | Unit |
|------------|--------|------|------|------|------|
| Reset Plus | tres | 1.0 | - | - | ms |

$V_{SS}=0V, V_{DD}=3.3V, T_{OP}=25^{\circ}C$

| Item | Symbol | MIN. | TYP. | MAX. | Unit |
|------------|--------|------|------|------|------|
| Reset Plus | tres | 1.0 | - | - | ms |

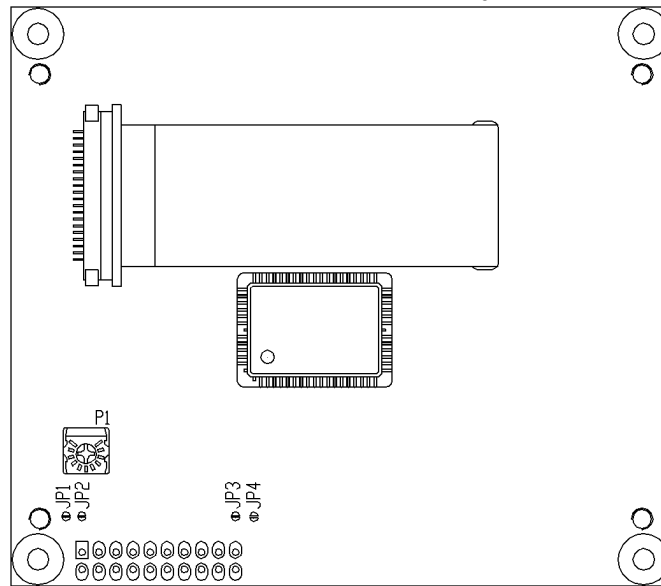


Reset Timing Diagram

4. Function Specifications

4.1 Adjusting the Display Contrast

A Variable-Resistor (P1) is provided on board for contrast adjustment.



LCD module back-side

4.2 Resetting the LCD module

The LCD module should be initialized by hardware reset, using /RES terminal.

4.3 Display Pixel Map

| | | | | | | | | | | | |
|---------------|---------------|---------------|---------------|---------------|-----|-----|-----------------|-----------------|-----------------|-----------------|-----------------|
| 1,1 (D7) | 2,1 (D6) | 3,1 (D5) | 4,1 (D4) | 5,1 (D3) | --- | --- | 188,1 (D4) | 189,1 (D3) | 190,1 (D2) | 191,1 (D1) | 192,1 (D0) |
| 1,2 (D7) | 2,2 (D6) | 3,2 (D5) | 4,2 (D4) | 5,2 (D3) | --- | --- | 188,2 (D4) | 189,2 (D3) | 190,2 (D2) | 191,2 (D1) | 192,2 (D0) |
| 1,3 (D7) | 2,3 (D6) | 3,3 (D5) | 4,3 (D4) | 5,3 (D3) | --- | --- | 188,3 (D4) | 189,3 (D3) | 190,3 (D2) | 191,3 (D1) | 192,3 (D0) |
| : | : | : | : | : | : | : | : | : | : | : | : |
| 1,126 (D7) | 2,126 (D6) | 3,126 (D5) | 4,126 (D4) | 5,126 (D3) | --- | --- | 188,126 (D4) | 189,126 (D3) | 190,126 (D2) | 191,126 (D1) | 192,126 (D0) |
| 1,127 (D7) | 2,127 (D6) | 3,127 (D5) | 4,127 (D4) | 5,127 (D3) | --- | --- | 188,127 (D4) | 189,127 (D3) | 190,127 (D2) | 191,127 (D1) | 192,127 (D0) |
| 1,128 (D7) | 2,128 (D6) | 3,128 (D5) | 4,128 (D4) | 5,128 (D3) | --- | --- | 188,128 (D4) | 189,128 (D3) | 190,128 (D2) | 191,128 (D1) | 192,128 (D0) |

Pixel mapping (Top View)

Note:

- *1. Based on the top view of the LCD module, the 1, 1 (x, y) pixel is the upper-left pixel; the 192, 128 (x, y) pixel is the lower-right pixel.
- *2. For the details of memory mapping please refer to controller datasheet.

4.4 Command Summary

| Command | Parameter | A0 | /RD | /WR | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | HEX | Descriptions |
|-------------|-----------|----|-----|-----|-------------|-----|-----|-----|------|-----|-----|-----|---------------------|---|
| SYSTEM SET | - | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 40 | Init device and display (with 8 parameters) |
| | P1 | 0 | 1 | 0 | 0 | 0 | IV | 1 | W/S | M2 | M1 | M0 | ** | M0=0: internal CG ROM M0=1: external CG ROM M1=0: no D6 correction M1=1: D6 correction M2=0: 8-pixel char height M2=1: 16-pixel char height W/S=0: single panel drive W/S=1: dual panel drive IV=0: Screen top-line correction IV=1: No screen top-line correction |
| | P2 | 0 | 1 | 0 | WF | 0 | 0 | 0 | 0 | | FX | | ** | FX: define the horizontal char size WF=0: 16-line AC drive WF=1: two frame AC drive |
| | P3 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | | | FY | | ** | FY: Vertical Char Size |
| | P4 | 0 | 1 | 0 | | | | | C/R | | | | ** | C/R: display line address range |
| | P5 | 0 | 1 | 0 | | | | | TC/R | | | | ** | TC/R: Line length selection |
| | P6 | 0 | 1 | 0 | | | | | L/F | | | | ** | L/F: Frame Height selection |
| | P7 | 0 | 1 | 0 | | | | | APL | | | | ** | APL: Horizontal address range (low byte) |
| | P8 | 0 | 1 | 0 | | | | | APH | | | | ** | APH: Horizontal address range (high byte) |
| SLEEP IN | - | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 1 | 53 | Enter standby mode |
| DISP ON/OFF | - | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | D | 58 / 59 | Enable and disable display and display flashing (with 1 parameter) |
| | P1 | 0 | 1 | 0 | FP5 | FP4 | FP3 | FP2 | FP1 | FP0 | FC1 | FC0 | ** | Each pair of bit in FP sets the attributes of one screen block |
| SCROLL | - | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 44 | Set display start address and display regions (with 8 or 10 parameters) |
| | P1 | 0 | 1 | 0 | A7 | A6 | A5 | A4 | A3 | A2 | A1 | A0 | ** | SAD 1L |
| | P2 | 0 | 1 | 0 | A15 | A14 | A13 | A12 | A11 | A10 | A9 | A8 | ** | SAD 1H |
| | P3 | 0 | 1 | 0 | L7 | L6 | L5 | L4 | L3 | L2 | L1 | L0 | ** | SL1 |
| | P4 | 0 | 1 | 0 | A7 | A6 | A5 | A4 | A3 | A2 | A1 | A0 | ** | SAD 2L |
| | P5 | 0 | 1 | 0 | A15 | A14 | A13 | A12 | A11 | A10 | A9 | A8 | ** | SAD 2H |
| | P6 | 0 | 1 | 0 | L7 | L6 | L5 | L4 | L3 | L2 | L1 | L0 | ** | SL2 |
| | P7 | 0 | 1 | 0 | A7 | A6 | A5 | A4 | A3 | A2 | A1 | A0 | ** | SAD3L |
| | P8 | 0 | 1 | 0 | A15 | A14 | A13 | A12 | A11 | A10 | A9 | A8 | ** | SAD3H |
| | P9 | 0 | 1 | 0 | A7 | A6 | A5 | A4 | A3 | A2 | A1 | A0 | ** | SAD4L (for both two-screen drive and two layer config are select) |
| | P10 | 0 | 1 | 0 | A15 | A14 | A13 | A12 | A11 | A10 | A9 | A8 | ** | SAD4H (for both two-screen drive and two layer config are select) |
| CSRFORM | - | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 1 | 5D | Set cursor type (with 2 parameters) |
| | P1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | X3 | X2 | X1 | X0 | ** | CRX |
| | P2 | 0 | 1 | 0 | CM | 0 | 0 | 0 | Y3 | Y2 | Y1 | Y0 | ** | CRY CM=0: underscore cursor; CM=1: block cursor |
| CGRAM ADR | - | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 5C | Set Start address of char generator RAM (with 2 parameters) |
| | P1 | 0 | 1 | 0 | A7 | A6 | A5 | A4 | A3 | A2 | A1 | A0 | ** | SAGL |
| | P2 | 0 | 1 | 0 | A15 | A14 | A13 | A12 | A11 | A10 | A9 | A8 | ** | SAGH |
| CSRDIR | - | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | CD1 | CD0 | 4C~4F | Set Direction of Cursor movement |
| HDOT SCR | - | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 0 | 5A | Set horizontal scroll position (with 1 parameters) |
| | P1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | D2 | D1 | D0 | ** | |
| OVLAY | - | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 1 | 5B | Set display overlay format (with 1 parameters) |
| | P1 | 0 | 1 | 0 | 0 | 0 | 0 | OV | DM2 | DM1 | MX1 | MX0 | ** | |
| CSRW | - | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 46 | Set cursor address (with 2 parameters) |
| | P1 | 0 | 1 | 0 | A7 | A6 | A5 | A4 | A3 | A2 | A1 | A0 | ** | CSRL |
| | P2 | 0 | 1 | 0 | A15 | A14 | A13 | A12 | A11 | A10 | A9 | A8 | ** | CSRH |
| CSRR | - | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 1 | 47 | Read Cursor Address (with 2 parameters) |
| | P1 | 1 | 0 | 1 | A7 | A6 | A5 | A4 | A3 | A2 | A1 | A0 | ** | CSRL |
| | P2 | 1 | 0 | 1 | A15 | A14 | A13 | A12 | A11 | A10 | A9 | A8 | ** | CSRH |
| MWRITE | - | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 42 | Write to display memory (with n parameters) |
| | P1 | 0 | 1 | 0 | Memory Data | | | | | | | ** | Display memory data | |
| | : | : | : | : | | | | | | | ** | | | |
| | Pn | 0 | 1 | 0 | Memory Data | | | | | | | ** | | |
| MREAD | - | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 43 | Read from display memory (with n parameters) |
| | P1 | 1 | 0 | 1 | Memory Data | | | | | | | ** | Display memory data | |
| | : | : | : | : | | | | | | | ** | | | |
| | Pn | 1 | 0 | 1 | Memory Data | | | | | | | ** | | |

Note:
For details please refer to S1D13305 datasheet.

4.5 Initialization Setting Example

The following setting should be issue to LCD module after hardware reset.
(It is an example only; it could be adjusted if necessary.)

| Command | Parameter | A0 | /RD | /WR | Value (binary) | HEX | Descriptions |
|-------------|-----------|----|-----|-----------|----------------|-------|---|
| SYSTEM SET | - | 1 | 1 | 0 | 0100 0000 | 40 | Init device and display, (with 8 parameters) |
| | P1 | 0 | 1 | 0 | 0011 0000 | 30 | M0=0: internal CG ROM M0=1: external CG ROM M1=0: no D6 correction M1=1: D6 correction M2=0: 8-pixel char height M2=1: 16-pixel char height W/S=0: single panel drive W/S=1: dual panel drive IV=0: Screen top-line correction IV=1: No screen top-line correction |
| | P2 | 0 | 1 | 0 | 1000 0111 | 87 | FX: define the horizontal char size WF=0: 16-line AC drive WF=1: two frame AC drive |
| | P3 | 0 | 1 | 0 | 0000 0111 | 07 | FY: Vertical Char Size |
| | P4 | 0 | 1 | 0 | 0001 0111 | 17 | C/R: display line address range |
| | P5 | 0 | 1 | 0 | 0001 1100 | 1C | TC/R: Line length selection |
| | P6 | 0 | 1 | 0 | 0111 1111 | 7F | L/F: Frame Height selection |
| | P7 | 0 | 1 | 0 | 0001 1000 | 18 | APL: Horizontal address range (low byte) |
| | P8 | 0 | 1 | 0 | 0000 0000 | 00 | APH: Horizontal address range (high byte) |
| DISP ON/OFF | - | 1 | 1 | 0 | 0101 1001 | 59 | Enable |
| | P1 | 1 | 1 | 0 | 1000 0000 | 80 | |
| SCROLL | - | 1 | 1 | 0 | 0100 0100 | 44 | Set cursor type (with 10 parameters) |
| | P1 | 0 | 1 | 0 | 0000 0000 | 00 | SAD 1L |
| | P2 | 0 | 1 | 0 | 0000 0000 | 00 | SAD 1H |
| | P3 | 0 | 1 | 0 | 0111 1111 | 7F | SL1 |
| | P4 | 0 | 1 | 0 | 0000 0000 | 00 | SAD 2L |
| | P5 | 0 | 1 | 0 | 0000 1100 | 0C | SAD 2H |
| | P6 | 0 | 1 | 0 | 0111 1111 | 7F | SL2 |
| | P7 | 0 | 1 | 0 | 0000 0000 | 00 | SAD3L |
| P8 | 0 | 1 | 0 | 0001 1000 | 18 | SAD3H | |
| CSRFORM | - | 1 | 1 | 0 | 0101 1101 | 5D | Set cursor type (with 2 parameters) |
| | P1 | 0 | 1 | 0 | 0000 0111 | 07 | CRX |
| | P2 | 0 | 1 | 0 | 0000 0111 | 07 | CRY |
| CSRDIR | - | 1 | 1 | 0 | 0100 1100 | 4C | Set Direction of Cursor movement |
| HDOT SCR | - | 1 | 1 | 0 | 0101 1010 | 5A | Set horizontal scroll position (with 1 parameters) |
| | P1 | 0 | 1 | 0 | 0000 0000 | 00 | |
| OVLAY | - | 1 | 1 | 0 | 0101 1011 | 5B | Set display overlay format (with 1 parameters) |
| | P1 | 0 | 1 | 0 | 0001 1100 | 1C | |
| CSRW | - | 1 | 1 | 0 | 0100 0110 | 46 | Set cursor address (with 2 parameters) |
| | P1 | 0 | 1 | 0 | 0000 0000 | 00 | CSRL |
| | P2 | 0 | 1 | 0 | 0000 0000 | 00 | CSRH |
| MWRITE | - | 1 | 1 | 0 | 0100 0010 | 42 | Write to display memory (with n parameters) |
| | P1 | 0 | 1 | 0 | Memory Data | ** | Display memory data |
| | : | : | : | : | : | ** | |
| | Pn | 0 | 1 | 0 | Memory Data | ** | |

Note:
For details please refer to S1D13305 datasheet.

5. Design and Handling Precaution

1. The LCD panel is made by glass. Any mechanical shock (eg. dropping from high place) will damage the LCD module.
2. Do not add excessive force on the surface of the display, which may cause the Display color change abnormally.
3. The polarizer on the LCD is easily get scratched. If possible, do not remove the LCD protective film until the last step of installation.
4. Never attempt to disassemble or rework the LCD module.
5. Only Clean the LCD with Isopropyl Alcohol or Ethyl Alcohol. Other solvents (eg. water) may damage the LCD.
6. When mounting the LCD module, make sure that it is free from twisting, warping and distortion.
7. Ensure to provide enough space (with cushion) between case and LCD panel to prevent external force adding on it, or it may cause damage to the LCD or degrade the display result.
8. Only hold the LCD module by its side. Never hold LCD module by add force on the heat seal or TAB.
9. Never add force to component of the LCD module. It may cause invisible damage or degrade of the reliability.
10. LCD module could be easily damaged by static electricity. Be careful to maintain an optimum anti-static work environment to protect the LCD module.
11. When peeling off the protective film from LCD, static charge may cause abnormal display pattern. It is normal and will resume to normal in a short while.
12. Take care and prevent get hurt by the LCD panel sharp edge.
13. Never operate the LCD module exceed the absolute maximum ratings.
14. Keep the signal line as short as possible to prevent noisy signal applying to LCD module.
15. Never apply signal to the LCD module without power supply.
16. IC chip (eg. TAB or COG) is sensitive to the light. Strong lighting environment could possibly cause malfunction. Light sealing structure casing is recommend.
17. LCD module reliability may be reduced by temperature shock.
18. When storing the LCD module, avoid exposure to the direct sunlight, high humidity, high temperature or low temperature. They may damage or degrade the LCD module