SPECIFICATION
FOR
APPROVAL

(●) **Preliminary Specification**
(   ) **Final Specification**

Title	20.1" UXGA TFT LCD

BUYER	
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LM201U02
Suffix	A3M1

*When you obtain standard approval,
 please use the above model name without suffix

SIGNATURE	DATE
/	
/	
/	

Please return 1 copy for your confirmation with
your signature and comments.

SIGNATURE	DATE
S.W.Lee / G.Manager	
REVIEWED BY	
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PREPARED BY	
K.G Park / Manager	

MNT/TV Products Engineering Dept.
LG. Philips LCD Co., Ltd

The preliminary document is subject to change without prior notice.

Contents

Product Specification

RECORD OF REVISIONS

Revision No	Revision Date	Page	DESCRIPTION
0.0	Nov.13.2001	-	First Draft (Preliminary)
0.1	Nov.19.2001	8	Interface Chip changed
			PTFP401A(Ti) → Sil253(Silicon Image)
	Jan.11.2002	6	V_{BL} (MIN): 740(8mA) -> 710(9mA)
	Jan.29.2002	4	Outline Dimension: 24.5(D) mm -> 24.3(D) mm
		4	Weight: 4,100 g (typ.) ->3,700 g (typ.)
		19	Outline Dimension: 24.1(D) mm(Max) -> 24.3(D) mm(Typ)
		19	Weight: 4,100 g (Typ) ->3,700 g (Typ)
			4,300 g (Max) ->4,000 g (Max)
		20	Front View Update
		21	Rear View Update

Product Specification

1. General Description

 The LM201U1 LCD is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp(CCFL) back light system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. This TFT-LCD has a 20.1 inch diagonally measured active display area with UXGA resolution(1200 vertical by 1600 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus, presenting a palette of more than 16,777,216 colors.

The LM201U1 has been designed to apply the TMDS^{TM}(Transition Minimized differential Signaling) as the interface method to enables a simple and low-cost implementation in both the host and monitor.
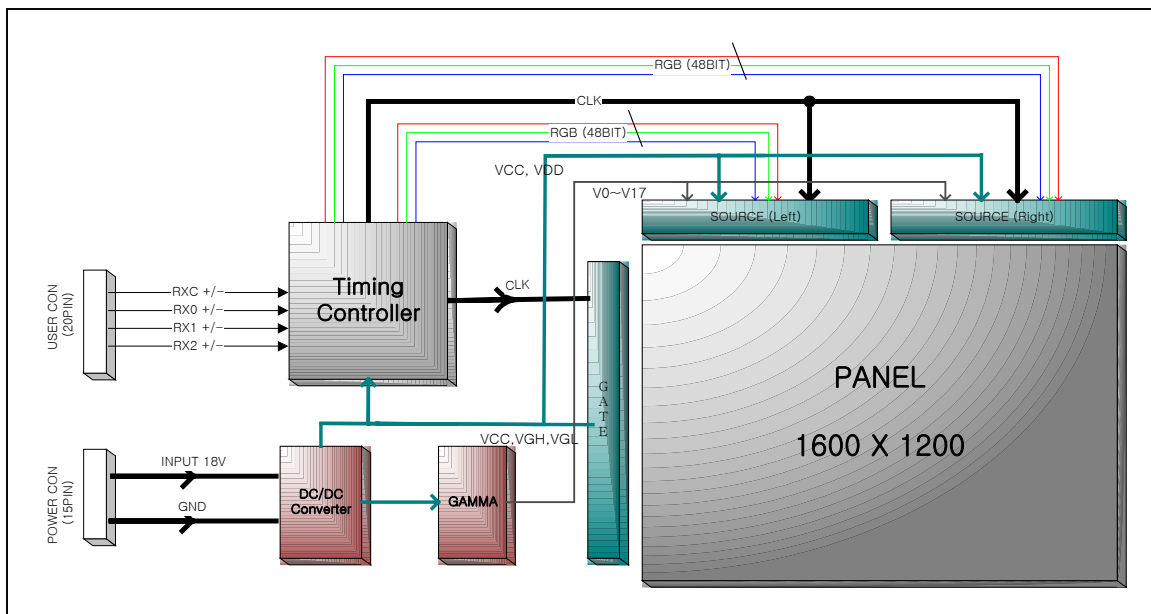
General Features

| Active Screen Size	20.1 inches(51cm) diagonal
Outline Dimension	467.8(H) x 361.0(V) x 24.3(D) mm(Typ.)
Pixel Pitch	0.255 mm x 0.255mm
Pixel Format	1600 horiz. By 1200 vert. Pixels RGB stripes arrangement
Color Depth	8-bit, 16,777,216 colors
Luminance, White	250 cd/m^2(Typ.)
Power Consumption	Total 41.0 Watt(Typ.)
Weight	3,700 g (typ.)
Display Operating Mode	Transmissive mode, normally black
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer,
Interface	TMDS
LAMP	6 CCFL's(Cold Cathode Fluorescent Lamp)

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2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Values		Units	Notes
		Min	Max		
Power Input Voltage	Vcc	-0.3	21	Vdc	at 25 ± 5°C
Operating Temperature	TOP	0	50	°C	1
Storage Temperature	HST	-20	60	°C	1
Operating Ambient Humidity	HOP	10	90	%RH	1
Storage Humidity	HST	10	90	%RH	1

Note : 1. Temperature and relative humidity range are shown in the figure below.
Wet bulb temperature should be 39 °C Max, and no condensation of water.
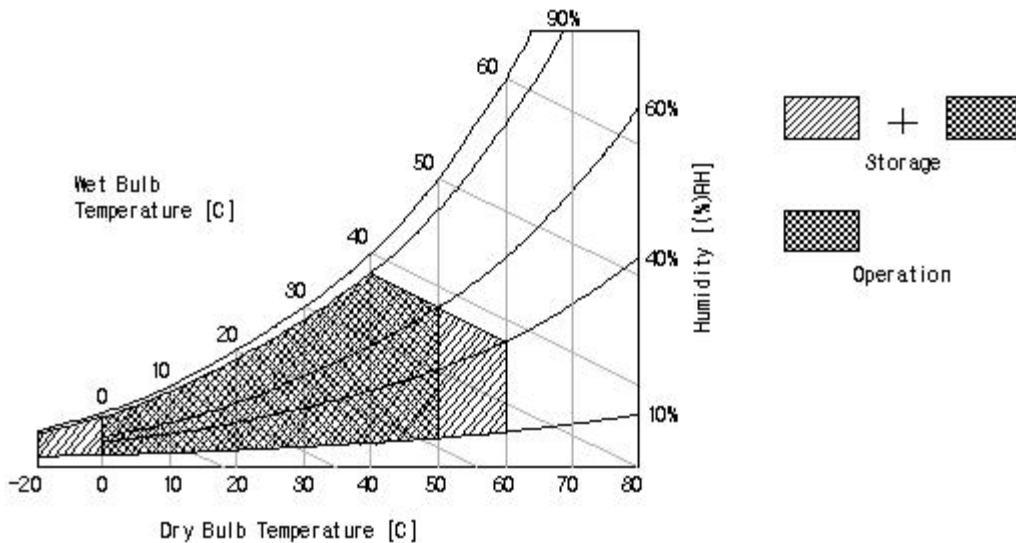
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3. Electrical Specifications

3-1. Electrical Characteristics

The LM201U1 requires two power inputs. One input is employed to power the LCD electronics and to drive the voltages to drive the TFT array and liquid crystal. And the second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Values			Unit	Notes
		Min	Typ	Max		
MODULE :						
Power Supply Input Voltage	Vcc	17.0	18.0	19.0	Vdc	
Power Supply Input Current	Icc	-	0.4	0.7	A	1
Power Consumption	Pc	-	6.3	12.6	Watt	1
Differential Impedance	Zm	90	100	110	Ohm	2
Inrush Current	I_{Rush}	-	-	3	A	3
LAMP :						
Operating Voltage	V_{BL}	710(9mA)	750(7.5mA)	920(3mA)	V_{RMS}	3
Operating Current	I_{BL}	3.0	7.5	9.0	mA	
Established Starting Voltage	Vs					4
at 25 °C		-	-	1080	V_{RMS}	
at 0 °C		-	-	1500	V_{RMS}	
Operating Frequency	F_{BL}	40	50	60	kHz	5
Power Consumption	P_{BL}	-	33.8	37.2	Watt	6
Discharge Stabilization Time	Ts	-	-	3	min	7
Life Time		30,000			Hrs	8

Notes : 1. The input current shall be measured at V_{CC} of 18.0Vdc at 25℃, refresh rate of 60Hz, and pixel clock frequency of 125MHz under mosaic pattern(B/W) (typ).
2. This impedance value is needed to proper display and measured from TMDS Tx output and input connector of module.
3. The measuring condition
    The duration of rush current is about 20ms, and rising time of Power input is min 1ms.
4. The variance of the voltage is ±10%.
5. The voltage above Vs should be applied to the lamps for more than 1second for start-up Otherwise,the lamps may not turn on.

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6. The output of the inverter must have symmetrical(negative and positive) voltage waveform and symmetrical current waveform.(Unsymmetrical ratio is less than 10%) Please do not use the inverter which has unsymmetrical voltage and unsymmetrical current and spike wave.
   Lamp frequency may produce interference with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away as possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
7. Let's define the brightness of the lamp after being lighted for 5 minutes as 100%.
   Ts is the time required for the brightness of the center of the lamp to be not less than 95%.
8. The lamp power consumption shown above does not include loss of external inverter.
   The used lamp current is typical current
9. The life time is defined as the time at which brightness of lamp is 50% compare to that of initial value at the typical lamp current on condition of continuous operating at 25±2°C.

※ Do not attach a conducting tape to lamp connecting wire.
    If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.

3-2. Interface Connections

This LCD employs three kinds of interface connections. A 21 pin connector is used for TMDS signals from the host computer. A 15-pin connector is used for LCD module power and LCM controls signal from external monitor control circuits. And four connectors, a three pin and two pin connector, are used for the integral backlight system.

3-2-1. Signal Interface

The TMDS signal interface connector is FI-WE21P-HF-E by JAE.

Interface chip in host side, must be used TMDS^{TM}, part No. SiI160, designed by Silicon Image Inc., or its equivalent.

The pin configuration for the 21 pin connector is shown in the table below.

Table 3 21PIN CONNECTOR (CNC1) PIN CONFIGURATION

Pin	Symbol	Description	Pin	Symbol	Description
1	TX1+	TMDS positive differential output (channel1)	11	TX2+	TMDS positive differential output (channel2)
2	TX1-	TMDS negative differential output (channel1)	12	TX2-	TMDS negative differential output (channel2)
3	SHLD1	Shield for TMDS channel 1	13	SHLD2	Shield for TMDS channel 2
4	SHLDC	Shield for TMDS clock	14	SHLD0	Shield for TMDS channel 0
5	TXC+	TMDS positive differential output (reference clock)	15	TX0+	TMDS positive differential output (channel 0)
6	TXC-	TMDS negative differential output (reference clock)	16	TX0-	TMDS negative differential output (channel 0)
7	GND	Logic Ground	17	NC	No Connection
8	+5V	Logic +5V Supply (See note 2)	18	HPD	Hot Plug Detection (See note 3)
9	NC	No Connection	19	DDC_DAT	DDC2B Data (See note 4)
10	NC	No Connection	20	DDC_CLK	DDC2B Clock (See note 5)
			21	NC	No connection

1. Interface chips
   1.1 LCD   : SiI253(Silicon Image)

2. Connector
   2.1 LCD : FI-WE21P-HF-E
   2.2 Mating : FI-W21S or FI-W21M or compatible
   2.3 Connector pin arrangement

21　　　　　　　　**1**

CNC1

Notes: 1. All shield pins and GND(ground) pin should be connected together and should also be connected to the LCD's metal frame.
2. This +5V is only for external monitor control circuits and directly connected to 12 pin of CNC2 connector.
3. This pin is internally connected to pin 8 (+5V) in LCM circuits.
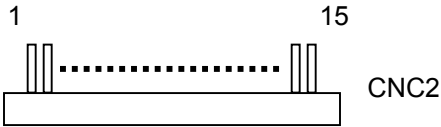4, 5. These pins are only for external monitor control circuits and directly connected to CNC2 connector.

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3-2-2. Power Interface

A 15 pin connector (CNC2) for external monitor control circuits, is a model 53261 manufactured by Molex. The mating connector part number is 51021 or its equivalent. The pin configuration for this connector is shown in the table below.

Table 4 15 PIN CONNECTOR (CNC2) PIN CONFIGURATION

Pin	Symbol	Description	Notes
1	GND	Ground	1
2	NC	No connection (Reserved)	
3	GND	Ground	
4	V_{AA}	LCM power supply, +18V $\pm$5%	
5	V_{AA}	LCM power supply, +18V $\pm$5%	
6	GND	Ground	
7	NC	No connection (Reserved)	
8	NC	No connection (Reserved)	
9	GND	Ground	
10	NC	No connection (Reserved)	
11	NC	No connection (Reserved)	
12	+5V_DDC	+5V out for DDC	2
13	SDA	DDC data line out	
14	SCL	DDC clock line out	
15	GND	Ground	

Connector pin arrangement	
	1 15 CNC2

Notes: 1. All GND(ground) pins should be connected together and should also be connected to the LCD's metal frame.
2. Pin 12, 13, 14 are for DDC2B communication between host computer and external monitor control circuits. These pins are directly connected to CNC1 connector.
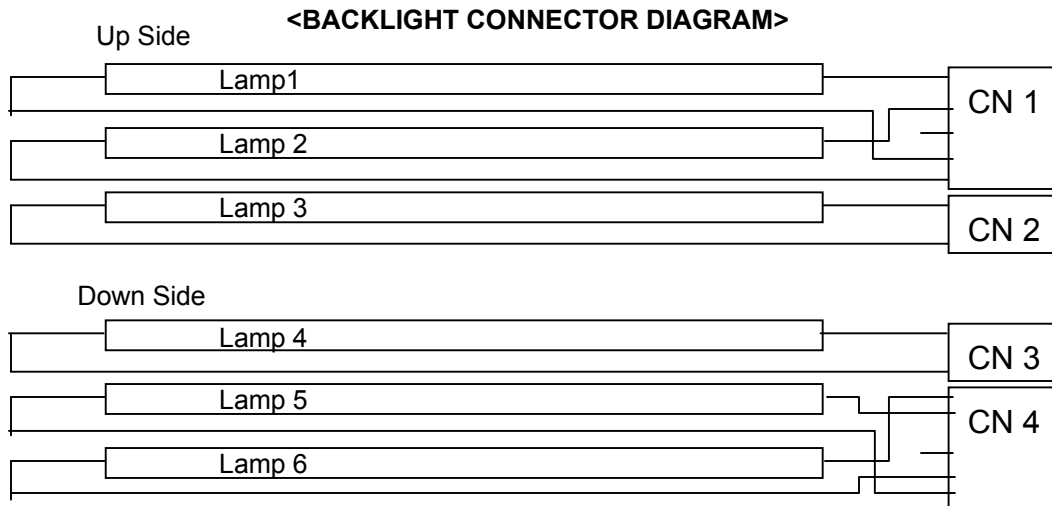
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3-2-3. Backlight Interface

The backlight interface connector is a model BHSR-02VS-1(CN2/CN3) and BHSR-05VS-2 (CN1/CN4) manufactured by JST. The mating connector part number are SM02B-BHSS-1-TB(2pin), SM04(9-E2)B-BHS-1-TB or equivalent. The pin configuration for the connector is shown in the table below.

Table 5 BACKLIGHT CONNECTOR PIN CONFIGURATION

No	Pin	Symbol	Description	Notes
CN1	1	HV	Power supply for lamp 1(High voltage side) - Pink	1
	2	HV	Power supply for lamp 2(High voltage side) - Blue	1
	3	NC	NC	
	4	LV	Power supply for lamp 1(Low voltage side) - White	2
	5	LV	Power supply for lamp 2(Low voltage side) - White	2
CN2	1	HV	Power supply for lamp 3(High voltage side) - Gray	1
	2	LV	Power supply for lamp 3(Low voltage side) - White	2
CN3	1	HV	Power supply for lamp 4(High voltage side) - Gray	1
	2	LV	Power supply for lamp 4(Low voltage side) - White	2
CN4	1	HV	Power supply for lamp 6(High voltage side) - Pink	1
	2	HV	Power supply for lamp 5(High voltage side) - Blue	1
	3	NC	NC	
	4	LV	Power supply for lamp 6(Low voltage side) - White	2
	5	LV	Power supply for lamp 5(Low voltage side) - White	2

Notes: 1. The high voltage power terminal is colored pink,blue,gray. Ground pin color is white.
2. The backlight ground should be common with LCD metal frame.

<BACKLIGHT CONNECTOR DIAGRAM>

Up Side

Lamp1	CN 1
Lamp 2	
Lamp 3	CN 2

Down Side

Lamp 4	CN 3
Lamp 5	CN 4
Lamp 6	

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3-3. Signal Timing Specifications

This is signal timing required at the input of the TMDS transmitter. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.
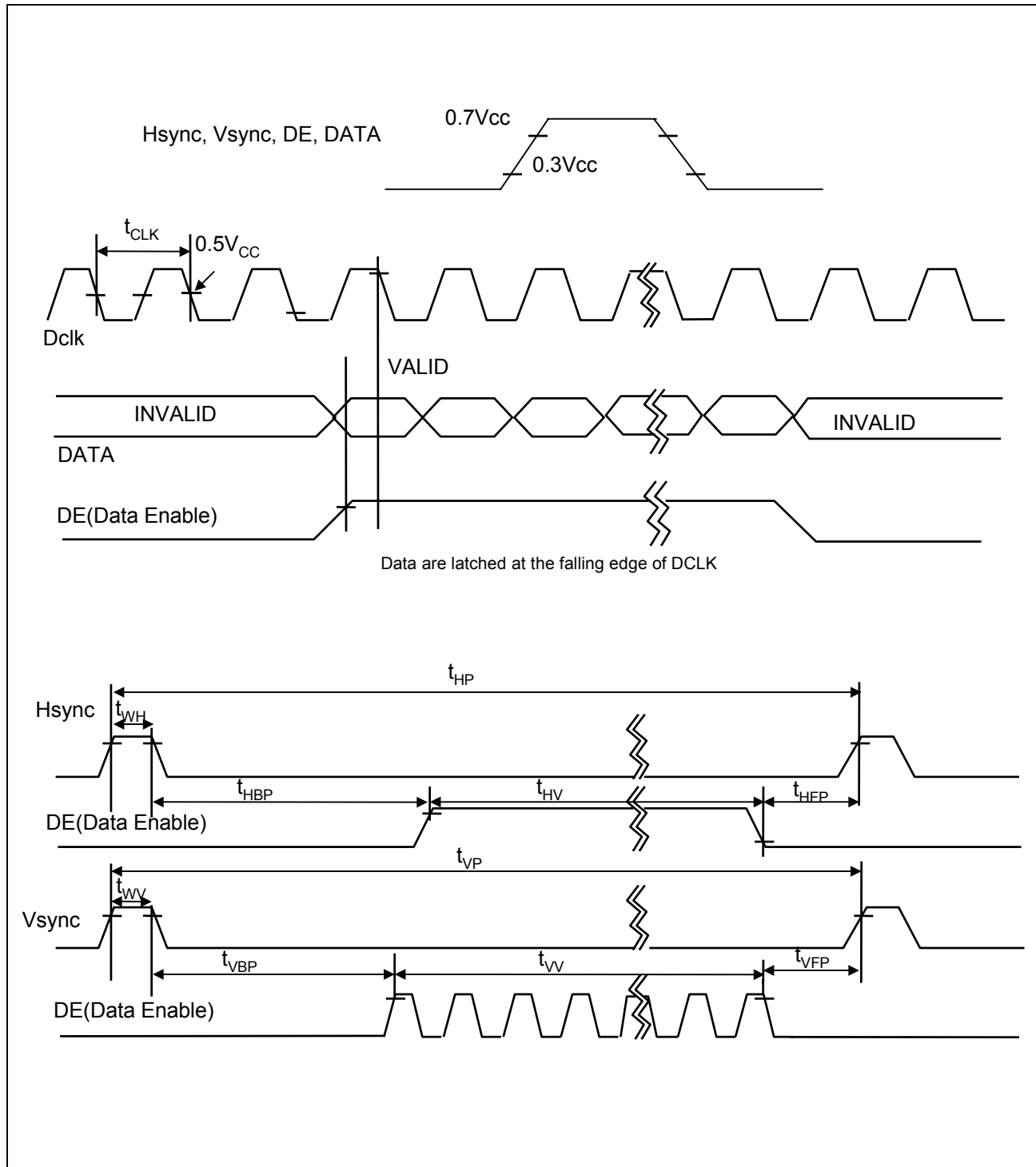
Table 6. Timing Table

ITEM		SYMBOL	Min	Typ	Max	Unit	Note
DCLK	Period	tCLK	8.33	8.00	7.87	ns	
	Frequency	fCLK	123	125	127	MHz	
Hsync	Period	tHP	1664	1680	1728	tCLK	1
	Width-Active	tWH	32	32	32		2
Vsync	Period	tVP	1230	1240	1240	tHP	
	Frequency	fv	60	60	60	Hz	3
	Width-Active	tWV	3	3	3	tHP	4
Data Enable	Horizontal Valid	tHV	1600	1600	1600	tCLK	
	Horizontal Back Porch	tHBP	16	32	32		
	Horizontal Front Porch	tHFP	16	16	64		
	Horizontal Blank	-	80	80	128		tWH+ tHBP+ tHFP
	Vertical Valid	tVV	1200	1200	1200	tHP	
	Vertical Back Porch	tVBP	25	35	35		
	Vertical Front Porch	tVFP	2	2	2		
	Vertical Blank	-	30	40	40		tWV+ tVBP+ tVFP

Notes: 1. Hsync period shall be a double number of 8
          2. Horizontal sync shall be active high.
          3. Vertical frequency is only 60Hz
          4. Vertical sync shall be active high.

3-4. Signal Timing Waveforms

Hsync, Vsync, DE, DATA

0.7Vcc
0.3Vcc

t_{CLK} $0.5V_{CC}$

Dclk

VALID

INVALID   DATA   INVALID

DATA

DE(Data Enable)

Data are latched at the falling edge of DCLK

t_{HP}

Hsync t_{WH}

t_{HBP} t_{HV} t_{HFP}

DE(Data Enable)

t_{VP}

Vsync t_{WV}

t_{VBP} t_{VV} t_{VFP}

DE(Data Enable)

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3-5. Color Input Data Reference

The brightness of each primary color(red,green and blue) is based on the 8-bit gray scale data input for the color ; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

Color		Input Color Data																							
		RED (MSB ... LSB)								GREEN (MSB ... LSB)								BLUE (MSB ... LSB)								
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	...				...								...								...				
	RED (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	...				...								...								...				
	GREEN (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
BLUE	BLUE (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	...				...								...								...				
	BLUE (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

3-6. Power Sequence

Parameter	Values			Units
	Min.	Typ.	Max.	
T_1	-	-	10	ms
T_2	0	-	50	ms
T_3	200	-	-	ms
T_4	200	-	-	ms
T_5	0	-	50	ms
T_6	-	-	10	ms
T_7	400	-	-	ms

Notes : 1. Please avoid floating state of interface signal at invalid period.
2. When the interface signal is invalid, be sure to pull down the power supply for LCD V_{AA} to 0V.
3. Lamp power must be turn on after power supply for LCD and interface signal are valid.

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4. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25 °C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 ° and aperture 1 degree.
FIG. 1 presents additional information concerning the measurement equipment and method.

FIG. 1 Optical Characteristic Measurement Equipment and Method

Optical Stage(x,y)
LCD Module
Field = 1 °
Prichard 880 or equivalent
500mm
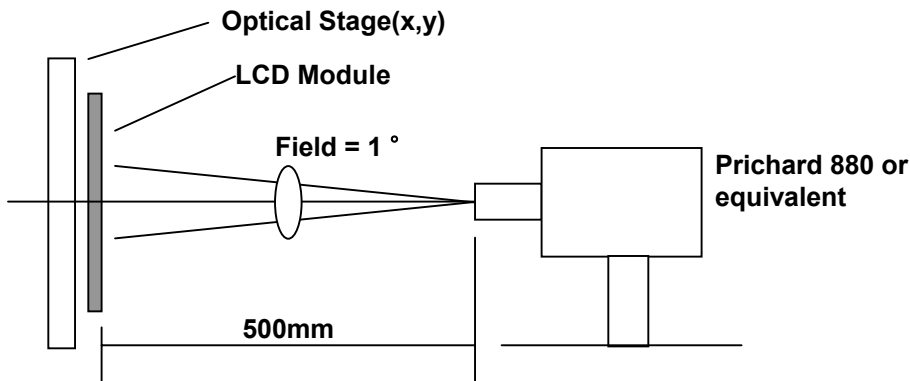
Table 8. OPTICAL CHARACTERISTICS (Ta=25 °C, V_{CC}=18.0V, f_V=60Hz Dclk=125MHz, I_{BL}=7.5mA)

Parameter		Symbol	Values			Units	Notes
			Min	Typ	MAx		
Contrast Ratio		CR	200	300			1
Surface Luminance, white		L_{WH}	200	250		cd/m²	2
Luminance Variation		δ_{WHITE}			1.42		3
Response Time		Tr				ms	4
	Rise Time	Tr_R		15	25		
	Decay Time	Tr_D		15	25		
Color Coordinates							
	RED	RX	0.600	0.630	0.660		
		RY	0.310	0.340	0.370		
	GREEN	GX	0.270	0.300	0.330		
		GY	0.550	0.580	0.610		
	BLUE	BX	0.120	0.150	0.180		
		BY	0.080	0.110	0.140		
	WHITE	WX	0.283	0.313	0.343		
		WY	0.299	0.329	0.359		
Viewing Angle							
	x axis, right(ϕ=0°)	θr	80			degree	5
	x axis, left (ϕ=180°)	θl	80				
	y axis, up (ϕ=90°)	θu	80				
	y axis, down (ϕ=270°)	θd	80				
Gray Scale							6

Notes : 1. Contrast Ratio(CR) is defined mathematically as :

Contrast Ratio = $\dfrac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$

Contrast ratio shall be measured at the center of the display (Location P1).

2. Surface Luminance (L_{WH})is measured at the center point (location P1) with all pixels displaying white

3. The variation in surface luminance, δ WHITE is defined as :

δ WHITE $=$ $\dfrac{\text{Maximum } (P_1, P_2, ...P_9)}{\text{Minimum } (P_1, P_2, ...P_9)}$

Where P1 to P9 are the luminance with all pixels displaying white at 9 locations.

| 400 | 800 | 1200 | (pixel) |

P2   P6   P3   — 300

P7   P1   P8   — 600

P4   P9   P5   — 900
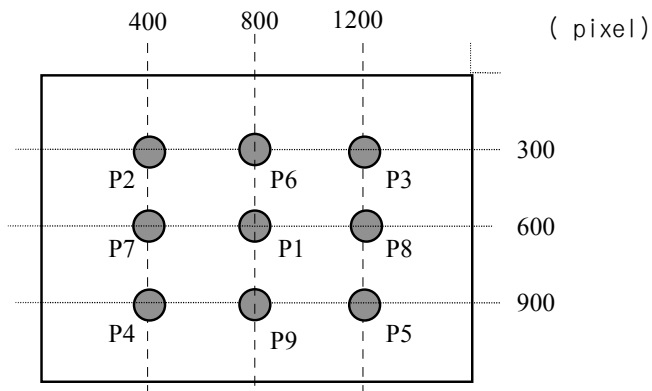
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4. The response time is defined as the following figure and shall be measured by switching
the input signal for "black" and "white".
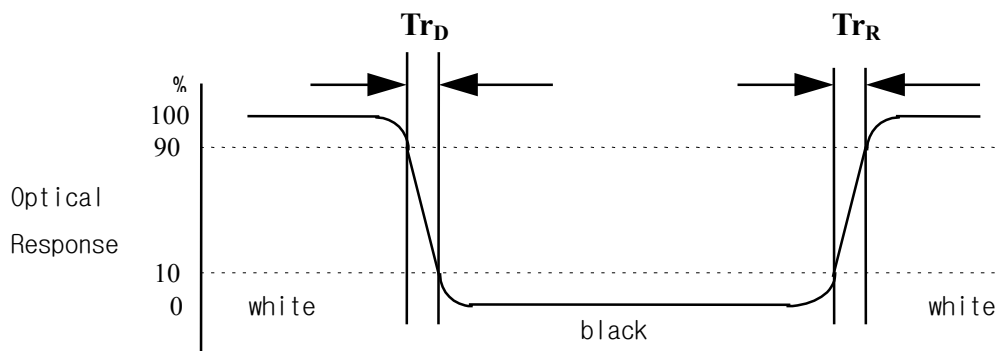
5. Viewing angle is the angle at which the contrast ratio is greater than 10.
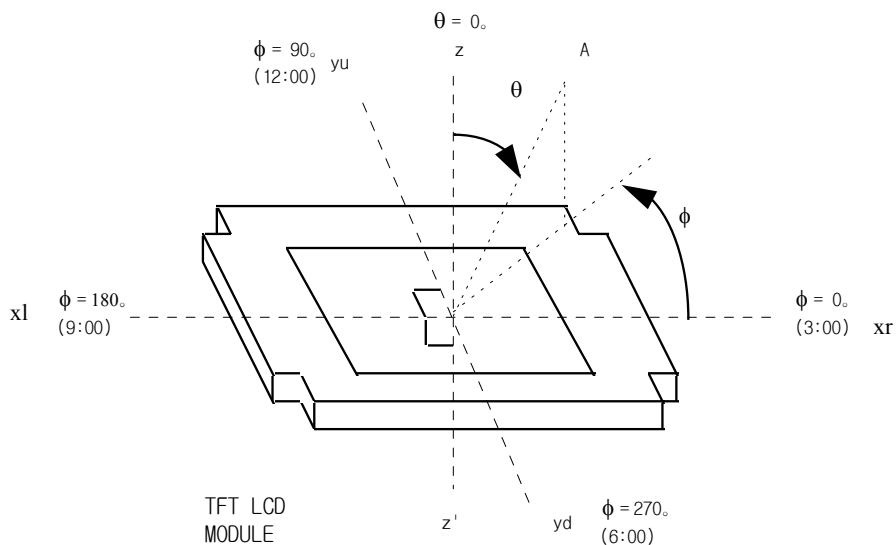
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Gray Level	Relative Luminance (%)
	Typ.
0	0.3
31	1.2
63	4.68
95	11.7
127	21.2
159	35.2
191	53.0
223	75.4
255	100

5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LM201U1. In addition the figures in the next page are detailed mechanical drawing of the LCD.

Outside dimensions	Horizontal	467.8 ± 0.7mm
	Vertical	361 ± 0.7mm
	Depth	24.3(Typ)mm
Bezel area	Horizontal	413 mm
	Vertical	311mm
Active display area	Horizontal	408.0mm
	Vertical	306.0mm
Weight(approximate)	3700g(Typ.), 4000g(Max.)	
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer	

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<FRONT VIEW>
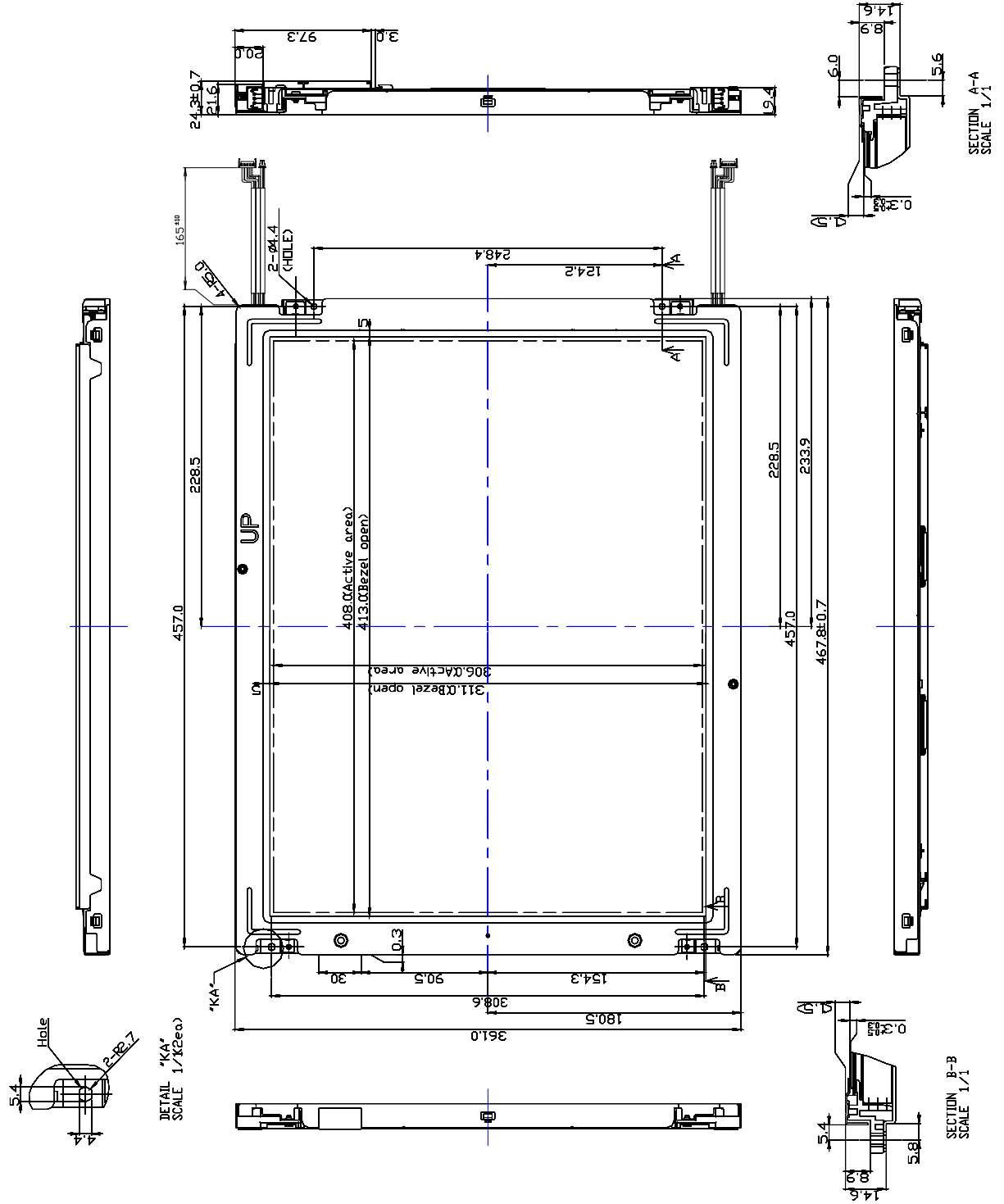
Product Specification

<REAR VIEW>
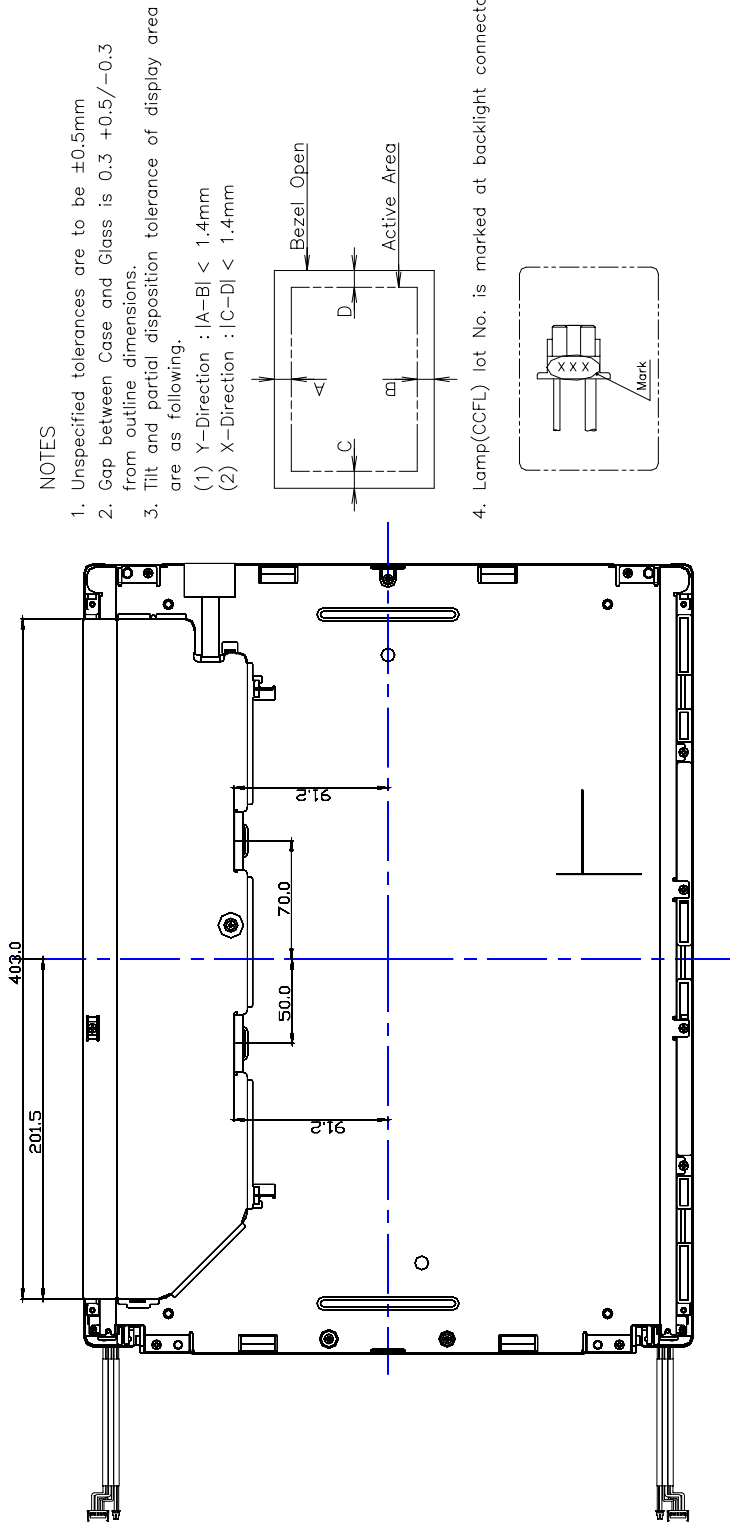
NOTES

1. Unspecified tolerances are to be ±0.5mm
2. Gap between Case and Glass is 0.3 +0.5/-0.3 from outline dimensions.
3. Tilt and partial disposition tolerance of display area are as following.
   (1) Y-Direction : |A-B| < 1.4mm
   (2) X-Direction : |C-D| < 1.4mm
4. Lamp(CCFL) lot No. is marked at backlight connector.
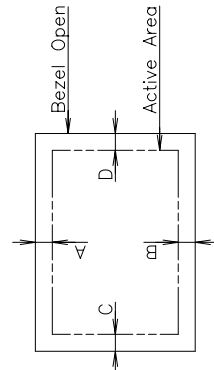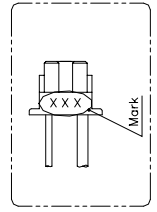
6. Reliability

Environment test condition

No.	Test Item	Conditions
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 60%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Waveform : Random Vibration level : 1.0G RMS Bandwidth : 10 ~ 500Hz Duration : X,Y,Z 10min One time each direction
6	Shock test (non-operating)	Shock level : 100G Waveform: half sine wave, 2ms Direction : ±X, ±Y, ±Z One time each direction
7	Altitude storage / shipment operating	0 - 40,000 feet(12,192m) 0 - 12,000 feet (3657.6m)

{ Result Evaluation Criteria }
 There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

7. International Standards

7-1. Safety

a) UL 1950 Third Edition, Underwriters Laboratories, Inc. Jan. 28, 1995.
   Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.
b) CAN/CSA C22.2 No. 950-95 Third Edition, Canadian Standards Association, Jan. 28, 1995.
   Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.
c) EN 60950 : 1992+A1: 1993+A2: 1993+A3: 1995+A4: 1997+A11: 1997
   IEC 950 : 1991+A1: 1992+A2: 1993+A3: 1995+A4: 1996
   European Committee for Electrotechnical Standardization(CENELEC)
   EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical
   Business Equipment.

7-2. EMC

a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical
   and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards
   Institute(ANSI), 1992
b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of
   Information Technology Equipment." International Special Committee on Radio Interference
c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of
   Information Technology Equipment." European Committee for Electrotechnical Standardization
   (CENELEC), 1998

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A	B	C	D	E	F	G	H	I	J	K	L	M

A,B,C : SIZE
D : YEAR
E : MONTH
F,G : PANEL CODE
H : ASSEMBLY CODE
I,J,K,L,M : SERIAL NO.

Note:
1. YEAR

YEAR	97	98	99	2000	2001	2002	2003	2004	2005	2006	2007
Mark	7	8	9	0	1	2	3	4	5	6	7

2. MONTH

MONTH	Jan.	Feb.	Mar.	Apr.	May.	Jun.	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	A	B	C

3. Serial No.

Serial No.	1 ~ 99999	100000 ~
Mark	00001 ~ 99999	A0001 ~ A9999,········, Z9999

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box : 3 pcs

b) Box Size : 470mm × 253mm × 573mm

9. PRECAUTIONS

Please pay attention to the following when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

(1) You must mount a module using holes arranged in four corners or four sides.
(2) You should consider the mounting structure so that uneven force(ex. Twisted stress) is not applied to the module.
And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
(3) Please attach a transparent protective plate to the surface in order to protect the polarizer.
Transparent protective plate should have sufficient strength in order to the resist external force.
(4) You should adopt radiation structure to satisfy the temperature specification.
(5) Acetic acid type and chlorine type materials for the cover case are not describe because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
(6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are determined to the polarizer.)
(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
(8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
(9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

(1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
V=±200mV(Over and under shoot voltage)
(2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
(3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
(4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
(5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
(6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.
(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
(2) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

(1) The protection film is attached to the bezel with a small masking tape
When the protection film is peeled off, static electricity is generated between the film and polarizer.
This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
(2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
(3) You can remove the glue easily. When the glue remains on the bezel or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

Product Specification

APPENDIX 1 : Required Signal Assignment for TMDS

Graphics Controller		PanelLink		Flat Panel Controller	
24-bits	18-bits	SiI160	SiI161	18-bits	24-bits
B0 - 0		DIE0	QE0		B0 - 0
B1 - 0		DIE1	QE1		B1 - 0
B2 - 0	B0 - 0	DIE2	QE2	B0 - 0	B2 - 0
B3 - 0	B1 - 0	DIE3	QE3	B1 - 0	B3 - 0
B4 - 0	B2 - 0	DIE4	QE4	B2 - 0	B4 - 0
B5 - 0	B3 - 0	DIE5	QE5	B3 - 0	B5 - 0
B6 - 0	B4 - 0	DIE6	QE6	B4 - 0	B6 - 0
B7 - 0	B5 - 0	DIE7	QE7	B5 - 0	B7 - 0
G0 - 0		DIE8	QE8		G0 - 0
G1 - 0		DIE9	QE9		G1 - 0
G2 - 0	G0 - 0	DIE10	QE10	G0 - 0	G2 - 0
G3 - 0	G1 - 0	DIE11	QE11	G1 - 0	G3 - 0
G4 - 0	G2 - 0	DIE12	QE12	G2 - 0	G4 - 0
G5 - 0	G3 - 0	DIE13	QE13	G3 - 0	G5 - 0
G6 - 0	G4 - 0	DIE14	QE14	G4 - 0	G6 - 0
G7 - 0	G5 - 0	DIE15	QE15	G5 - 0	G7 - 0
R0 - 0		DIE16	QE16		R0 - 0
R1 - 0		DIE17	QE17		R1 - 0
R2 - 0	R0 - 0	DIE18	QE18	R0 - 0	R2 - 0
R3 - 0	R1 - 0	DIE19	QE19	R1 - 0	R3 - 0
R4 - 0	R2 - 0	DIE20	QE20	R2 - 0	R4 - 0
R5 - 0	R3 - 0	DIE21	QE21	R3 - 0	R5 - 0
R6 - 0	R4 - 0	DIE22	QE22	R4 - 0	R6 - 0
R7 - 0	R5 - 0	DIE23	QE23	R5 - 0	R7 - 0
B0 - 1		DIO0	QO0		B0 - 1
B1 - 1		DIO1	QO1		B1 - 1
B2 - 1	B0 - 1	DIO2	QO2	B0 - 1	B2 - 1
B3 - 1	B1 - 1	DIO3	QO3	B1 - 1	B3 - 1
B4 - 1	B2 - 1	DIO4	QO4	B2 - 1	B4 - 1
B5 - 1	B3 - 1	DIO5	QO5	B3 - 1	B5 - 1
B6 - 1	B4 - 1	DIO6	QO6	B4 - 1	B6 - 1
B7 - 1	B5 - 1	DIO7	QO7	B5 - 1	B7 - 1
G0 - 1		DIO8	QO8		G0 - 1
G1 - 1		DIO9	QO9		G1 - 1
G2 - 1	G0 - 1	DIO10	QO10	G0 - 1	G2 - 1
G3 - 1	G1 - 1	DIO11	QO11	G1 - 1	G3 - 1
G4 - 1	G2 - 1	DIO12	QO12	G2 - 1	G4 - 1
G5 - 1	G3 - 1	DIO13	QO13	G3 - 1	G5 - 1
G6 - 1	G4 - 1	DIO14	QO14	G4 - 1	G6 - 1
G7 - 1	G5 - 1	DIO15	QO15	G5 - 1	G7 - 1
R0 - 1		DIO16	QO16		R0 - 1
R1 - 1		DIO17	QO17		R1 - 1
R2 - 1	R0 - 1	DIO18	QO18	R0 - 1	R2 - 1
R3 - 1	R1 - 1	DIO19	QO19	R1 - 1	R3 - 1
R4 - 1	R2 - 1	DIO20	QO20	R2 - 1	R4 - 1
R5 - 1	R3 - 1	DIO21	QO21	R3 - 1	R5 - 1
R6 - 1	R4 - 1	DIO22	QO22	R4 - 1	R6 - 1
R7 - 1	R5 - 1	DIO23	QO23	R5 - 1	R7 - 1
Shift CLK	Shift CLK	IDCK	ODCK	Shift CLK	Shift CLK
VSYNC	VSYNC	VSYNC	VSYNC	VSYNC	VSYNC
HSYNC	HSYNC	HSYNC	HSYNC	HSYNC	HSYNC
DE	DE	DE	DE	DE	DE