

# SPECIFICATION FOR APPROVAL

(	) Preliminary	Specification
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( ● ) Final Specification

Title	21.5" Full HD TFT LCD

BUYER	APPLE		
MODEL	K60		

SUPPLIER LG Display Co., Ltd.

\*MODEL LM215WF3

SUFFIX SDC2

\*When you obtain standard approval,

please use the above model name without suffix

APPROVED BY	SIGNATURE DATE
	_
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/	
Please return 1 copy for you	ur confirmation with

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## **RECORD OF REVISIONS**

Revision Date	Page	Description
Sep.15 . 2010	_	First Draft (Preliminary)
Jan.13 . 2011	32~35	Update EDID
WW/WW/	WW/	
	Sep.15 . 2010  Jan.13 . 2011	Sep.15 . 2010 -

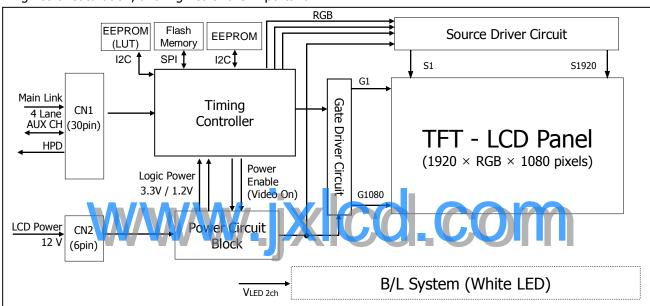


#### 1. General Description

LM215WF3 is a Color Active Matrix Liquid Crystal Display with Light Emitting Diode (White LED) backlight system without LED driver. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 21.5inch diagonally measured active display area with Full HD resolution (1920 vertical by 1080 horizontal pixel array) Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8bit gray scale signal for each dot, thus, presenting a palette of more than 16M colors.

It has been designed to apply the 8bit 4Lane Display port interface.

It is intended to support displays where high brightness, super wide viewing angle, high color saturation, and high color are important.



[ Figure 1 ] Block diagram

#### **General Features**

Active Screen Size	21.46 inches(545.22mm) diagonal
Outline Dimension	495.6(H) x 305.25(V) x 14.8(D) mm(Typ.)
Pixel Pitch	0.2475mm x 0.2475mm
Pixel Format	1920 horiz. By 1080 vert. Pixels RGB stripes arrangement
Color Depth	8-bit, 16,777,216 colors
Luminance, White	365 cd/m² ( 5point Avg.)
Viewing Angle(CR>10)	View Angle Free (R/L 178(Typ.), U/D 178(Typ.))
Power Consumption	Total 46.7 Watt (Max.) (7.5 Watt @VLCD, Max 39.2 Watt_ Duty 100% of DC 350 mA_ w/o driver)
Weight	2100g (typ.)
Display Operating Mode	Transmissive mode, normally black
Surface Treatment	Hard coating(2H), Glare (Low Reflection treatment of the front polarizer)



### 2. Absolute Maximum Ratings

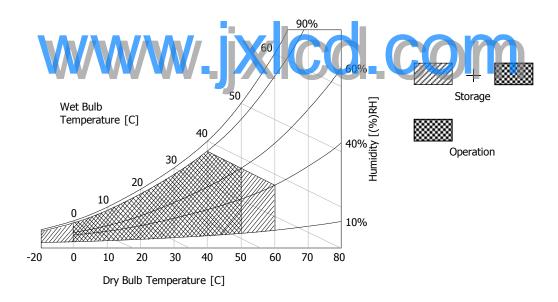
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Valu	ies	Units	Notes	
Parameter	Symbol	Min	Max	Offics		
Power Input Voltage	VLCD	-0.3	14	Vdc	at 25 ± 2°C	
Operating Temperature	Тор	0	50	°C		
Storage Temperature	Тѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH		

#### Note.

- 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C Max, and no condensation of water.
- 2. Storage condition is guaranteed under packing condition.



[ Figure 2 ] Temperature and relative humidity



### 3. Electrical Specifications

#### 3-1. Electrical Characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the WLED.

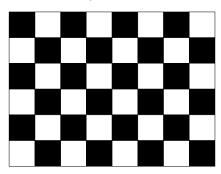
Table 2-1. ELECTRICAL CHARACTERISTICS

Parameter	Symbol		Values	Unit	Notes	
raianictei	Symbol	Min	Тур	Max	Offic	140005
MODULE:						
Power Supply Input Voltage	VLCD	11.4	12.0	12.6	Vdc	
Permissive Power Input Ripple	VRF	-	-	400	mV	
Dower Cupply Input Current	Tuch	310	387	464	mA	1
Power Supply Input Current	ILCD	416	520	624	mA	2
Dower Consumption	PLCD		4.64	5.57	Watt	1
Power Consumption	PLCD		6.24	7.50	Watt	2
Rush current	Irush	-	-	3.0	Α	3

#### Note.

- 1. The specified current and power consumption are under the  $V_{LCD}$ = 12.0V, 25  $\pm$  2°C,  $f_V$ =60Hz condition whereas mosaic pattern(8 x 6) is displayed and  $f_V$  is the frame frequency.
- 2. The current is specified at the maximum current pattern.
- 3. The duration of rush current is about 2ms and rising time of power Input is 1ms(min.)

White: 255Gray Black: 0Gray



Mosaic Pattern(8 x 6)

Maximum current pattern



White Pattern



Table 2-2. LED Bar ELECTRICAL CHARACTERISTICS

Items	Symbol	Spec			Unit	Remark	Notes
items	Symbol	Min	Тур	Max	Ullit	Remark	Notes
LED String Voltage	$V_{S}$	49.6	52.8	56.0	Vrms Ta=25°C, at Duty 100% of DC 350mA		1,2,3, 7
LED String Power	$P_{S}$	17.36	18.48	19.60	W	Ta=25℃, at Duty 100% of DC 350mA	1,2,3, 4,6,7
BL Power	$P_{BL}$	ı	36.96	39.2	W	Ta=25℃, at Duty 100% of DC 350mA	1,2,4, 6,7
LED Life Time	LED_LT	30K		ı	Hrs	Tj≤90℃, at Duty 100% of DC 350mA	5,7,8
LED Junction Temperature	Tj			150	°C	-	7,8

#### LED driver design guide

: The design of the LED driver must have specifications for the LED in LCD Assembly.

The performance of the LED in LCM, for example life time or brightness, is extremely influenced by the characteristics of the LED driver.

So all the parameters of an LED driver should be carefully designed and output current should be Constant current control.

Please control feedback current of each string individually to compensate the current variation among the strings of LEDs.

When you design or order the LED driver, please make sure unwanted lighting caused by the mismatch of the LED and the LED driver (no lighting, flicker, etc) never occurs.

When you confirm it, the LCD module should be operated in the same condition as installed in your instrument.

- 1. Specified values are for a single LED bar.
- 2. The specified current is input LED chip 100% duty current.
- The specified voltage is input LED string and Bar voltage at typical 350mA 100% duty current.
- 4. The specified power consumption is input LED bar power consumption at typical 350mA 100% duty current.
- 5. The LED life time is determined as the time at which brightness of the LED is 70% compared to that of initial value at the typical LED current on condition of continuous operating at below junction temperature 90°C.
- 6. The LED power consumption shown above does not include loss of external driver.
  - The used LED BL current is the LED typical current.
  - String Power Consumption is calculated with  $P_S = V_S x 350 \text{mA}$
  - BL Power Consumption is calculated with  $P_{BL} = V_S \times 350 \text{mA} \times 2(\text{string no.})$
- 7. LED operating DC Forward Current and Junction Temperature must not exceed LED Max Ratings.
- 8. The LED life time and the maximum rating of LED junction temperature are evaluated at LED package level, not at liquid crystal module level.



#### 3-2. Interface Connections

#### 3-2-1. LCD Module

- LCD Connector(CN1): 20525-030E-01 (I-PEX) or Equivalent

Table 3. MODULE CONNECTOR(CN1) PIN CONFIGURATION

Symbol	Description	No	Symbol	Description
GND	High Speed Ground	16	Lane0N	Component Signal for Main Link 0
DDC_SDA	DDC Data	17	GND	High Speed Ground
DDC_SCL	DDC Clock	18	Lane1P	True Signal for Main Link 1
GND	High Speed Ground	19	Lane1N	Component Signal for Main Link 1
I2C_SDA	DDC Data	20	GND	High Speed Ground
I2C_SCL	DDC Clock	21	Lane2P	True Signal for Main Link 2
GND	High Speed Ground	22	Lane2N	Component Signal for Main Link 2
SPDIF	Audio output from DP RX	23	GND	High Speed Ground
OPTION-1	Logic (N.C)	24	La <mark>n</mark> e3P	True Signal for Main Link 3
HPD	Hot Plug Detect Signal	25	Lane3N	Component Signal for Main Link 3
GND	High Speed Ground	26	GND	High Speed Ground
AUX_CHN	Component Signal for Auxiliary Channel	27	VIDEO_ON	Video status from DP RX
AUX_CHP	True Signal for Auxiliary Channel	28	OPTION-2	Logic (N.C)
GND	High Speed Ground	29	VSYNC	Vertical Frame Sync.
Lane0P	True Signal for Main Link 0	30	GND	High Speed Ground
	GND DDC_SDA DDC_SCL GND I2C_SDA I2C_SCL GND SPDIF OPTION-1 HPD GND AUX_CHN AUX_CHP GND	GND High Speed Ground  DDC_SDA DDC Data  DDC_SCL DDC Clock  GND High Speed Ground  I2C_SDA DDC Data  I2C_SCL DDC Clock  GND High Speed Ground  SPDIF Audio output from DP RX  OPTION-1 Logic (N.C)  HPD Hct Plug De ect Signal  GND High Speed Ground  AUX_CHN Component Signal for Auxiliary Channel  AUX_CHP True Signal for Auxiliary Channel  GND High Speed Ground	GND High Speed Ground 16  DDC_SDA DDC Data 17  DDC_SCL DDC Clock 18  GND High Speed Ground 19  I2C_SDA DDC Data 20  I2C_SCL DDC Clock 21  GND High Speed Ground 22  GND High Speed Ground 22  SPDIF Audio output from DP RX 23  OPTION-1 Logic (N.C) 24  HPD Hot Plug De ect Signa 25  GND High Speed Ground 26  AUX_CHN Component Signal for Auxiliary Channel 27  AUX_CHP True Signal for Auxiliary Channel 28  GND High Speed Ground 29	GND High Speed Ground 16 LaneON  DDC_SDA DDC Data 17 GND  DDC_SCL DDC Clock 18 Lane1P  GND High Speed Ground 19 Lane1N  I2C_SDA DDC Data 20 GND  I2C_SCL DDC Clock 21 Lane2P  GND High Speed Ground 22 Lane2N  SPDIF Audio output from DP RX 23 GND  OPTION-1 Logic (N.C) 24 Lane3P  HPD Hot Plug Delect Signal 25 Lane3N  GND High Speed Ground 26 GND  AUX_CHN Component Signal for Auxiliary Channel 27 VIDEO_ON  AUX_CHP True Signal for Auxiliary Channel 28 OPTION-2  GND High Speed Ground 29 VSYNC

Note: 1. All GND(ground) pins should be connected together and to Vss which should also be connected to the LCD's metal frame.



[ Figure 3 ] User Connector diagram



#### 3-2-2. User Connector

- LCD Connector(CN2): 53780-8606 (Manufactured by Molex) or Equivalent

Table 4. LCM POWER CONNECTOR(CN2) PIN CONFIGURATION

Pin	Symbol	Description	NOTES				
1	GND	GROUND					
2	GND	GND GROUND					
3	GND	GND GROUND					
4	VLCD	12V for LCM main power					
5	VLCD	12V for LCM main power					
6	VLCD	12V for LCM main power					

Note: 1) NC: No Connection



Rear view of LCM

[ Figure 3-1 ] User Connector diagram

- LCD Connector(CN6): 53780-8604 (Manufactured by Molex) or Equivalent

Table 4-1. Thermal sensor CONNECTOR(CN6) PIN CONFIGURATION

Pin	Symbol	Description	NOTES
1	DXP	Positive connection to remote temperature sensor.	
2	DXN	Negative connection to remote temperature sensor.	
3	GND	Ground	
4	VSYNCM	VSYNCM for synchronized LED Driver	

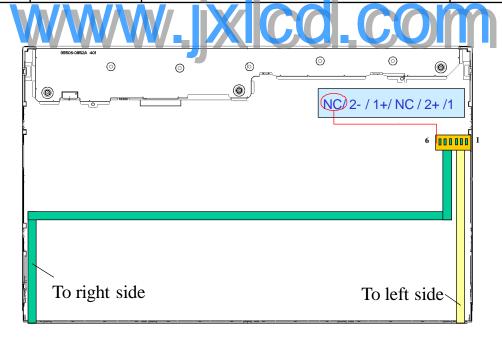


### 3-2-3. Backlight Interface

Driver connector: H401K-D06N-12B (Manufactured by E&T) Mating Connector: 4530K-F06N-01R (Manufactured by E&T)

**Table 5. LED DRIVER CONNECTOR PIN CONFIGURATION** 

Pin	Symbol	Description	NOTES
1	LED1-	LED channel 1 cathode – Left bar	
2	LED2+	LED channel 2 Anode – Left bar	
3	NC	NC	
4	LED1+	LED channel 1 Anode – Right bar	
5	LED2-	LED channel 2 cathode – Right bar	
6	NC	NC NC	



[ Figure 4 ] LED Driver Connector Pin



### 3-3. Signal Timing Specifications

All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Table 6. TIMING TABLE

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Period	tclk	-	7.22	-	ns	
DCLK	Frequency	-	-	138.5	-	MHz	
	total	thp	-	2080	-	tclk	
l	Frequency	f⊦	-	66.59	-	KHz	
Horizontal	Blanking		-	160	-	tclk	
	valid	twн	-	1920	-	tclk/2	
	total	tvp	-	1111	-	thp	
Vertical	Frequency	fv	-	60	-	Hz	
Vertical	Blanking		-	31	-	thp	
	valid	twv	-	1080	-	thp	

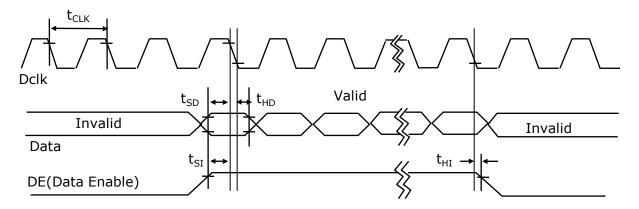
Note: Hsync period and Hsync width-active should be even number times of tCLK. If the value is odd number times of tCLK, display control signal can be asynchronous. In order to operate this LCM a Hsync, Vsync, and DE(data enable) signals should be used.

- 1. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rates.
- 2. Vsync and Hsync should be keep the above specification.
- 3. Hsync Period, Hsync Width, and Horizontal Back Porch should by any times of character number(8).
- 4. The polarity of Hsync, Vsync is not restricted.

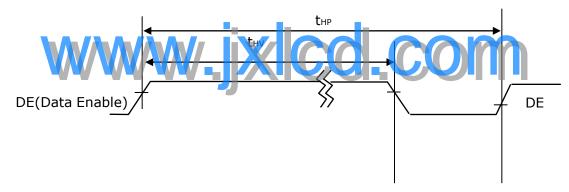


## 3-4. Signal Timing Waveforms

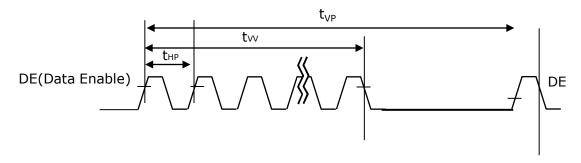
### 1. Dclk, DE, DATA waveforms



#### 2. Horizontal waveform



### 3. Vertical waveform





## 3-5. Color Input Data Reference

The Brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

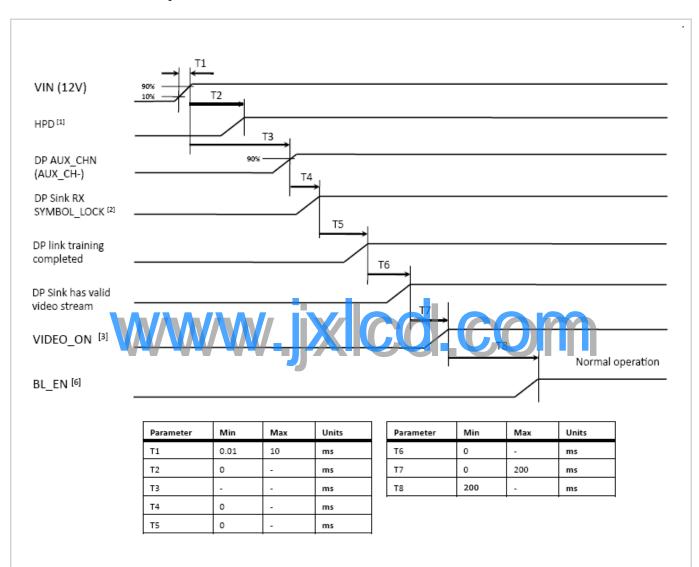
**Table 7. COLOR DATA REFERENCE** 

												Inpu	ut Co	olor	Data	a									
	Color				RI	ΞD							GRI	EEN							BL	UE			
	Coloi	MS	SB					L	SB	MS	SB					L	SB	MS	В					L	SB
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	В5	В4	В3	B2	В1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic Color	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	X	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																									
	RED (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN																									
	GREEN (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE		Ĺ																							
	BLUE (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



#### 3-6. Power Sequence

### 3-6-1. Power Sequence



Notes: [1] HPD is asserted high by Sink at power-up

[2] SYMBOL\_LOCK indicated by contents of Sink DPCD registers 00202h to 00205h

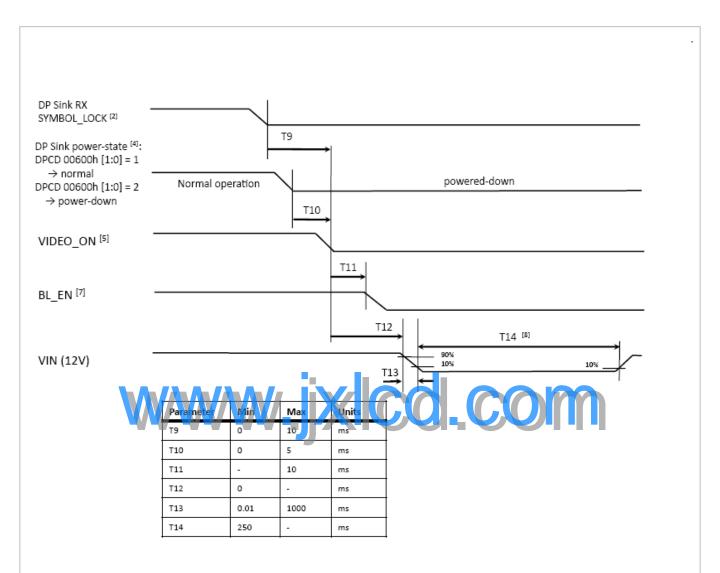
[3] VIDEO\_ON asserted high by Sink when video to panel is valid

[6] BL\_EN is an active-high MLB enable signal for panel BLU

Notes: 1. Please avoid floating state of interface signal at invalid period.

- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD  $V_{LCD}$  to 0V.
- 3. LED power must be turn on after power supply for LCD and interface signal are valid.



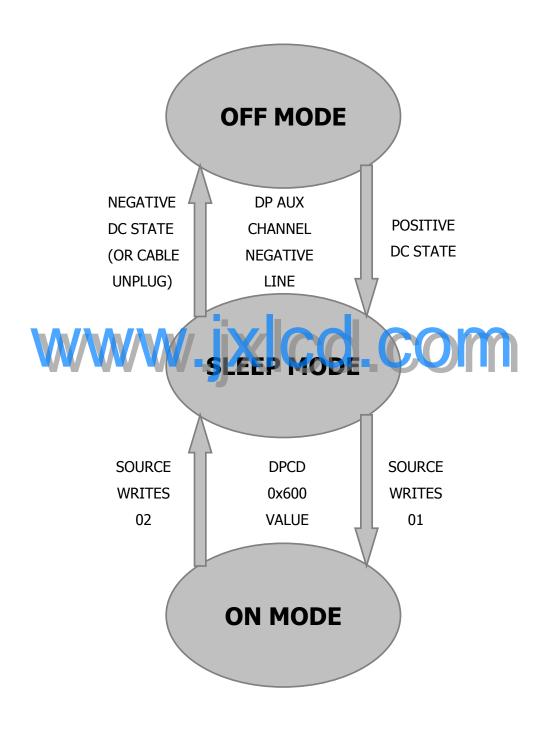


Notes: [2] SYMBOL\_LOCK indicated by contents of Sink DPCD registers 00202h to 00205h

- [4] Power-state set by Source in Sink DPCD register 00600h
- [5] VIDEO\_ON asserted low by Sink because of :
  - 1) loss of SYMBOL\_LOCK or
  - 2) DP Sink is powered down
- [7] BL\_EN must be asserted low by system as rapidly as possible when video is invalid to avoid visible artifacts
- [8] T14 defines minimum off-time for 12V power
- [9] min. times of 0 indicate precedence ordering of events, e.g. where actual timing is TBD



### 3-6-2. State Machine

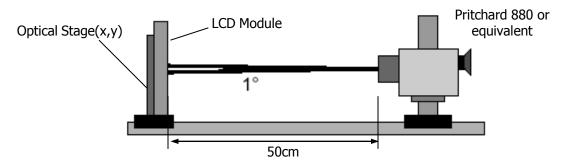




#### 4. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' for approximately 70 minutes in a dark environment at  $25\pm2^{\circ}$ C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of  $\Phi$  and  $\theta$  equal to 0 ° and aperture 1 degree.

FIG. 8 presents additional information concerning the measurement equipment and method.



[ Figure 5 ] Optical characteristic measurement equipment and method

**Table 8. OPTICAL CHARACTERISTICS** (Ta=25 °C,  $V_{LCD}$ =12.0V,  $f_V$ =60Hz Dclk=138.5MHz)

	Parame	tor	Symbol		Values		Units	Notes
	Parame	tei	Зуппрог	Min	Тур	Max	UTILS	Notes
Contrast Rat	io		CR	700	1000	- (		1
Surface Lum	in <mark>ance,</mark> v	vhite	L <sub>wh</sub>	290	365		cd/m²	2
Luminance V		VWWV	V δ WHITE			30	%	3
Dosnonso Ti	mo	Rise Time	Tr <sub>R</sub>	-	6.5	12	ms	4.1
Response Ti	ille	Decay Time	$Tr_D$	-	7.5	12	ms	4.1
		RED	Rx		0.653	ļ		
			Ry		0.332			
		GREEN	Gx		0.301			
Color Coordinates		Gy	Тур	0.618	Тур			
[CIE1931]		BLUE	Bx	-0.03	0.147	+0.03		
			Ву		0.048			
		WHITE	Wx		0.313			
			Wy		0.329			
Calay Chift		Horizontal	$\theta_{CST\_H}$	-	178	-	Dogwoo	5
Color Shift		Vertical	$\theta_{\text{CST\_V}}$	-	178	-	Degree	5
Viewing Ang	le (CR>1	0)						
Conoral	Horizor	ntal	$\theta_{H}$	170	178	-	Dograd	6
General	Vertica	I	$\theta_{\sf V}$	170	178	-	Degree	6
Effective	Horizon	tal	$\theta_{\text{GMA\_H}}$		178	-	Dograd	7
Ellective	Vertical		$\theta_{GMA\_V}$		178	-	Degree	/
Gray Scale					2.2			8



Notes 1. Contrast Ratio(CR) is defined mathematically as:

$$Contrast\ Ratio = \frac{SurfaceLuminance\ with\ all\ white\ pixels}{SurfaceLuminance\ with\ all\ black\ pixels}$$

It is measured at center point(Location P1)

- 2. Surface luminance(LwH)is luminance value at 5 points average across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 9.  $L_{WH} = Average[L_{on}1,L_{on}2,L_{on}3,L_{on}4,L_{on}5]$
- 3. The variation in surface luminance ,  $\delta$  WHITE is defined as :

$$\delta_{\textit{WHITE}} = \frac{\text{Maximum}(L_{\text{on1}}, L_{\text{on2}}, ..... L_{\text{on13}}) - \text{Minimum}(L_{\text{on1}}, L_{\text{on2}}, ..... L_{\text{on13}})}{\text{Average}(L_{\text{on1}}, L_{\text{on2}}, .... L_{\text{on5}})} \times 100(\%)$$

Where L1 to L13 are the luminance with all pixels displaying white at 13 locations. For more information see FIG 9.

- 4. Response time is the time required for the display to transition from black to white (Rise Time,  $Tr_R$ ) and from white to black (Decay Time,  $Tr_D$ ). For additional information see FIG 10
- 5. Color shift is the angle at which the color difference is lower than 0.04.



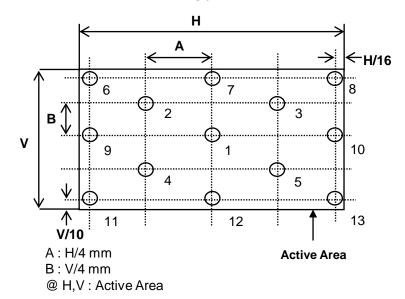
$$\Delta u'v' = \sqrt{(u'_1 - u'_2)^2 + (v'_1 - v'_2)^2}$$

u'1, v'1 : u'v' value at viewing angle direction u'2, v'2 : u'v' value at front  $(\theta=0)$ 

- Pattern size: 25% Box size
- Viewing angle direction of color shift: Horizontal, Vertical
- 6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 12.
- 7. Effective viewing angle is the angle at which the gamma shift of gray scale is lower than 0.3. For more information see FIG 13 and FIG 14.
- 8. Gray scale specification Gamma Value is approximately 2.2. For more information see Table 10.

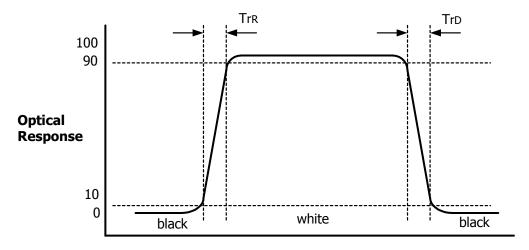


Measuring point for surface luminance & measuring point for luminance variation.



[ FIG 6 ] Measure Point for Luminance

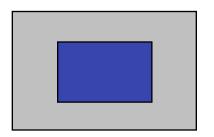
The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



[FIG 7] Response Time



Color shift is defined as the following test pattern and color.



25% Box size

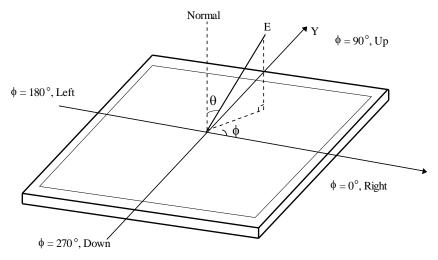
[ FIG 8 ] Test Pattern

Average RGB values in Bruce RGB for Macbeth Chart

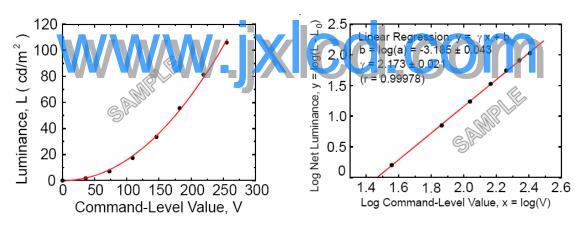
	Dark skin	Light skin	Blue sky	Foliage	Blue flower	Bluish green
R	98	206	85	77	129	114
G	56	142	112	102	118	199
В	45	123	161	46	185	178
	Orange	Purplish blue	Moderate red	Purple	Yellow green	Orange yellow
R	219	56	211	76	160	230
G	104	69	67	39	193	162
В	24	174	87	86	58	29
	Blue	Green	Red	Yellow	Magenta	cyan
R	26	72	197	241	207	35
G	32	148	27	212	62	126
В	145	65	37	36	151	172
	White	Neutral 8	Neutral 6.5	Neutral 5	Neutral 3.5	black
R	240	206	155	110	63	22
G	240	206	155	110	63	22
В	240	206	155	110	63	22



Dimension of viewing angle range.



[FIG 9] Viewing angle



[ FIG 10 ] Sample Luminance vs. gray scale (using a 256 bit gray scale)

 $L = aV^r + L_b$ 

[ FIG 11 ] Sample Log-log plot of luminance vs. gray scale

$$\log(L - L_b) = r \log(V) + \log(a)$$

Here the Parameter  $\alpha$  and  $\gamma$  relate the signal level V to the luminance L.

The GAMMA we calculate from the log-log representation (FIG. 11)



**Table 9. Gray Scale Specification** 

Gray Level	Relative Luminance [%] (Typ.)
0	0.10
31	1.08
63	4.71
95	11.5
127	21.7
159	35.5
191	53.1
223	74.5
255	100





#### 5. Mechanical Characteristics

The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

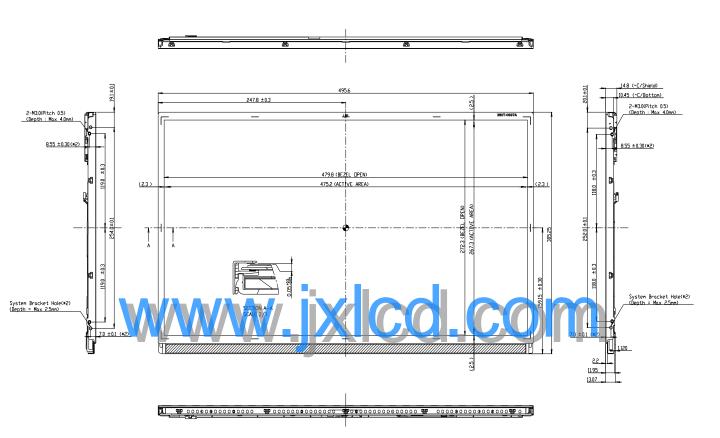
	Horizontal	495.6mm				
Outline Dimension	Vertical	305.25mm				
	Depth	14.8mm				
Bezel Area	Horizontal	479.84mm				
Dezel Aled	Vertical	272.32mm				
Active Dicalov Aven	Horizontal	475.2mm				
Active Display Area	Vertical	267.3mm				
Weight	2100g (Typ.)					
Surface Treatment	Hard coating(2H) Glare, Low Reflection treatment of the front polarizer					

Notes: Please refer to a mechanic drawing in terms of tolerance at the next page.



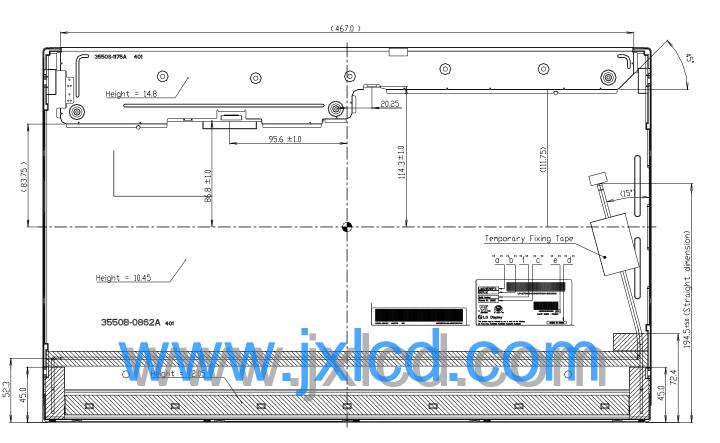


<FRONT VIEW>

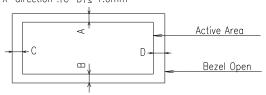


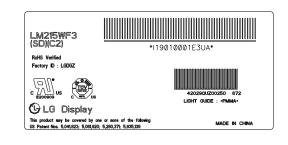


#### <REAR VIEW>



- 1. Unspecified tolerances are to be  $\pm 0.5$ mm.
- 2. Tilt and partial disposition tolerance of display area are following.
- (1) Y-direction :|A-B|≤ 1.0mm (2) X-direction :|C-D|≤ 1.0mm





- 3. Unspecified contents have to be discussed with designer
- 4. Torque Spec of User Mounting: 7.0 ~ 8.0kgf cm
- 5. LCM Weight: 2.3kg (Typ.), 2.4kg (Max.)
- 6. The ass'y should have no defect in appearance.
- 7. LCM Flatness spec : Max 0.5mm
  - Measuring method : The gap is less than 0.5 from the flat surface plate to front side.



## 6. Reliability

### Environment test condition

No	Test Item	Condition							
1	High temperature storage test	Ta= 60°C 240h							
2	Low temperature storage test	Ta= -20°C 240h							
3	High temperature operation test	Ta= 50°C 50%RH 240h							
4	Low temperature operation test	Ta= 0°C 240h							
5	Vibration test (non-operating)	Wave form: random Vibration level: 1.0G RMS Bandwidth: 10-300Hz Duration: X,Y,Z, 20 min One time each direction							
6	Shock test (non-operating)	Shock level : 120G Waveform : half sine wave, 2ms Direction : $\pm$ X, $\pm$ Y, $\pm$ Z One time each direction							
7	Humidity condition Operation	Ta=.40 °C,90%RH							
8	Altitude storage / shipment	0 - 40,000 feet(12,192m)							



#### 7. International Standards

#### 7-1. Safety

- a) UL 60950-1, Second Edition, Underwriters Laboratories Inc.
  Information Technology Equipment Safety Part 1: General Requirements.
- b) CAN/CSA C22.2 No.60950-1-07, Second Edition, Canadian Standards Association. Information Technology Equipment Safety Part 1: General Requirements.
- c) EN 60950-1:2006 + A11:2009, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment Safety Part 1 : General Requirements.
- d) IEC 60950-1:2005, Second Edition, The International Electrotechnical Commission (IEC). Information Technology Equipment - Safety - Part 1 : General Requirements. (Including report of IEC60825-1:2001 clause 8 and clause 9)

#### **Notes**

1. Laser (LED Backlight) Information

IEC60825-1 : 2001
Embedded LED Power (Class1M)
Power : 5.6602 mW (Max.)
Wavelength : 453 (nm)
Width : 1.0 x 1.0 (mm)

Class 1M LED Product

2. Caution : LED inside. Class 1M laser (LEDs) radiation when open.

Do not open while operating.

#### 7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

#### 7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003



### 8. Packing

### 8-1. Designation of Lot Mark

a) Lot Mark

Α	В	С	D	Е	F	G	Н	I	J	K	L	М
---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C: SIZE(INCH) D: YEAR

E: MONTH  $F \sim M$ : SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

#### 2. MONTH

M	onth	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
M	1ark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

## 8-2. Packing Form

a) Package quantity in one box: 7pcs

b) Box Size : 360 \* 310 \* 562 (mm)



#### 9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

#### 9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

#### 9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :  $V=\pm 200$ mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In higher temperature, it becomes lower.)

  And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw. (if not, it causes metallic foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.



#### 9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

#### 9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

#### 9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

#### 9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape.
  When the protection film is peeled off, static electricity is generated between the film and polarizer.
  This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



### 10. EDID DATA FOR LM215WF3-SDC2

### 10-1. EDID Data

LM215WF3-SDC2 EDID DATA (1920X1080 @ 60Hz )

Byte#	Byte#	Field Name and Comments	Value	Value	Value	
(decimal)	(HEX)	Field Name and Comments	(HEX)	(binary)	(DEC)	
0	00	Header	00	00000000	0	
1	01	Header	FF	11111111	255	
2		Header	FF	11111111	255	
3		Header	FF	11111111	255	Header
44		Header	FF	11111111	255	
5		Header	FF	11111111	255	
6		Header	FF 00	11111111	255	
7 8		Header EISA manufacture code ( 3 Character ID ) APP	06	000000110	6	
9		EISA manufacture code ( S Character ID ) APP EISA manufacture code (Compressed ASC II)	10	00010000	16	
10		Panel Supplier Reserved - Product Code AOCh	0C	00001100	12	product ID for <b>LM215WF3-5DC2</b> =
11	OB	( Hex. LSB first )	A0	10100000	160	0xA00C
12	00	32-bit serial #	00	00000000	0	Vender/
13	OD	32 bt 3010 #	00	00000000	0	Product ID
14	0E		00	00000000	0	110ddcc 15
15	OF		00	00000000		
16	10	Week of Manufacture	32	00110010	50	50th weeks
17	11	Year of Manufacture 2010 years	14	00010100	20	COLL MODIO
18		EDID structure version # = 1	01	000000001	1	EDID Version/
19		EDID revision # = 4	04	000000001	4	Revision
20		Video input Definition = DisplayPort 10bit	B5	10110101	181	TCYISIOT1
21		Max H image size (Rounded cm) = 48 cm	30	00110000	48	Display
22	16	Max V image sizer(Rounded cm) = 27 cm //	1B	00011011	27	Parameter
23	17	Display gamma = (gamma* (00)-100 = Example:(2:2*100)-100=120 Feature Support Display Rower Managament (DPM): No_stanby No_suspend Active Off (Very Low Powers Display Color Type:	78	01111000	120	
		Feature Support ( Display Power Management (DPM) :				
		No_stanby, No_suspend, Active Off/Very Low Power,, Display Color Type:				
24	18	Monochrome of Grayscale display , Other Feature Support Flags ; No_sRGB, Preferred Timing Mode, No_Display is continuous frequency	22	00100010		
		(Multi-mode_Base EDID and Extension Block).]			34	
25	19	Red/Green Low Bits (RxRy/GxGy)	6F	01101111	111	
26		Blue/White Low Bits (BxBy/WxWy)	B1	10110001	177	
27	1B	Red X Rx = 0.653	A7	10100111	167	
28	1⊂	Red Y Ry = 0.334	55	01010101	85	
29	1D	Green X Gx = 0.300	40	01001100	76	Color
30		Green Y Gy = 0.620	9E	10011110	158	Characteristic
31		Blue X Bx = 0.146	25	00100101	37	
32		Blue Y By = 0.050	0C	00001100	12	
33	21	White X Wx = 0.313	50	01010000	80	
34	22	White Y Wy = 0.329	54	01010100	84	5.15.1
35	23	Established timing 1 (00h if not used)	00	00000000	0	Established
36	24	Established timing 2 (00h if not used)	00	00000000	0	Timings
37	25	Manufacturer's timings	00	00000000		
3/	25	Manufacturer's timings	"	00000000	o	
38	26	Standard timing ID1 (01h if not used)	01	00000001	1	
39	27	Standard timing ID1 (01h if not used)	01	00000001	1	
40	28	Standard timing ID2 (01h if not used)	01	00000001	1	
41		Standard timing ID2 (01h if not used)	01	00000001	1	
42		Standard timing ID3 (01h if not used)	01	00000001	<u>1</u>	
43	2B	Standard timing ID3 (01h if not used)	01	00000001	1	
44	2C	Standard timing ID4 (01h if not used)	01	00000001	1	Standard
45	2D	Standard timing ID4 (01h if not used)	01	00000001	1	Timing ID
46	2E	Standard timing ID5 (01h if not used)	01	00000001	1	Timing LD
47	2F	Standard timing IDS (01) if not used)	01	00000001	1	
48	30	Standard timing IDS (0111) Not used)	01	00000001	1	
49	31	Standard timing ID6 (01) if not used)	01	00000001		
50	32	Standard timing ID6 (0111) Not used)	01	00000001	1	
51	33	Standard timing ID7 (01H ii Hot used) Standard timing ID7 (01H if not used)	01	00000001	1	
52	34	Standard timing ID7 (01h ir not used) Standard timing ID8 (01h if not used)	01	00000001	1	
					1	
53	35	Standard timing ID8 (01h if not used)	01	00000001	1	



F4	0.0	Detailed the desired and	4.4	00011010		
54	36	Detailed timing/monitor	1A	00011010	26	
55	37	Pixel Clock = 138.5 MHz	36	00110110	54	
56	38	Hor active= 1920 pixels	80	10000000	128	
57	39	Hor blanking= 160 pixels	A0	10100000	160	
58	3A		70	01110000	112	
59	3B	Vertcal active= 1080 lines	38	00111000	56	
60	3C	Vertical blanking= 31 lines	1F	00011111	31	Detailed
61	3D		40	01000000	64	Timing
		U avea Officet 40 sixels	30	00110000	48	Description
62	3E	H sync. Offset= 48 pixels				
63	3F	H sync. Width= 32 pixels	20	00100000	32	#1
64	40	V sync. Offset=3 lines, V sync. Width= 5 lines	35	00110101	53	
	41	1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 -	00	00000000	0	
65						
66	42	H image size= 475 mm	DB	11011011	219	
67	43	V image size = 267 mm	0B	00001011	11	
	44	1 111040 1110	11	00010001	17	
68						
69	45	No Horizontal Border	00	00000000	0	
70	46	No Vertical Border	00	00000000	0	
71		Non-Interlace, Normal display, no stereo, Digital Separate [ Vsync_NEG, Hsyr	1A	00011010	26	
	47					
72	48	Detailed timing/monitor	8D	10001101	141	
73	49	Pixel Clock = 37.25 MHz	0E	00001110	14	
	4A	Hor active= 960 pixels	C0	11000000	192	
74	•					
75	4B	Hor blanking= 160 pixels	A0	10100000	160	
76	4C		30	00110000	48	
		Vertcal active= 540 lines				
77	4D		10	00011100	28	
78	4E	Vertical blanking= 16 lines	10	00010000	16	Detailed
79	4F		20	00100000	32	Timing
80		H sync. Offset= 48 pixels	30	00110000	48	Description
81		H sync, Width= 32 pixels	20	00100000	32	#2
82	52	V sync, Offset=3 lines, V sync, Width= 5 lines	35	00110101	53	
83	53		00	00000000	Ö	
	<b></b>					
84	54	H image size= 475 mm	DB	11011011	219	
85	55	V image size = 267 mm	OB	00001011	11	
86	56		11	00010001	17	
	<b></b>					
87	57	No Horizontal Border	00	00000000	0	
88	58	No Vertical Border	00	000000000	1 0	
89	59		1.4	00011010	26	
90	5A	Detailed timing/monitor descriptor #3	00	00000000	0	
91	5B	descriptor #3	00	00000000	0	
92	5C		00	00000000	0	
93	5D		FC FC	11111100	252	
94	5E		00	00000000	0	
95	5F	i	69	01101001	105	
	60			01001101		Detailed
96		M	4D		77	
97	61	a	61	01100001	97	Timing
98	62	С	63	01100011	99	Description
99	63		0A	00001010	10	#3
						#3
100	64		20	00100000	32	
101	65		20	00100000	32	Ascii Data String:
102	66		20	00100000	32	
103	67		20	00100000	32	
104	68		20	00100000	32	
105	69		20	00100000	32	
106	6A		20	00100000	32	
107	6B		20	00100000	32	
108	6C	Detailed timing/monitor	00	00000000	0	
	•				Ö	
109	6D	descriptor #4	00	00000000		
110	6E		00	00000000	0	
111	6F		00	00000000	0	
112	70		00	00000000	Ö	
		<u> </u>				
113	71		00	00000000	0	
114	72		00	00000000	0	Detailed
115	73		00	00000000	0	Timing
	<b>.</b>	<u> </u>				-
116	74		00	00000000	0	Description
117	75		00	00000000	0	#4
118	76		00	00000000	Ö	
119	<b>+</b>			00000000	i o	Monitor Name
	77		00			Monitor Name:
			00	00000000	0	Color LCD
120	78					
120				00000000	l o	
120 121	78 79		00	00000000		
120 121 122	78 79 7A		00 00	00000000	0	
120 121 122 123	78 79 7A 7B		00 00 00	00000000 00000000	0	
120 121 122	78 79 7A		00 00	00000000	0	
120 121 122 123 124	78 79 7A 7B 7C		00 00 00 00	00000000 00000000 00000000	0 0 0	
120 121 122 123 124 125	78 79 7A 7B 7C 7D	Extension Flore Of	00 00 00 00 00	00000000 00000000 00000000 00000000	0 0 0	Extension Class
120 121 122 123 124 125 126	78 79 7A 7B 7C 7D 7E	Extension Flag = 01	00 00 00 00	00000000 00000000 00000000	0 0 0	Extension Flag
120 121 122 123 124 125	78 79 7A 7B 7C 7D	Extension Flag = 01 Checksum	00 00 00 00 00	00000000 00000000 00000000 00000000	0 0 0	Extension Flag <b>Checksum</b>



128	80	Tag	02	00000010	2	
	<b>+</b>					
129	81	Revision Number	03	00000011	3	
130	82	Offset of first Detailed Timing Description	0C	00001100	12	
131	83	Device Support & No. of Native Formats	⊂1	11000001	193	
132	84	Audio Data Block Tag	23	00100011	35	
133	85	CEA Short Video Descriptor 1	09	00001001	9	
	·····					
134	86	Audio Data Block Tag	07	00000111		
134	00	Mudio Data Block Tay	07	00000111	7	
	ļ					
135	87	CEA Short Audio Descriptor 1	07	00000111	7	
136	88	Speaker Allocation Data Block Tag	83	10000011	131	
• • • • • • • • • • • • • • • • • • • •		Dealer Milocation Data block Tag			131	
137	89		01	00000001	1	
138	8A	Speaker Allocation Payload	00	00000000	0	
		Speaksi Allocadoiri ayloaa				1
139	8B		00	00000000	0	
140	8C	Detailed timing/monitor	1A	00011010	26	
=						
141	<b>4</b>	Pixel Clock = 138.5 MHz	36	00110110	54	
142	8E	Hor active= 1920 pixels	80	10000000	128	
143	8F	Hor blanking= 160 pixels	A0	10100000		
	<b></b>	Inor planking= 160 pixeis			160	
144	90		70	01110000	112	
145	91	Vertcal active= 1080 lines	38	00111000	56	
			•••••		20	
146	92	Vertical blanking= 31 lines	1F	00011111	31	
147	93		40	01000000		
	L				64	
148	94	H sync. Offset= 48 pixels	30	00110000	48	
149		H sync. Width= 32 pixels	20	00100000	32	1
150	96	V sync, Offset=3 lines	35	00110101	53	
				00000000		
151		V sync. Width= 5 lines	00		0	
152	98	H image size= 475mm	DB	11011011	219	
	•	<del>-</del>				
153	<b></b>	V image size = 267mm	OB .	00001011	11	
154	9A		11	00010001	17	
		Na Harianakal Bandan				
155	9B	No Horizontal Border	00	00000000	0	
156	9C	No Vertical Border	00	00000000	0	
157	9D	Non-interlaced, Normal display, No stereo, Digital separate sync, H/V pol Neg.	1A	00011010	26	
$\overline{}$						
158	9E	Detailed timing/monitor	8D	10001101	141	
159	9F	Pixel Clock = 37.25 MHz	0E	00001110	14	
	<u>-</u>	The same of the sa		11000000	100	
160	A0	Hor active= 960 pixels	(0)	11000000	192	
161	A1	Hor blankno = 160 pixes	A0	10100000	160	
162	A2		30	00110000	192 160 48 28	
		Vertcal active= 540 lines		00001100	20	
163			10	00011100		
164	A4	Vertical blanking= 16 lines	10	00010000	16	
165	A5		20	00100000		1
					32	
166	A6	H sync. Offset= 48 pixels	30	00110000	48	
167	A7	H sync. Width= 32 pixels	20	00100000	32	
168	A8	V sync. Offset=3 lines, V sync. Width= 5 lines	35	00110101	53	
169	A9		00	00000000	0	
170	AA	H image size= 475 mm	DB	11011011		1
	•	•			219	
171	AB	V image size = 267 mm	OB	00001011	11	
172	AC		11	00010001	17	
	<b></b>	No. 11-2-2-2-1-1-1-2-2-2-2-2-2-2-2-2-2-2-2-			• • • • • • • • • • • • • • • • • • • •	
173	AD	No Horizontal Border	00	00000000	0	1
174	AE	No Vertical Border	00	00000000	0	
175	AF		1A	00011010	26	
176	B0		00	00000000	0	
177	B1		00	00000000		
					0	
178	B2		00	00000000	0	
179	В3		00	00000000	0	
						1
180	B4		00	00000000	0	
181	B5		00	00000000	0	1
	<b>†</b>					1
182	B6		00	00000000	0	
183	B7		00	00000000	0	
	<b>†</b>			00000000		
184	B8		00		0	
185	B9		00	00000000	0	
186	BA		00	00000000	0	
	<b></b>					
187	BB		00	00000000	0	1
188	BC		00	00000000	0	
189	BD		00	00000000	0	
	<b>†</b>					1
190	BE		00	00000000	0	
191	BF		00	00000000	0	
192	CO		00	00000000	0	
	<b>†</b>		00	00000000	0	
193	C1					
194	C2		00	00000000	0	
195	C3		00	00000000	0	
• • • • • • • • • • • • • • • • • • • •						
196	C4		00	00000000	0	I



#### **Product Specification** 00 00 C6 C7 C8 C9 CA CB CC CD CE D0 201 00 00 00 00 00 206 207 208 209 00 D2 D3 D4 D5 D6 D7 D8 D9 DA 00 00 00 00 00 00 DC DD DE 00 00 00 00 DF E0 E1 E3 E4 E5 00 E6 E7 E8 E9 EA EB 00 00 00 ED 00 00 00 245 246 247 248 249 250 251 252

## 10-2. EDID DATA READ/WRITE PROTOCOL

### 10-2-1. READ Operation

<Start><Slave Address, RW=0><Byte Address><Start><Slave Address, RW=1><Data><Stop>

## 10-2-2. WRITE Operation

<Start><Slave Address, RW=0><Byte Address><Data><Stop>

Device Address (Slave Address)

Туре			Hex						
IS24C02B	1	0	1	0	0	0	0	RW	0xA0 + RW

#### - Byte Address

Byte Address						
Decimal	0 ~ 255					
Hex	0x00 ~ 0xFF					