



LM2902

LINEAR INTEGRATED CIRCUIT

QUADRUPLE OPERATIONAL AMPLIFIERS

DESCRIPTION

UTC **LM2902** consist of four independent, high-gain, frequency-compensated operational amplifiers which are designed to operate from a single power supply over a wide range of voltage. Operation from split supplies is also possible so long as the difference between the two supplies is 3V ~ 30V. The low supply current drain is independent of the magnitude of the supply voltage.

The device can easily be implemented in single supply voltage system, including transducer amplifiers, DC gain blocks, and all of conventional OP Amp circuits.

FEATURES

- * Wide Supply Voltage Range. (Single Supply: 3~30V)
- * Low Supply Current Drain Independent of Supply Voltage: 0.8 mA Typ
- * Large DC voltage gain: 100dB
- * Input Common-Mode Voltage Range Includes Ground.
- * Low Input Bias and Offset Parameters
- * Differential Input Voltage Range Equal to Maximum Rated Supply Voltage: 32V
- * Open-Loop Differential Voltage Amplification: 100 V/mV Typ.
- * Internal Frequency Compensation for Unity Gain

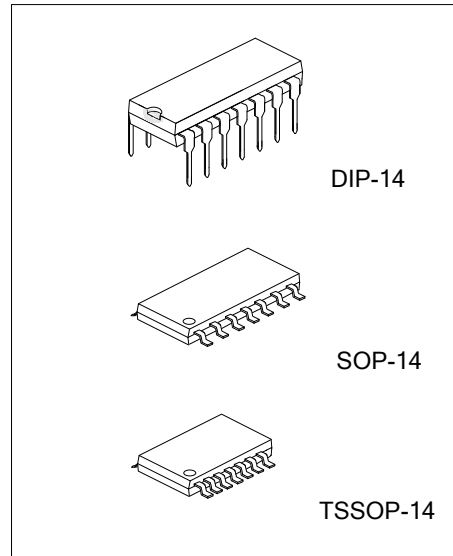
ORDERING INFORMATION

Ordering Number		Package	Packing
Lead Free	Halogen-Free		
LM2902L-D14-T	LM2902G-D14-T	DIP-14	Tube
LM2902L-S14-R	LM2902G-S14-R	SOP-14	Tape Reel
LM2902L-P14-R	LM2902G-P14-R	TSSOP-14	Tape Reel

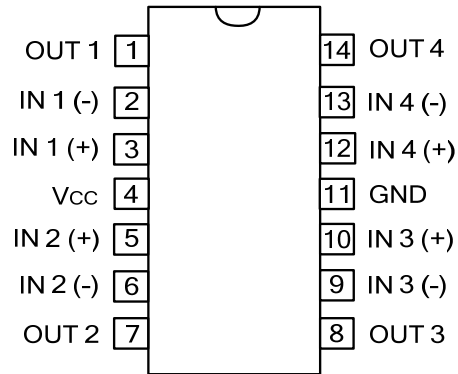
<p>LM2902G-D14-T</p> <p>(1)Packing Type (2)Package Type (3)Green Package</p>	<p>(1) T: Tube, R: Tape Reel (2) DIP: DIP-14, S14: SOP-14, P14: TSSOP-14 (3) G: Halogen Free and Lead Free, L: Lead Free</p>
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MARKING

DIP-14	SOP-14 / TSSOP-14
<p>Date Code L: Lead Free G: Halogen Free Lot Code</p>	<p>Date Code L: Lead Free G: Halogen Free Lot Code</p>



■ PIN CONFIGURATION



■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage (Note 1)	V_{CC}	32	V
Differential Input voltage (Note 2)	$V_{I(DIFF)}$	± 32	V
Input Voltage	V_{IN}	-0.3 ~ +32	V
Output Short Circuit (one amplifier) to Ground ($T_A \leq 25^\circ\text{C}$, $V_{CC} \leq 15\text{V}$) (Note 3)		Continuous	
Operation Temperature	T_{OPR}	-40 ~ +125	$^\circ\text{C}$
Storage Temperature	T_{STG}	-65 ~ +150	$^\circ\text{C}$

Notes: 1. All voltage values (except differential voltages and V_{CC} specified for the measurement of $I_{IN(OS)}$) are with respect to the network GND.

2. Differential voltages are at IN+ with respect to IN-.

3. Short circuits from outputs to V_{CC} can cause excessive heating and eventual destruction.

■ THERMAL DATA

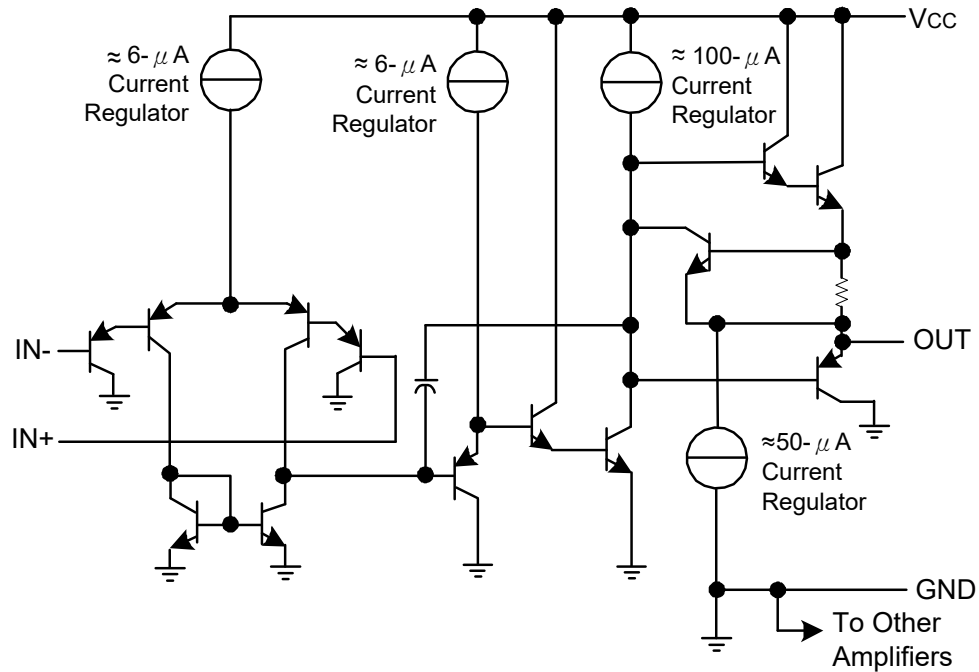
PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	SOP-14	86	$^\circ\text{C/W}$
	DIP-14	80	$^\circ\text{C/W}$
	TSSOP-14	113	$^\circ\text{C/W}$

■ ELECTRICAL CHARACTERISTICS ($V_{CC}=5\text{V}$, unless otherwise specified, $V_{CC}=30\text{V}$ for testing only.)

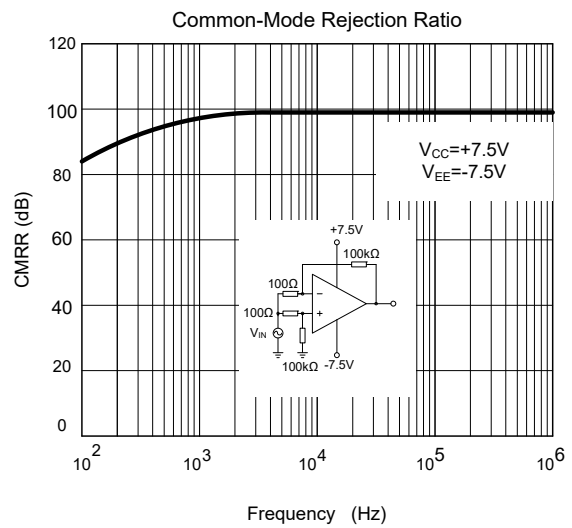
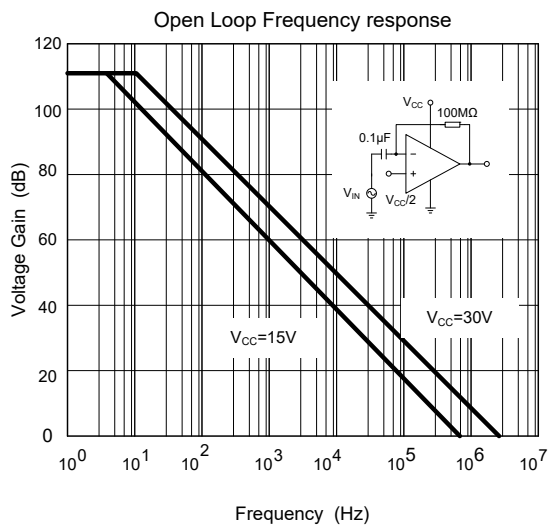
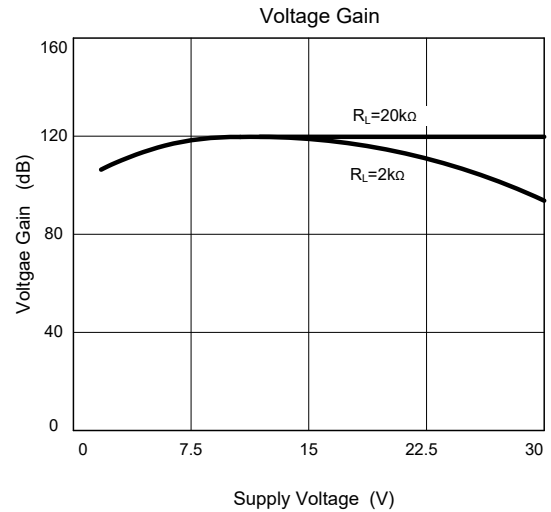
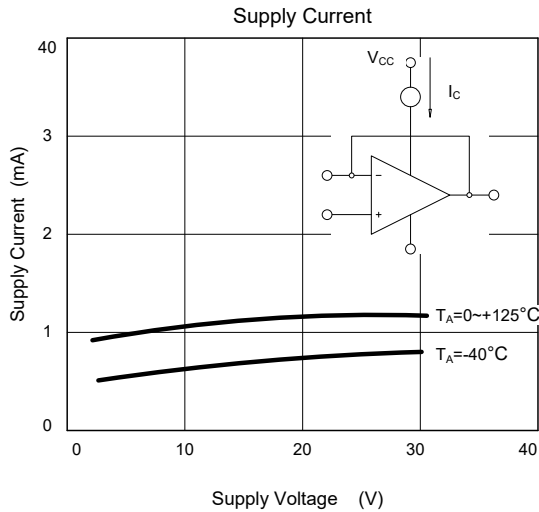
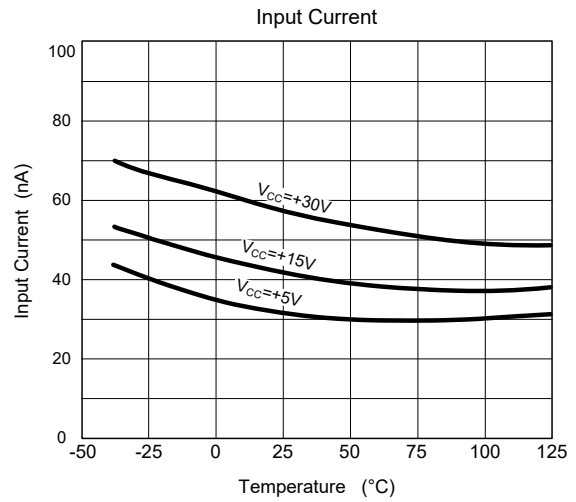
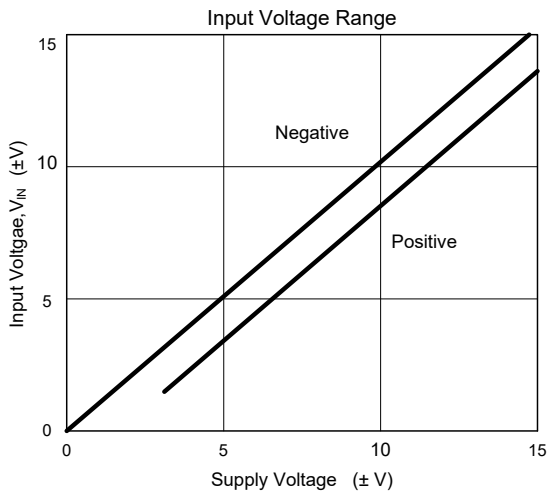
PARAMETER	SYMBOL	TEST CONDITIONS*	MIN	TYP	MAX	UNIT		
Input Offset Voltage	$V_{I(OFF)}$	$V_{CC}=5\text{V to }30\text{V}$, $V_{IC}=V_{ICR(min)}$, $V_{OUT}=1.4\text{V}$	$T_A=25^\circ\text{C}$		3	5	mV	
			$T_A=-40\sim+125^\circ\text{C}$			7	mV	
Input Offset Current	$I_{I(OFF)}$	$V_{OUT}=1.4\text{V}$	$T_A=25^\circ\text{C}$		2	30	nA	
			$T_A=-40\sim+125^\circ\text{C}$			100	nA	
Input Bias Current	$I_{I(BIAS)}$	$V_{OUT}=1.4\text{V}$	$T_A=25^\circ\text{C}$		-20	-150	nA	
			$T_A=-40\sim+125^\circ\text{C}$			-300	nA	
Input Common-mode Voltage Range	$V_{I(CM)}$	$V_{CC}=5\text{V to }30\text{V}$	$T_A=25^\circ\text{C}$	0		$V_{CC}-1.5$	V	
			$T_A=-40\sim+125^\circ\text{C}$	0		$V_{CC}-2$	V	
Output Voltage Level	High	V_{OH}	$R_L=2\text{k}\Omega$	$T_A=25^\circ\text{C}$	$V_{CC}-1.5$		V	
			$V_{CC}=30\text{V}$, $R_L=2\text{k}\Omega$	$T_A=-40\sim+125^\circ\text{C}$	26			V
			$V_{CC}=30\text{V}$, $R_L \geq 10\text{k}\Omega$		27	28		V
Low	V_{OL}	$R_L \leq 10\text{k}\Omega$	$T_A=-40\sim+125^\circ\text{C}$		5	20	mV	
Large Signal Current Gain	G_V	$V_{CC}=15\text{V}$, $V_{OUT}=1\text{V} \sim 11\text{V}$ $R_L \geq 2\text{k}\Omega$	$T_A=25^\circ\text{C}$	50	100		V/mV	
			$T_A=-40\sim+125^\circ\text{C}$	25			V/mV	
Common-mode Rejection Ratio	CMRR	$V_{IC}=V_{ICR(min)}$	$T_A=25^\circ\text{C}$	70	80		dB	
Supply Voltage Rejection Ratio ($\Delta V_{CC}/\Delta V_{IO}$)	PSRR		$T_A=25^\circ\text{C}$	65	100		dB	
Crosstalk Attenuation	V_{O1}/V_{O2}	$f=1\text{kHz} \sim 20\text{kHz}$	$T_A=25^\circ\text{C}$		120		dB	
Output Current	I_{OUT}	$V_{CC}=15\text{V}$, $V_{ID}=1\text{V}$, $V_{OUT}=0$	$T_A=25^\circ\text{C}$	-20	-30	-60	mA	
			$T_A=-40\sim+125^\circ\text{C}$	-10			mA	
			$T_A=25^\circ\text{C}$	10	20		mA	
			$T_A=-40\sim+125^\circ\text{C}$	5			mA	
Short-circuit Output Current	I_{OS}	$V_{CC}=5\text{V}$, $V_{OUT}=0$, GND at -5V	$T_A=25^\circ\text{C}$		± 40	± 60	mA	
Supply Current (four amplifiers)	I_{CC}	$V_{OUT}=2.5\text{V}$, $R_L=\infty$ $V_{CC}=30\text{V}$, $V_{OUT}=0.5V_{CC}$, $R_L=\infty$	$T_A=-40\sim+125^\circ\text{C}$		0.7	1.2	mA	
					1.4	3	mA	

Note: All characteristics are measured under open-loop conditions with zero common-mode input voltage.

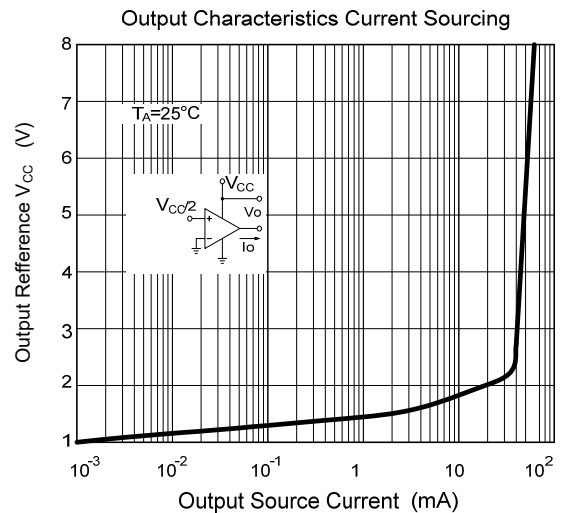
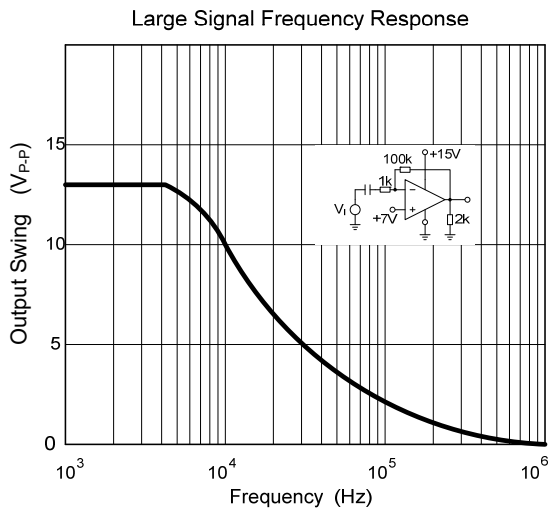
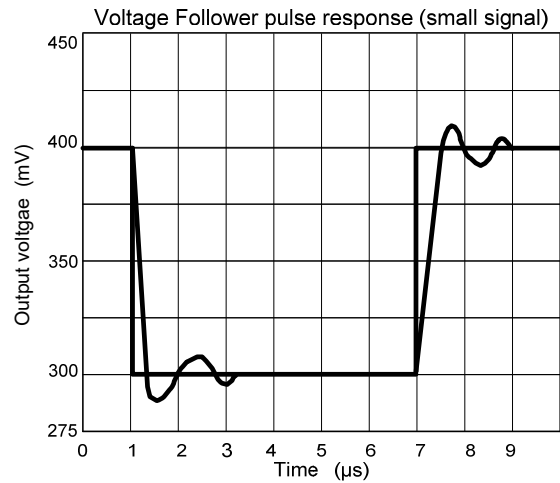
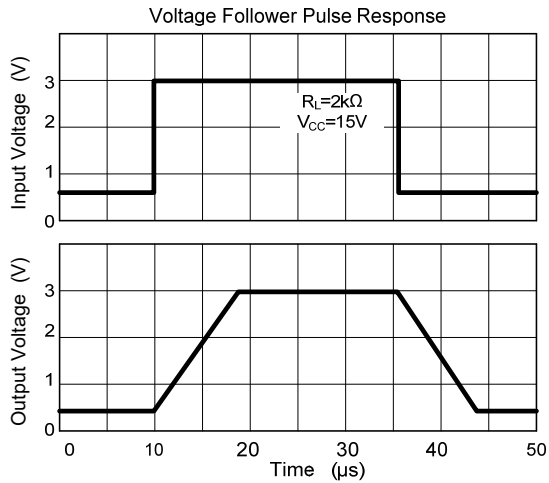
■ SCHEMATIC DIAGRAM (One Section Only)



TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (Cont.)



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