

LM2902EP Low Power Quad Operational Amplifiers

Check for Samples: [LM2902EP](#)

FEATURES

- Internally frequency compensated for unity gain
- Large DC voltage gain 100 dB
- Wide bandwidth (unity gain) 1 MHz
 - (temperature compensated)
- Wide power supply range:
 - Single supply 3V to 26V
 - or dual supplies $\pm 1.5V$ to $\pm 13V$
- Very low supply current drain (700 μA)—essentially independent of supply voltage
- Low input biasing current 45 nA
 - (temperature compensated)

- Low input offset voltage 2 mV
 - and offset current: 5 nA
- Input common-mode voltage range includes ground
- Differential input voltage range equal to the power supply voltage
- Large output voltage swing 0V to $V^+ - 1.5V$

APPLICATIONS

- Selected Military Applications
- Selected Avionics Applications

DESCRIPTION

The LM2902 consists of four independent, high gain, internally frequency compensated operational amplifiers which were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage.

Application areas include transducer amplifiers, DC gain blocks and all the conventional op amp circuits which now can be more easily implemented in single power supply systems. For example, the LM2902 can be directly operated off of the standard +5V power supply voltage which is used in digital systems and will easily provide the required interface electronics without requiring the additional $\pm 15V$ power supplies.

ENHANCED PLASTIC

- Extended Temperature Performance of $-40^{\circ}C$ to $+85^{\circ}C$
- Baseline Control - Single Fab & Assembly Site
- Process Change Notification (PCN)
- Qualification & Reliability Data
- Solder (PbSn) Lead Finish is standard
- Enhanced Diminishing Manufacturing Sources (DMS) Support

Unique Characteristics

- In the linear mode the input common-mode voltage range includes ground and the output voltage can also swing to ground, even though operated from only a single power supply voltage
- The unity gain cross frequency is temperature compensated
- The input bias current is also temperature compensated

Advantages

- Eliminates need for dual supplies
- Four internally compensated op amps in a single package
- Allows directly sensing near GND and V_{OUT} also goes to GND
- Compatible with all forms of logic

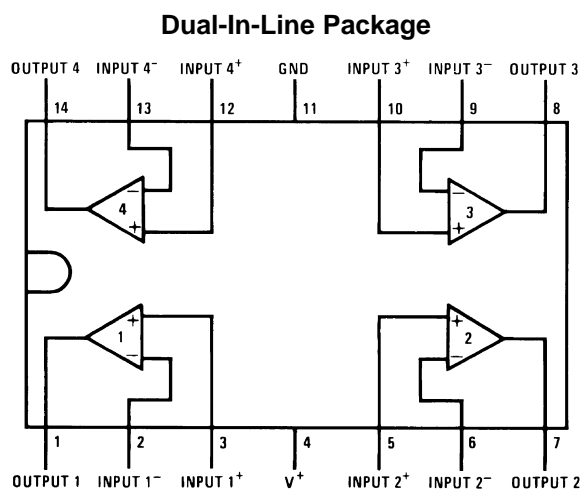


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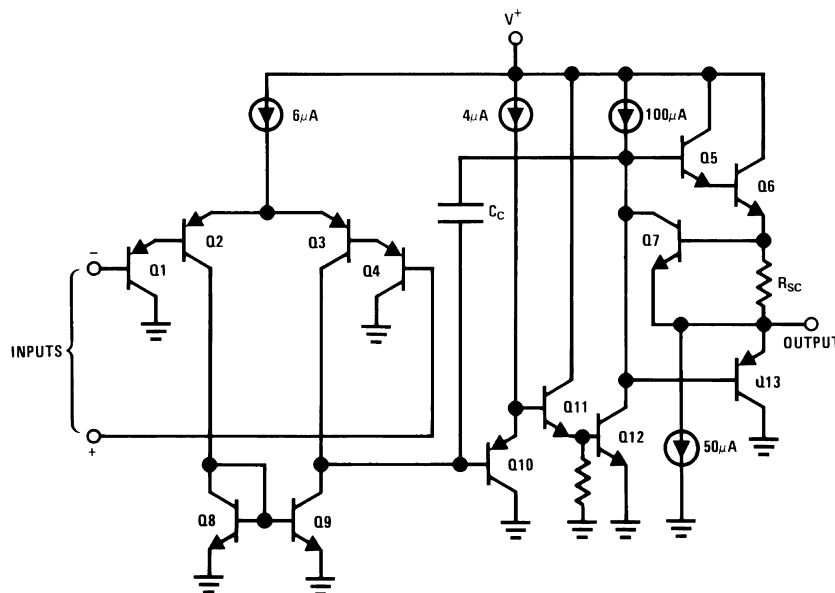
- Power drain suitable for battery operation

Connection Diagram



Schematic Diagram

(Each Amplifier)



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Absolute Maximum Ratings ⁽¹⁾

Supply Voltage, V ⁺	26V
Differential Input Voltage	26V
Input Voltage	-0.3V to +26V
Input Current	
(V _{IN} < -0.3V) ⁽²⁾	50 mA
Power Dissipation ⁽³⁾	
Molded DIP	1130 mW
Small Outline Package	800 mW
Output Short-Circuit to GND	
(One Amplifier) ⁽⁴⁾	
V ⁺ ≤ 15V and T _A = 25°C	Continuous
Operating Temperature Range	-40°C to +85°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (Soldering, 10 seconds)	220°C
Soldering Information	
Small Outline Package	
Vapor Phase (60 seconds)	215°C
Infrared (15 seconds)	220°C
ESD Tolerance ⁽⁵⁾	250V

- (1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur.
- (2) This input current will only exist when the voltage at any of the input leads is driven negative. It is due to the collector-base junction of the input PNP transistors becoming forward biased and thereby acting as input diode clamps. In addition to this diode action, there is also lateral NPN parasitic transistor action on the IC chip. This transistor action can cause the output voltages of the op amps to go to the V⁺ voltage level (or to ground for a large overdrive) for the time duration that an input is driven negative. This is not destructive and normal output states will re-establish when the input voltage, which was negative, again returns to a value greater than -0.3V (at 25°C).
- (3) For operating at high temperatures, the LM2902EP must be derated based on a +125°C maximum junction temperature and a thermal resistance of 88°C/W which applies for the device soldered in a printed circuit board, operating in a still air ambient. The dissipation is the total of all four amplifiers—use external resistors, where possible, to allow the amplifier to saturate or to reduce the power which is dissipated in the integrated circuit.
- (4) Short circuits from the output to V⁺ can cause excessive heating and eventual destruction. When considering short circuits to ground, the maximum output current is approximately 40 mA independent of the magnitude of V⁺. At values of supply voltage in excess of +15V, continuous short-circuits can exceed the power dissipation ratings and cause eventual destruction. Destructive dissipation can result from simultaneous shorts on all amplifiers.
- (5) Human body model, 1.5 kΩ in series with 100 pF.

Electrical Characteristics

$V^+ = +5.0V$, unless otherwise stated ⁽¹⁾ ⁽²⁾

Parameter		Conditions	LM2902			Units
			Min	Typ	Max	
Input Offset Voltage ⁽³⁾		$T_A = 25^\circ C$		2	7	mV
Input Bias Current ⁽⁴⁾		$I_{IN(+)} \text{ or } I_{IN(-)}, V_{CM} = 0V, T_A = 25^\circ C$		45	250	nA
Input Offset Current		$I_{IN(+)} \text{ or } I_{IN(-)}, V_{CM} = 0V, T_A = 25^\circ C$		5	50	nA
Input Common-Mode Voltage Range ⁽⁵⁾		$V^+ \text{ } V^+ = 26V, T_A = 25^\circ C$	0		$V^+ - 1.5$	V
Supply Current		Over Full Temperature Range				
		$R_L = \infty$ On All Op Amps				mA
		$V^+ = 26V$		1.5	3	
		$V^+ = 5V$		0.7	1.2	
Large Signal		$V^+ = 15V, R_L \geq 2k\Omega,$	25	100		V/mV
Voltage Gain		$(V_O = 1V \text{ to } 11V), T_A = 25^\circ C$				
Common-Mode		DC, $V_{CM} = 0V \text{ to } V^+ - 1.5V,$	50	70		dB
Rejection Ratio		$T_A = 25^\circ C$				
Power Supply		$V^+ = 5V \text{ to } 26V$	50	100		dB
RejectionRatio		$T_A = 25^\circ C$				
Amplifier-to-Amplifier		$f = 1 \text{ kHz to } 20 \text{ kHz}, T_A = 25^\circ C$		-120		dB
Coupling ⁽⁶⁾		(Input Referred)				
Output Current	Source	$V_{IN}^+ = 1V, V_{IN}^- = 0V,$	20	40		
		$V^+ = 15V, V_O = 2V, T_A = 25^\circ C$				mA
	Sink	$V_{IN}^- = 1V, V_{IN}^+ = 0V,$	10	20		
		$V^+ = 15V, V_O = 2V, T_A = 25^\circ C$				
		$V_{IN}^- = 1V, V_{IN}^+ = 0V,$	12	50		μA
		$V^+ = 15V, V_O = 200 \text{ mV}, T_A = 25^\circ C$				
Short Circuit to Ground		⁽⁷⁾ $V^+ = 15V, T_A = 25^\circ C$		40	60	mA
Input Offset Voltage		⁽³⁾			10	mV
V_{OS} Drift		$R_S = 0\Omega$		7		$\mu V/^\circ C$
Input Offset Current		$I_{IN(+)} - I_{IN(-)}, V_{CM} = 0V$		45	200	nA
I_{OS} Drift		$R_S = 0\Omega$		10		$\mu A/^\circ C$
Input Bias Current		$I_{IN(+)} \text{ or } I_{IN(-)}$		40	500	nA
Input Common-Mode Voltage Range ⁽⁵⁾		$V^+ = 26V$	0		$V^+ - 2$	V
Large Signal Voltage Gain		$V^+ = +15V (V_{OSwing} = 1V \text{ to } 11V)$				
		$R_L \geq 2 \text{ k}\Omega$	15			V/mV
Output Voltage	V_{OH}	$V^+ = 26V$	$R_L = 10 \text{ k}\Omega$	23	24	V

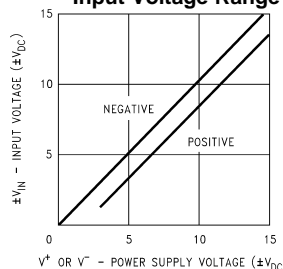
- (1) The LM2902EP specifications are limited to $-40^\circ C \leq T_A \leq +85^\circ C$.
- (2) "Testing and other quality control techniques are used to the extent deemed necessary to ensure product performance over the specified temperature range. Product may not necessarily be tested across the full temperature range and all parameters may not necessarily be tested. In the absence of specific PARAMETRIC testing, product performance is assured by characterization and/or design."
- (3) $V_O \approx 1.4V, R_S = 0\Omega$ with V^+ from 5V to 26V; and over the full input common-mode range (0V to $V^+ - 1.5V$)
- (4) The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output so no loading change exists on the input lines.
- (5) The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.3V (at 25°C). The upper end of the common-mode voltage range is $V^+ - 1.5V$ (at 25°C), but either or both inputs can go to +26V without damage, independent of the magnitude of V^+ .
- (6) Due to proximity of external components, insure that coupling is not originating via stray capacitance between these external parts. This typically can be detected as this type of capacitance increases at higher frequencies.
- (7) Short circuits from the output to V^+ can cause excessive heating and eventual destruction. When considering short circuits to ground, the maximum output current is approximately 40 mA independent of the magnitude of V^+ . At values of supply voltage in excess of +15V, continuous short-circuits can exceed the power dissipation ratings and cause eventual destruction. Destructive dissipation can result from simultaneous shorts on all amplifiers.

Electrical Characteristics (continued)
 $V^+ = +5.0V$, unless otherwise stated ⁽¹⁾ ⁽²⁾

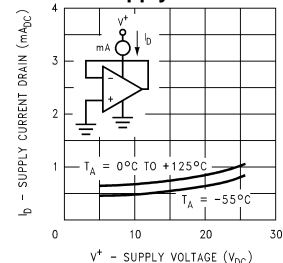
Parameter		Conditions		LM2902			Units
				Min	Typ	Max	
Swing	V_{OL}	$V^+ = 5V, R_L = 10\text{ k}\Omega$			5	100	mV
Output Current	Source	$V_O = 2V$	$V_{IN}^+ = +1V,$	10	20		
			$V_{IN}^- = 0V,$ $V^+ = 15V$				mA
	Sink		$V_{IN}^- = +1V,$	5	8		
			$V_{IN}^+ = 0V,$ $V^+ = 15V$				

Typical Performance Characteristics

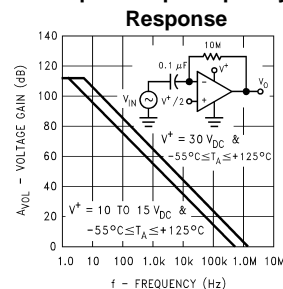
Input Voltage Range



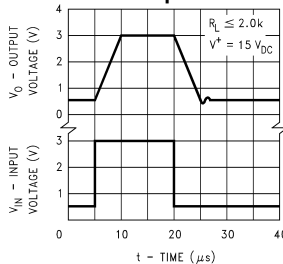
Supply Current



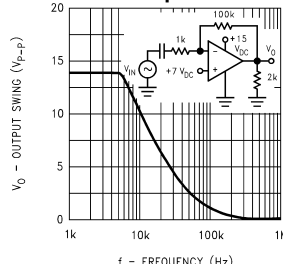
Open Loop Frequency Response



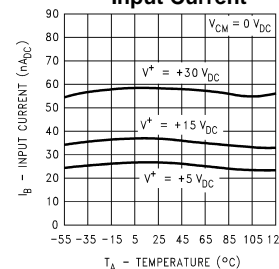
Voltage Follower Pulse Response



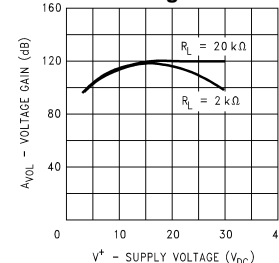
Large Signal Frequency Response



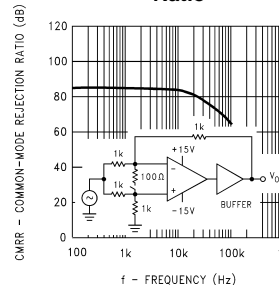
Input Current



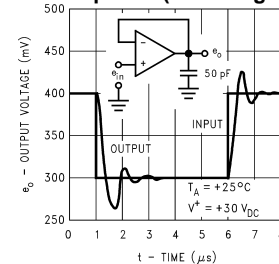
Voltage Gain



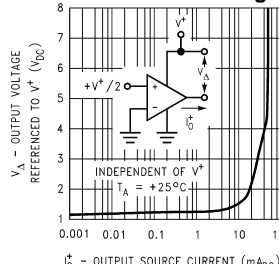
Common Mode Rejection Ratio



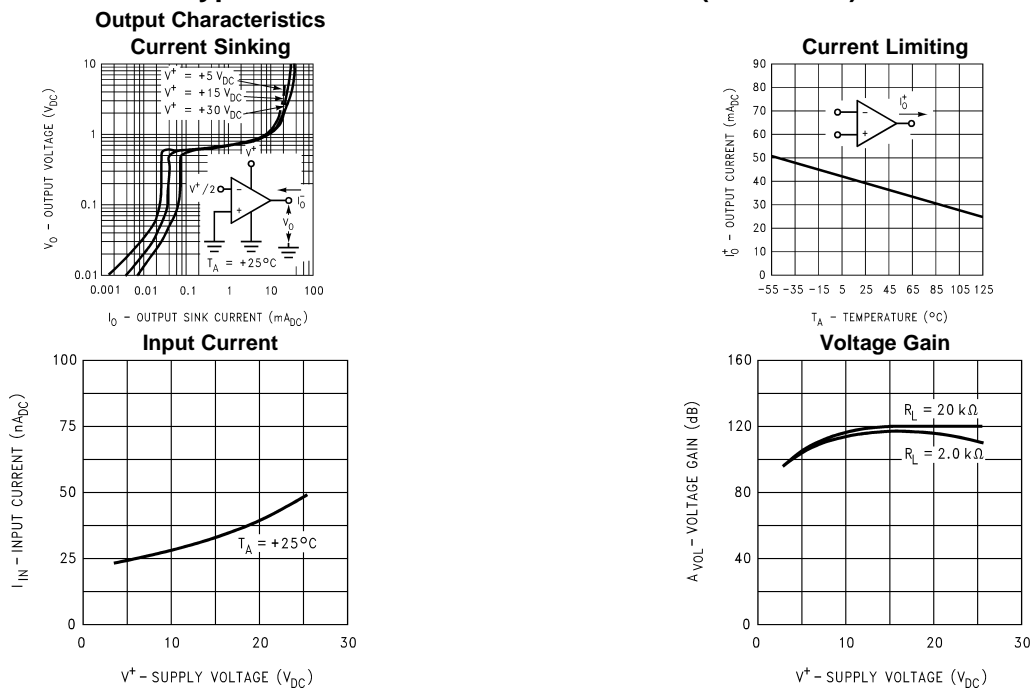
Voltage Follower Pulse Response (Small Signal)



Output Characteristics Current Sourcing



Typical Performance Characteristics (continued)



Application Hints

The LM2902EP is an op amp which operates with only a single power supply voltage, has true-differential inputs, and remains in the linear mode with an input common-mode voltage of 0 V_{DC}. This amplifier operates over a wide range of power supply voltages with little change in performance characteristics. At 25°C amplifier operation is possible down to a minimum supply voltage of 2.3 V_{DC}.

The pinouts of the package have been designed to simplify PC board layouts. Inverting inputs are adjacent to outputs for all of the amplifiers and the outputs have also been placed at the corners of the package (pins 1, 7, 8, and 14).

Precautions should be taken to insure that the power supply for the integrated circuit never becomes reversed in polarity or that the unit is not inadvertently installed backwards in a test socket as an unlimited current surge through the resulting forward diode within the IC could cause fusing of the internal conductors and result in a destroyed unit.

Large differential input voltages can be easily accommodated and, as input differential voltage protection diodes are not needed, no large input currents result from large differential input voltages. The differential input voltage may be larger than V⁺ without damaging the device. Protection should be provided to prevent the input voltages from going negative more than -0.3 V_{DC} (at 25°C). An input clamp diode with a resistor to the IC input terminal can be used.

To reduce the power supply drain, the amplifier has a class A output stage for small signal levels which converts to class B in a large signal mode. This allows the amplifier to both source and sink large output currents. Therefore both NPN and PNP external current boost transistors can be used to extend the power capability of the basic amplifier. The output voltage needs to raise approximately 1 diode drop above ground to bias the on-chip vertical PNP transistor for output current sinking applications.

For ac applications, where the load is capacitively coupled to the output of the amplifier, a resistor should be used, from the output of the amplifier to ground to increase the class A bias current and prevent crossover distortion.

Where the load is directly coupled, as in dc applications, there is no crossover distortion.

Capacitive loads which are applied directly to the output of the amplifier reduce the loop stability margin. Values of 50 pF can be accommodated using the worst-case non-inverting unity gain connection. Large closed loop gains or resistive isolation should be used if larger load capacitance must be driven by the amplifier.

The bias network of the LM2902EP establishes a drain current which is independent of the magnitude of the power supply voltage over the range of from $3 V_{DC}$ to $26 V_{DC}$.

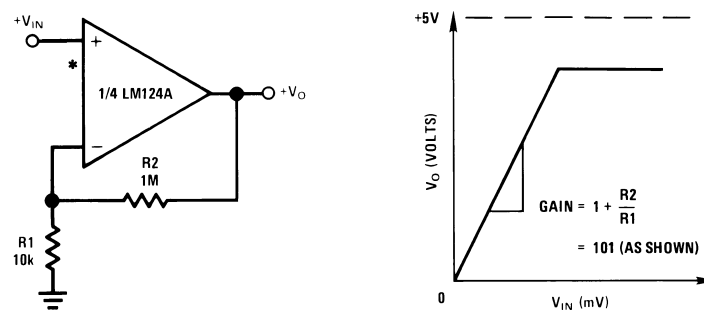
Output short circuits either to ground or to the positive power supply should be of short time duration. Units can be destroyed, not as a result of the short circuit current causing metal fusing, but rather due to the large increase in IC chip dissipation which will cause eventual failure due to excessive junction temperatures. Putting direct short-circuits on more than one amplifier at a time will increase the total IC power dissipation to destructive levels, if not properly protected with external dissipation limiting resistors in series with the output leads of the amplifiers. The larger value of output source current which is available at 25°C provides a larger output current capability at elevated temperatures (see typical performance characteristics) than a standard IC op amp.

The circuits presented in the section on typical applications emphasize operation on only a single power supply voltage. If complementary power supplies are available, all of the standard op amp circuits can be used. In general, introducing a pseudo-ground (a bias voltage reference of $V^+/2$) will allow operation above and below this value in single power supply systems. Many application circuits are shown which take advantage of the wide input common-mode voltage range which includes ground. In most cases, input biasing is not required and input voltages which range to ground can easily be accommodated.

Typical Single-Supply Applications

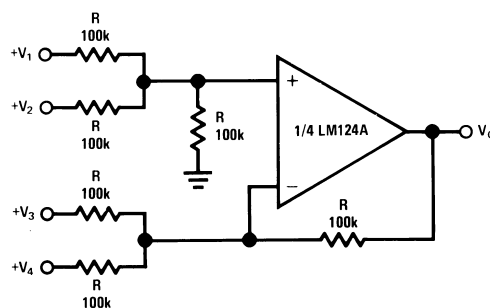
($V^+ = 5.0 V_{DC}$) ⁽¹⁾

Figure 2. Non-Inverting DC Gain ($0\text{V Input} = 0\text{V Output}$)



*R not needed due to temperature independent I_{IN}

Figure 3. DC Summing Amplifier
($V_{IN}'S \geq 0 V_{DC}$ and $V_O \geq V_{DC}$)

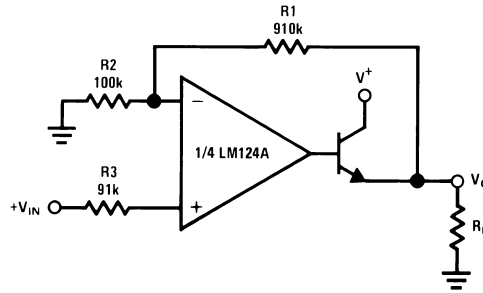


Where: $V_O = V_1 + V_2 - V_3 - V_4$
($V_1 + V_2 \geq (V_3 + V_4)$ to keep $V_O > 0 V_{DC}$)

(1) The LM124 within this data sheet's graphics is referenced because of it's a similarity to the LM2902, however is not offered in this data sheet.

($V^+ = 5.0 V_{DC}$) ⁽¹⁾

Figure 4. Power Amplifier



$V_o = 0 V_{DC}$ for $V_{IN} = 0 V_{DC}$
 $A_V = 10$

Figure 5. LED Driver

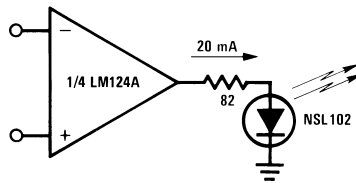
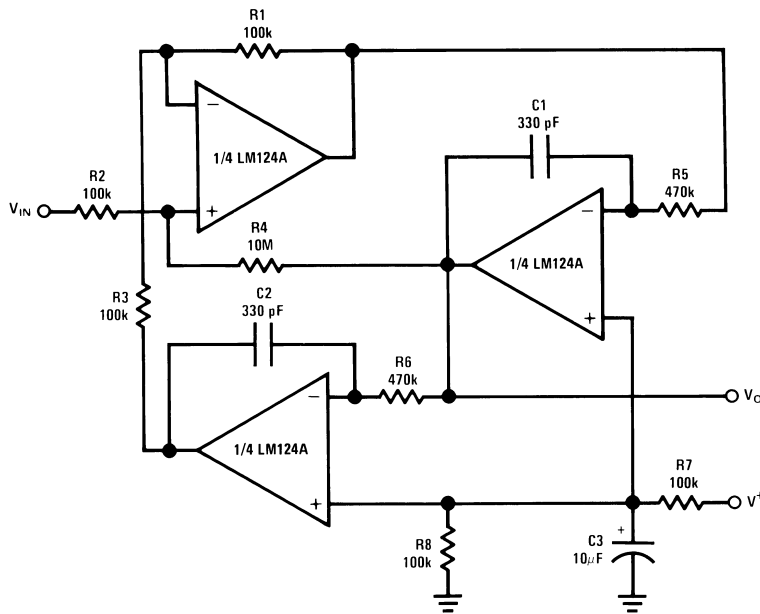
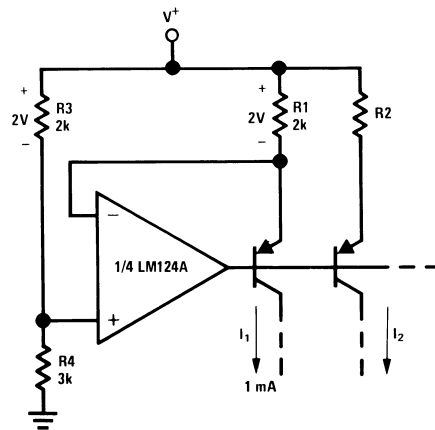


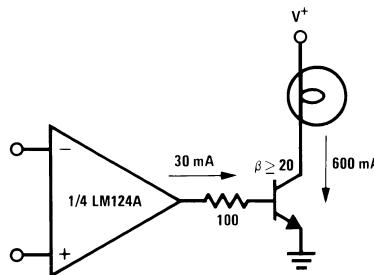
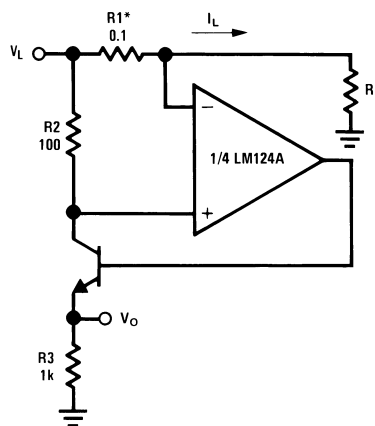
Figure 6. "BI-QUAD" RC Active Bandpass Filter



$f_o = 1 \text{ kHz}$
 $Q = 50$
 $A_V = 100$ (40 dB)

$(V^+ = 5.0 V_{DC})^{(1)}$
Figure 7. Fixed Current Sources


$$I_2 = \left(\frac{R1}{R2} \right) I_1$$

Figure 8. Lamp Driver

Figure 9. Current Monitor


$$V_O = \frac{1V(I_L)}{1A}$$

$$V_L \leq V^+ - 2V$$

*(Increase R1 for I_L small)

($V^+ = 5.0 V_{DC}$) ⁽¹⁾

Figure 10. Driving TTL

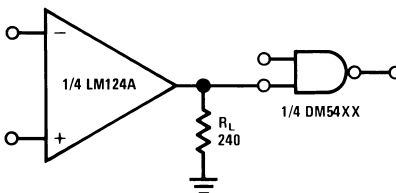


Figure 11. Voltage Follower

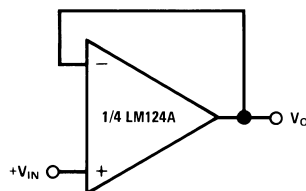


Figure 12. Pulse Generator

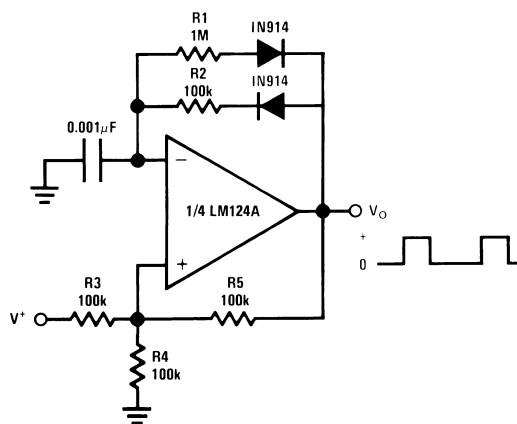
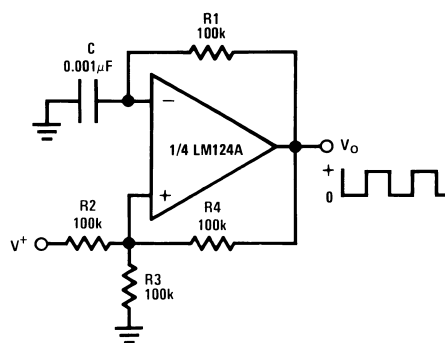


Figure 13. Squarewave Oscillator



($V^+ = 5.0 V_{DC}$) ⁽¹⁾

Figure 14. Pulse Generator

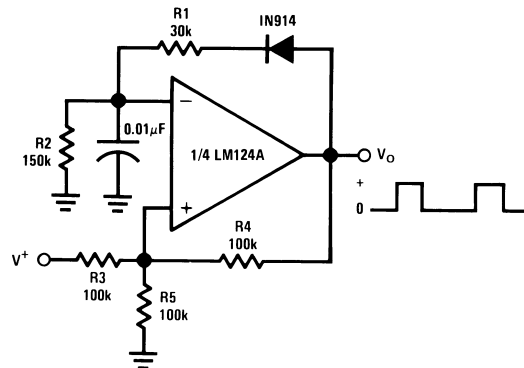
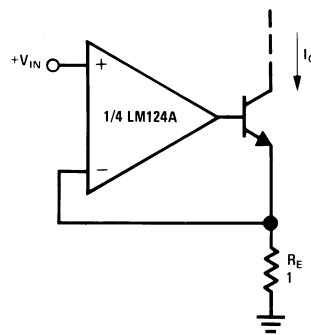
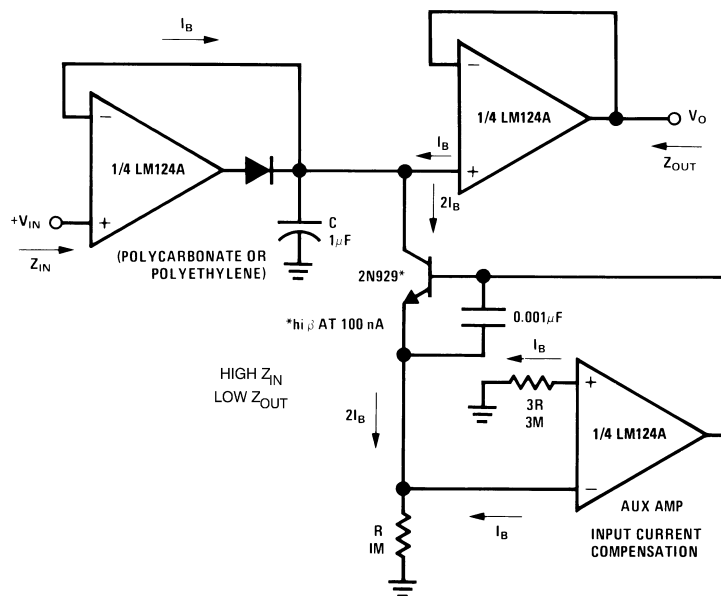


Figure 15. High Compliance Current Sink



$I_o = 1 \text{ amp/volt } V_{IN}$
 (Increase R_E for I_o small)

Figure 16. Low Drift Peak Detector



($V^+ = 5.0 V_{DC}$) ⁽¹⁾

Figure 17. Comparator with Hysteresis

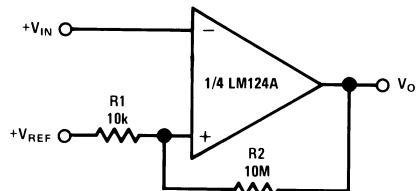
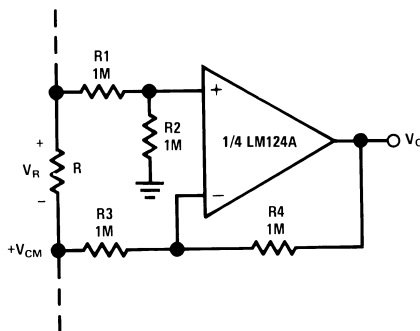
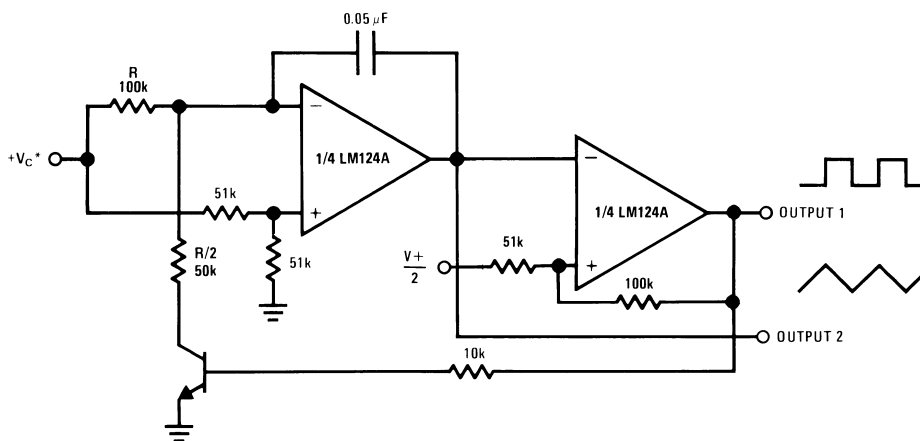


Figure 18. Ground Referencing a Differential Input Signal



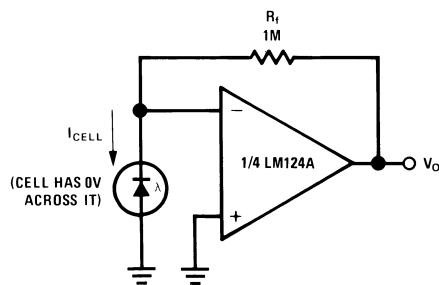
$V_O = V_R$

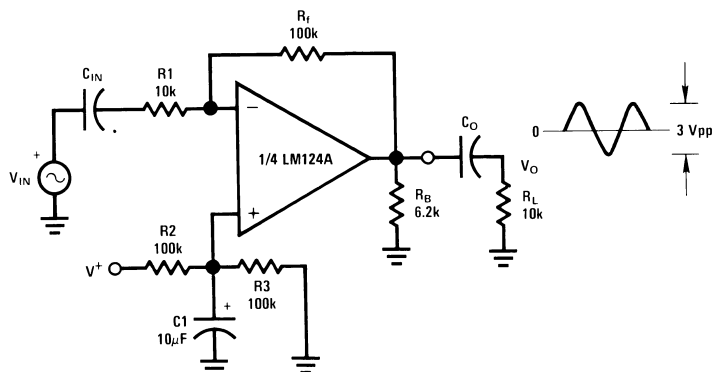
Figure 19. Voltage Controlled Oscillator Circuit



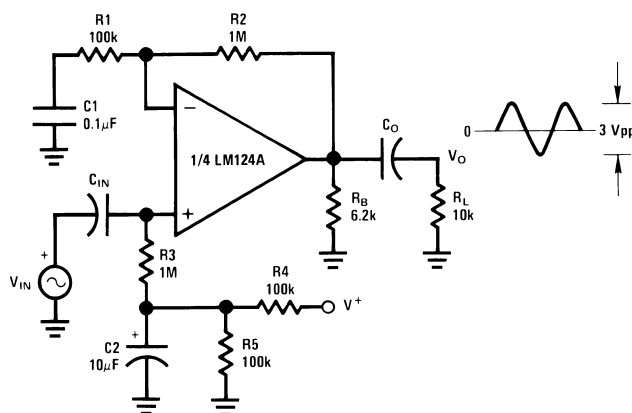
*Wide control voltage range: $0 V_{DC} \leq V_C \leq 2 (V^+ - 1.5 V_{DC})$

Figure 20. Photo Voltaic-Cell Amplifier



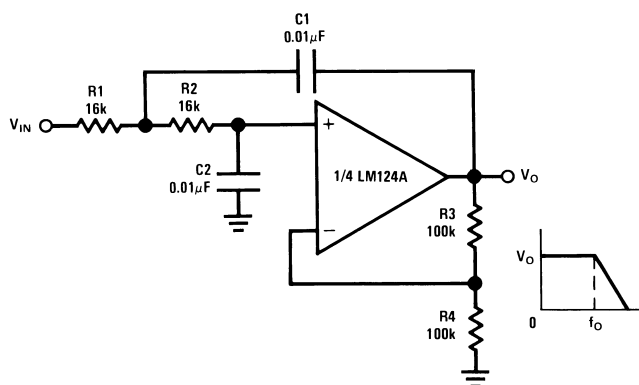
$(V^+ = 5.0 V_{DC})^{(1)}$ **Figure 21. AC Coupled Inverting Amplifier**

$$A_V = \frac{R_f}{R_1} \text{ (As shown, } A_V = 10\text{)}$$

Figure 22. AC Coupled Non-Inverting Amplifier

$$A_V = 1 + \frac{R_2}{R_1}$$

$$A_V = 11 \text{ (As shown)}$$

Figure 23. DC Coupled Low-Pass RC Active Filter

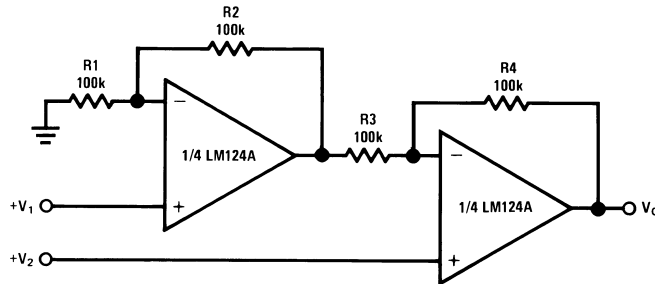
$$f_0 = 1 \text{ kHz}$$

$$Q = 1$$

$$A_V = 2$$

($V^+ = 5.0 V_{DC}$) ⁽¹⁾

Figure 24. High Input Z, DC Differential Amplifier

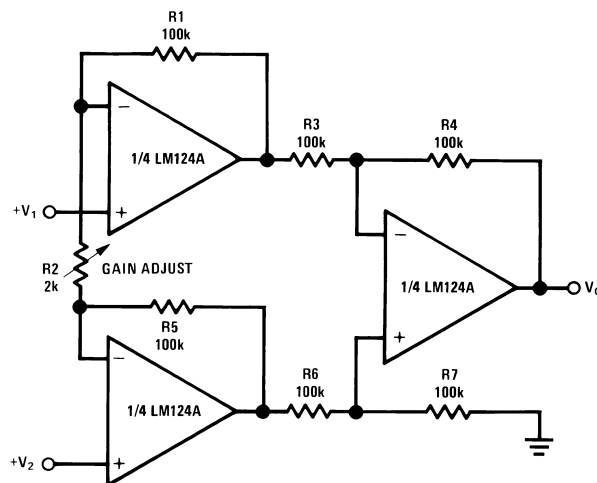


For $\frac{R1}{R2} = \frac{R4}{R3}$ (CMRR depends on this resistor ratio match)

$$V_O = 1 + \frac{R4}{R3}(V_2 - V_1)$$

As shown: $V_O = 2(V_2 - V_1)$

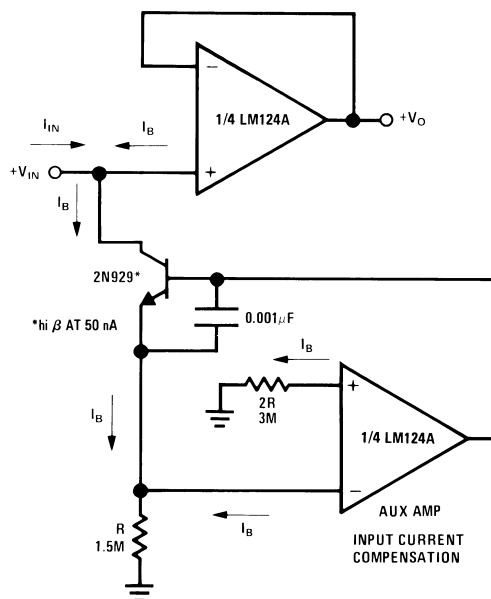
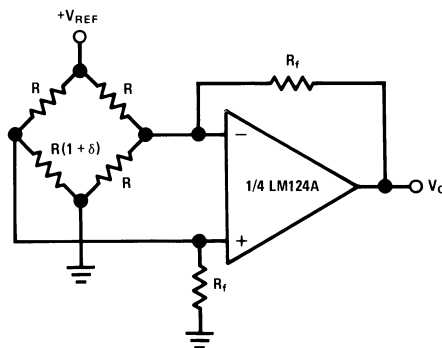
Figure 25. High Input Z Adjustable-Gain DC Instrumentation Amplifier



If $R1 = R5$ & $R3 = R4 = R6 = R7$ (CMRR depends on match)

$$V_O = 1 + \frac{2R1}{R2}(V_2 - V_1)$$

As shown $V_O = 101(V_2 - V_1)$

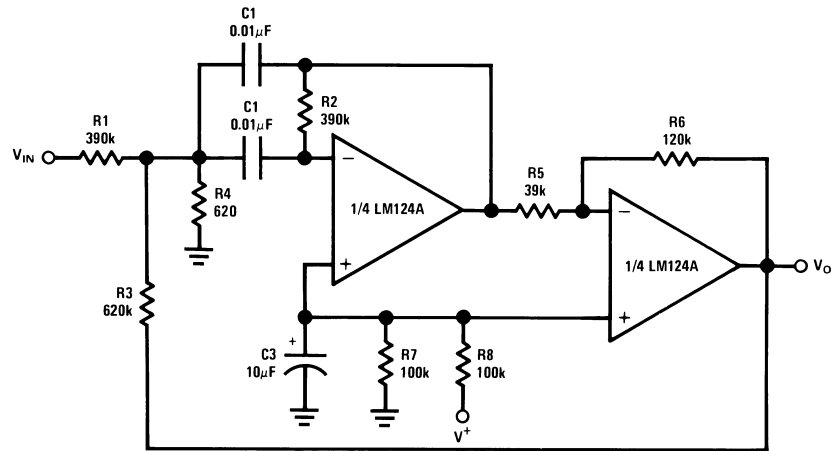
$(V^+ = 5.0 V_{DC})^{(1)}$
Figure 26. Using Symmetrical Amplifiers to Reduce Input Current (General Concept)

Figure 27. Bridge Current Amplifier


For $\delta \ll 1$ and $R_f \gg R$

$$V_O \approx V_{REF} \left(\frac{\delta}{2} \right) \frac{R_f}{R}$$

($V^+ = 5.0 V_{DC}$) ⁽¹⁾

Figure 28. Bandpass Active Filter



$f_0 = 1 \text{ kHz}$
 $Q = 25$

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