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规格书编号 File No.	PN-RD-0013A
作成日　Issue Date:	2012 年(Y) 07 月(M) 21 日(D)
改订日 Revision Date:	

《新规》　·　变更》
New　　　Revision

产 品 规 格 书
Product Specification

产品名
Product　　TFT-LCD Module

机种名
Model　　LM315TA6A

【接收印栏】

※ 本基准书由封面、附件等全 21 页构成。
　 如果对该规格书有异议，请在下订单前提出。

※ This Product Specification have 21 pages including the coversheet and Appendices. Please negotiate the objection point before purchase order.

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-CONTENTS -

REVISION HISTORY

MODEL NO: LM315TA6A

DATE	NO.	REVISED No.	PAGE	SUMMARY	NOTE
2012/07/21	PN-RD-0013A	V 1.0	21	1st issue	Final Version

1. GENERAL DESCRIPTION

1.1 OVERVIEW

This module is color active matrix LCD module incorporating amorphous silicon TFT(Thin Film Transistor) LCD panel. It is composed of a color TFT-LCD panel, driver ICs, LED Backlight… etc. Graphics and texts can be displayed on a 1366×RGB×768 dots panel with about 16,777,216 colors(R/G/B 8bit in each color) by using LVDS(Low Voltage Differential Signaling) to interface, +12V of DC supply voltage.

In order to improve the response time of LCD, this module applies the Over Shoot driving (O/S driving) technology for the control circuit. In the O/S driving technology, signals are being applied to the Liquid Crystal according to a pre-fixed process as an image signal of the present frame when a difference is found between image signal of the previous frame and that of the current frame after comparing them.

By using the captioned process, the image signals of this LCD module are being set so that image response can be completed within one frame, as a result, image blur can be improved and clear image performance can be realized.

1.2 CHARACTERISTICS

CHARACTERISTICS ITEMS	SPECIFICATIONS
Screen Diagonal　[in]	31.5"
Pixels　[lines]	1366×768
Active Area　[mm]	697.685 (H) x 392.256 (V)
Pixel Pitch　[mm]	0.51075(H) x 0.51075 (V)
Pixel Arrangement	RGB vertical stripe
Display Colors	16.7M
Display Mode	Normally Black
Surface treatment（Without the protection film）	Anti-glare,2H

1.3 MECHANICAL SPECIFICATIONS

	Item	Min.	Typ.	Max.	Unit	Remark
Module Size	Horizontal (H)	759	760	761	mm	[Note 1]
	Vertical (V)	449	450	451	mm	[Note 1]
	Depth (D)	40.1	41.1	42.1	mm	[Note 1]
Weight			5300	5500	g	

[Note 1] Please refer to the attached drawings for more information of front and back outline dimensions and the dimension of bosses are not included.

2. ABSOLUTE MAXIMUM RATINGS

## 2.1ABSOLUTE RATINGS OF ENVIRONMENT

Item	Symbol	Value		Unit	Remark
		Min.	Max.		
Storage Temperature	T_{ST}	-20	+60	ºC	[Note 1,3]
Operating Ambient Temperature	T_{OP}	0	50	ºC	[Note 1,2,3]

Storage Condition: With shipping package.

Shelf life: one year

[Note 1] Temperature and relative humidity range is shown in the figure below.

(a) 80 %RH Max. (Ta ≦ 40 ºC).

(b) Wet-bulb temperature should be 39 ºC Max. (Ta > 40 ºC).

(c) No condensation.
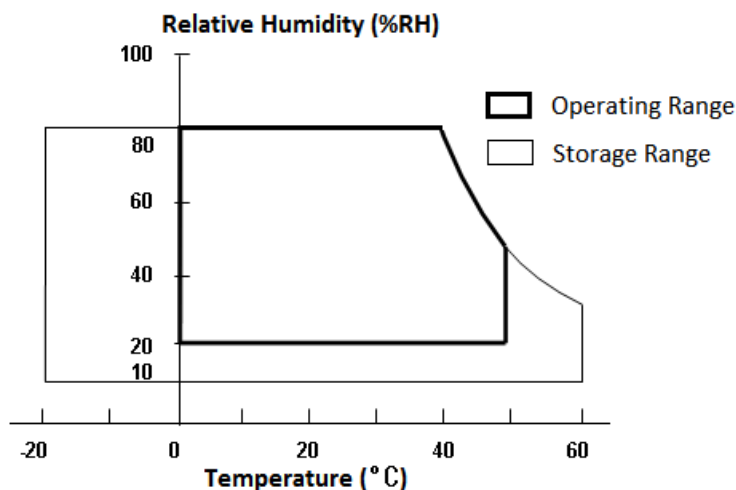
[Note 2] The maximum operating temperature is based on the test condition that the surface temperature of display area is less than or equal to 50 ºC with LCD module alone in a temperature controlled chamber. Thermal management should be considered in your product design to prevent the surface temperature of display area from being over 60 ºC. The range of operating temperature may degrade in case of improper thermal management in your product design.

[Note 3] The rating of environment is base on LCD module. Except LCD module, the customer has to consider the ability of other parts of LCD set and LCD set assembly process.

3. ELECTRICAL CHARACTERISTICS
3.1 ABSOLUTE MAXIMUM RATING

Parameter	Symbol	Condition	Ratings	Unit	Remark
+12V supply voltage	Vcc	Ta=25°C	0～+14	V	CN1 Pin1~4
Control voltage	VSELLVDS	Ta=25°C	-0.3～3.6	V	CN1 Pin9
Storage temperature	Tstg	-	-20～+60	°C	
Operation temperature	Topa	-	0～+50	°C	

3.2 CONTROL CIRCUIT DRIVING

Parameter		Symbol	Min.	Typ.	Max.	Unit	Remark
+12V supply voltage	Supply voltage	V_{CC}	+10.8	+12.0	+13.2	V	[Note 1]
	Current dissipation	I_{CC}	-	350	600	mA	[Note 2]
		I_{RUSH}	-	-	5	A	[Note 5]
Permissible input ripple voltage		V_{RP}	-	-	100	mV_{P-P}	V_{CC}=+12.0V
Differential input threshold voltage	High	V_{TH}		-	100	mV	[Note 4]
	Low	V_{TL}	-100	-	-	mV	
Input Low voltage		V_{IL}	0		0.7	V	[Note 3]
Input High voltage		V_{IH}	2.6	-	3.3	V	
Input leak current (Low)		I_{IL}	-	-	400	µA	V_I=0V　　[Note 3]
Input leak current (High)		I_{IH}	-	-	100	µA	V_I =3.3V [Note 3]
Terminal resistor		RT	-	100	-	Ω	Differential input
Input Differential voltage		|VID|	200	400	600	mV	[Note 4]
Differential input common mode voltage		VCM	|VID|/2	1.2	2.4-|VID|/2	V	[Note 4]

VCM: Common mode voltage of LVDS driver.

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[Note 1]
Input voltage sequences

$50us \leqq t1 \leqq 20ms$

$20ms < t2\text{-}1$

$20ms < t2\text{-}2$

$0 < t3\text{-}1 \leqq 1s$

$0 < t3\text{-}2 \leqq 1s$

$1s \quad t4$

$300ms \quad t5\text{-}1$

$300ms \leqq t5\text{-}2$

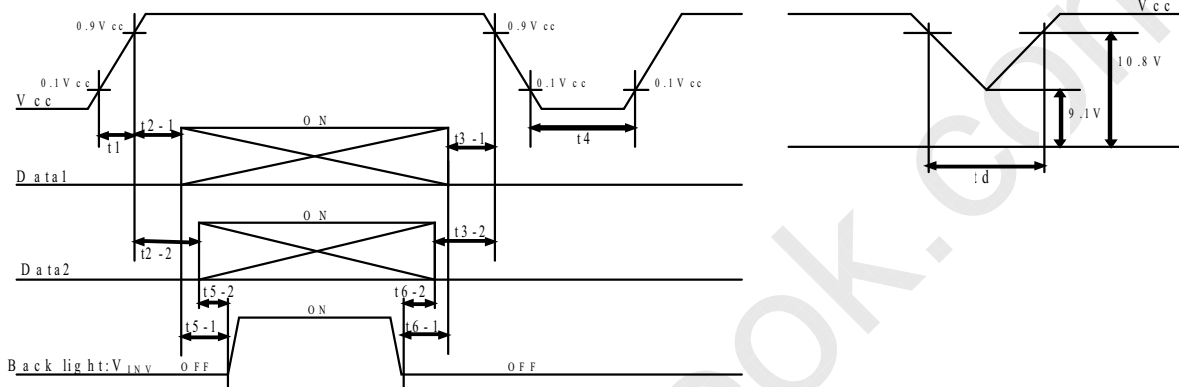$0 < t6\text{-}1$

$0 < t6\text{-}2$

Dip conditions for supply voltage

a) $9.1V \leqq V_{CC} < 10.8V$

   $td \leqq 10ms$

b) $V_{CC} < 9.1V$

   Dip conditions for supply voltage is
   based on input voltage sequence.
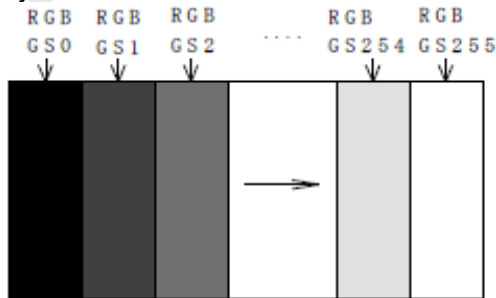

※ Data1: CLKIN±,RIN0±,RIN1±, RIN2±, RIN3±

※ Data2: SELLVDS

※ About the relation between data input and back light lighting, please base on the above-mentioned input sequence.

※ When back light is switched on before panel operation or after a panel operation stop, it may not display normally. But this phenomenon is not based on change of an incoming signal, and does not give damage to a liquid crystal display.

[Note 2] Typical current situation: 256 gray-bar pattern (VCC = +12.0V).
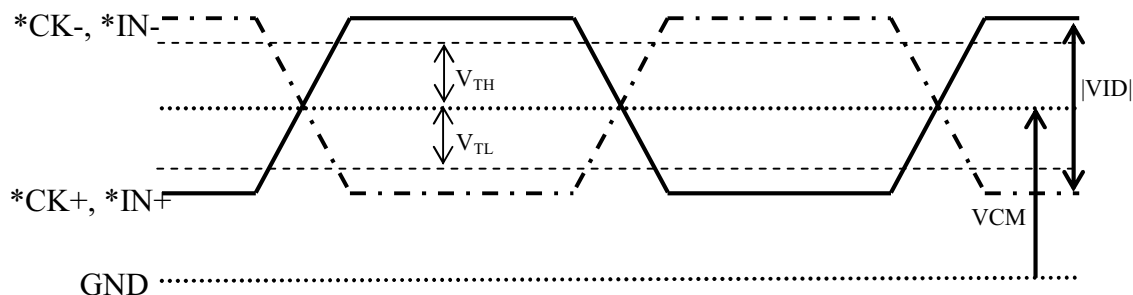   The explanation of RGB gray scale is seen in section 8.

Typical current situation

[Note 3] SELLVDS

[Note 4] CLKIN+/CLKIN-, RIN0+/RIN0-, RIN1+/RIN1-, RIN2+/RIN2-, RIN3+/RIN3-

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3.3 LED LIGHTBAR SPECIFICATION FOR BACKLIGHT

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Input Voltage	V_{pin}	22.8	24	25.2	VDC	Duty 100% [Note 1]
Input Current	I_{pin}	-	660*2	-	mADC	Duty 100% Per Lightbar [Note 1]
LED Life Time	LT	30,000	-	-	Hrs	[Note 2]
Power Consumption	PBL	30.1	31.7	33.3	W	
Dimming Duty Ratio	-	20	-	100	%	

[Note 1] Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

[Note 2] The life time of LED is defined as the time when it continues to operate under the condition at Ta = 25±2°C and Ipin = 660 mA(Per EA) *2 until the brightness becomes　50% of its original value.

4. INTERFACE PIN CONNECTION
4.1 TFT LCD MODULE

CN1 (Interface signals and +12V DC power supply) shown on the next table.

Using connector: IS100-L30B-C23 (UJU)

Matching connector: FI-X30C2L or Equivalent (Japan Aviation Electronics Ind., Ltd)

Matching LVDS transmitter: THC63LVDM83R (THine) or equivalent device

Pin No.	Symbol		Remark
1	VCC	+12V Power Supply	[Note 3]
2	VCC	+12V Power Supply	
3	VCC	+12V Power Supply	
4	VCC	+12V Power Supply	
5	GND	Ground	
6	GND	Ground	
7	GND	Ground	
8	GND	Ground	
9	SELLVDS	Select LVDS data order[Note1]	Default: pull down (L:GND) [Note 2]
10	Reserved	Not Available	
11	GND	Ground	
12	RIN0-	Negative(-) LVDS differential data input	LVDS
13	RIN0+	Positive(+) LVDS differential data input	LVDS
14	GND	Ground	
15	RIN1-	Negative(-) LVDS differential data input	LVDS
16	RIN1+	Positive(+) LVDS differential data input	LVDS
17	GND	Ground	
18	RIN2-	Negative(-) LVDS differential data input	LVDS

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19	RIN2+	Positive(+) LVDS differential data input	LVDS
20	GND	Ground	
21	CLKIN-	Clock Signal(-)	LVDS
22	CLKIN+	Clock Signal(+)	LVDS
23	GND	Ground	
24	RIN3-	Negative(-) LVDS differential data input	LVDS
25	RIN3+	Positive(+) LVDS differential data input	LVDS
26	GND	Ground	
27	Reserved	Not Available	
28	Reserved	Not Available	
29	Reserved	Not Available	
30	Reserved	Not Available	

[Note 1] SELLVDS

| Transmitter		SELLVDS	
Pin No	Data	VESA	JEITA
		= L(GND) or Open	=H(3.3V)
51	TA0	R0(LSB)	R2
52	TA1	R1	R3
54	TA2	R2	R4
55	TA3	R3	R5
56	TA4	R4	R6
3	TA5	R5	R7(MSB)
4	TA6	G0(LSB)	G2
6	TB0	G1	G3
7	TB1	G2	G4
11	TB2	G3	G5
12	TB3	G4	G6
14	TB4	G5	G7(MSB)
15	TB5	B0(LSB)	B2
19	TB6	B1	B3
20	TC0	B2	B4
22	TC1	B3	B5
23	TC2	B4	B6
24	TC3	B5	B7(MSB)
27	TC4	NA	NA
28	TC5	NA	NA
30	TC6	DE(*)	DE(*)
50	TD0	R6	R0(LSB)
2	TD1	R7(MSB)	R1
8	TD2	G6	G0(LSB)
10	TD3	G7(MSB)	G1
16	TD4	B6	B0(LSB)
18	TD5	B7(MSB)	B1
25	TD6	NA	NA

NA: Not Available

(*)The display position is prescribed by the rise of DE (Display Enable) signal, please do not fix DE signal during operation at "High."

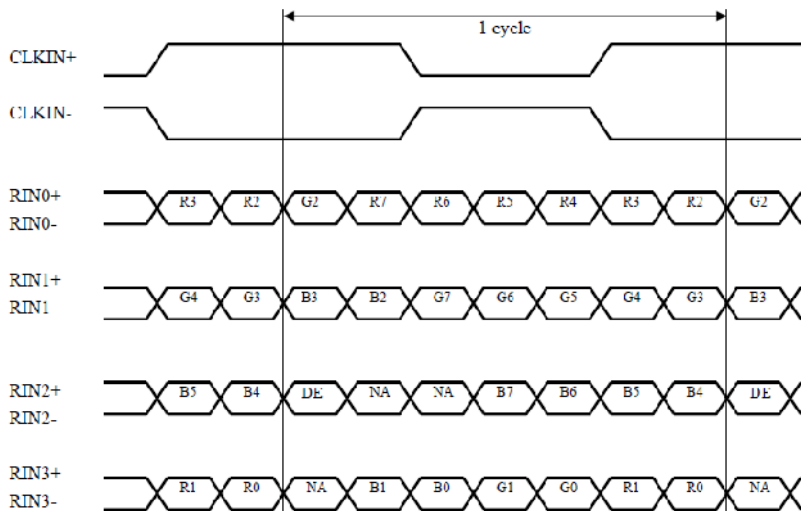[Note 2] The equivalent circuit figure of the terminal
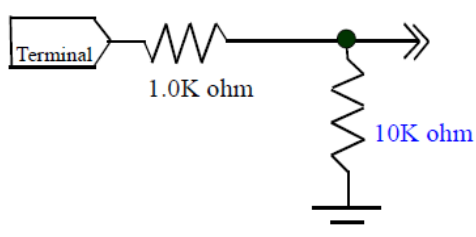
[Note 3] The location of Pin 1 is shown in Fig 12-1.
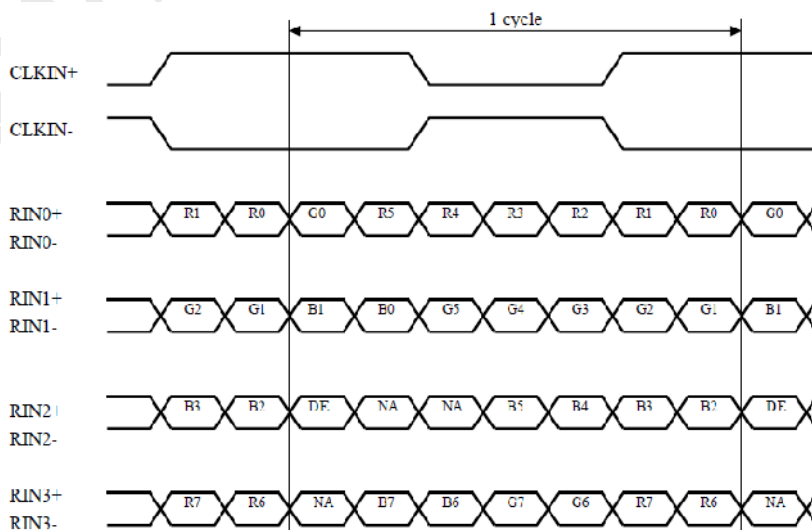
4.2 BLOCK DIAGRAM (TFT LCD MODULE)
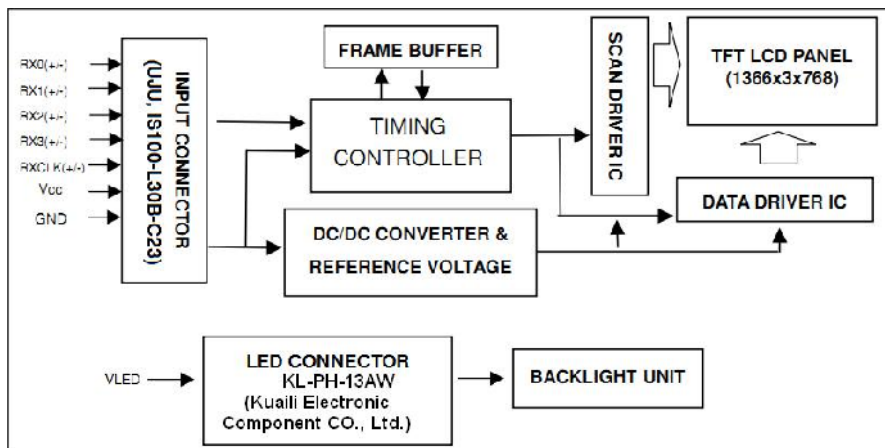
4.3 LVDS INTERFACE
SELLVDS(JEITA)= High (3.3V)

SELLVDS(VESA)= LOW (GND) or OPEN

LVDS data map

DE: Display Enable

NA: Not Available (Fixed Low)

4.4 COLOR DATA INPUT ASSIGNMENT

| Colors & Gray scale		Gray Scale	R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7
Basic Color	Black	—	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	—	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	—	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan	—	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red	—	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	—	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	—	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	—	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	↓				↓								↓								↓				
	⇩	↓				↓								↓								↓				
	Brighter	GS253	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇩	GS254	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Green	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	GS1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	↓				↓								↓								↓				
	⇩	↓				↓								↓								↓				
	Brighter	GS253	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	⇩	GS254	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Green	GS255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale of Blue	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	⇧	↓				↓								↓								↓				
	⇩	↓				↓								↓								↓				
	Brighter	GS253	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1
	⇩	GS254	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1
	Blue	GS255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

0: Low level voltage,

1: High level voltage.

Each basic color can be displayed in 256 gray scales from 8 bit data signals. According to the combination of total 24 bit data signals, the 16,7M colors display can be achieved on the screen.

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4.5 BACKLIGHT INTERFACE CONNECTION

LED lightbar housing type：KL-PH-13AW, manufactured by Kuaili Electronic Component Co., Ltd.. The mating header is KL-PH-13Y or equivalent.

Pin No.	Symbol	Description
1	BL ON-OFF	+3.3V
2	PWM-	+3.3V
3	N/C	N/C
4	N/C	N/C
5	N/C	N/C
6	N/C	N/C
7	LED-	GND
8	LED-	GND
9	N/C	N/C
10	N/C	N/C
11	N/C	N/C
12	LED+	+24V
13	LED+	+24V
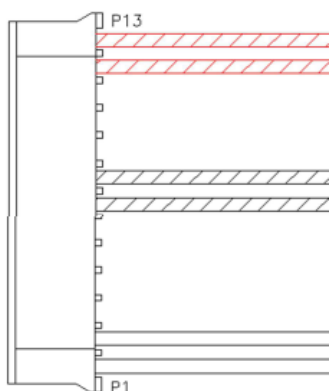
4.6 BACKLIGHT UNIT

The backlight unit contains two lightbars module. The next figure is electrical circuit of single one.

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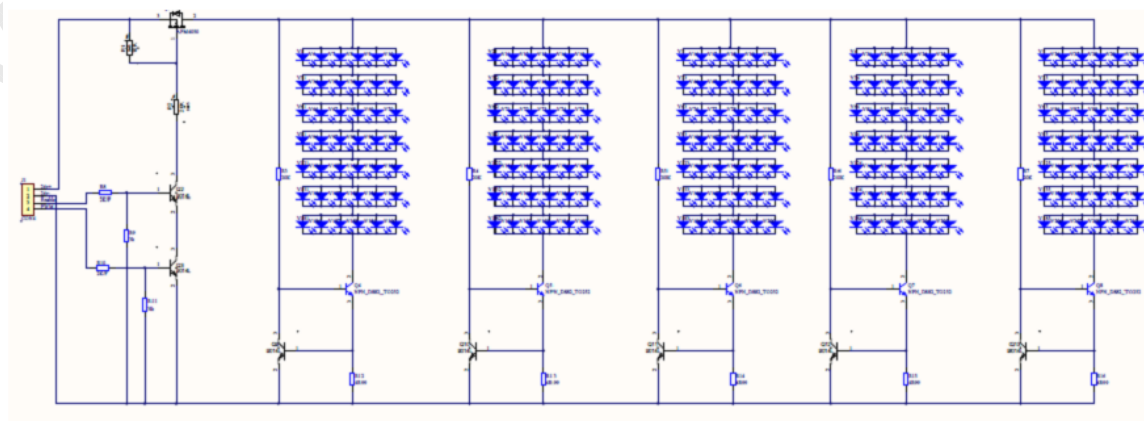
5. INTERFACE TIMING

5.1 INPUT SIGNAL TIMING SPECIFICATIONS

The input signal timing specifications are shown as the following table and timing diagram.

Parameter		Symbol	Min	Typ.		Max.	Unit
				NTSC	PAL		
Clock	Frequency	1/Tc	72	82	82	85	MHz
Data enable signal	Horizontal period	TH	1540	1696	1696	1940	clock
			17.15	20.68	20.68	21.42	μs
	Horizontal period (High)	THd	1366	1366	1366	1366	clock
	Horizontal Blanking period	TH-THd	174	330	330	574	clock
	Vertical period	TV	778	806	967	972	line
			47.70	60	50	62.35	Hz
	Vertical period (High)	TVd	768	768	768	768	line
	Vertical Blanking period	TV-TVd	10	38	199	204	line

*Timing diagrams of input signal are shown below
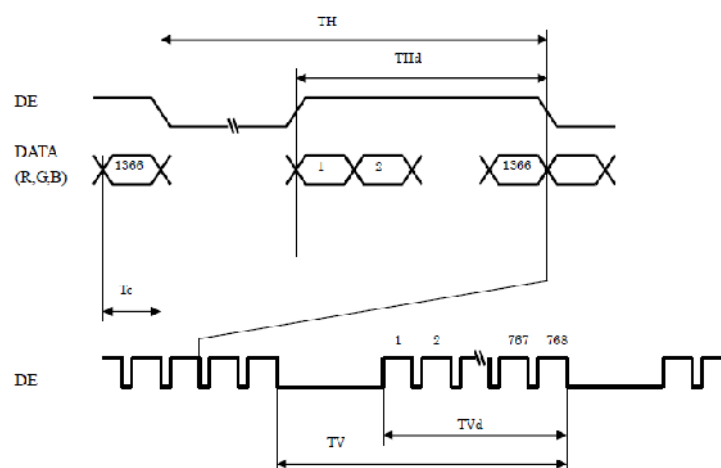
5.2 LVDS SIGNAL CHARACTERISTICS
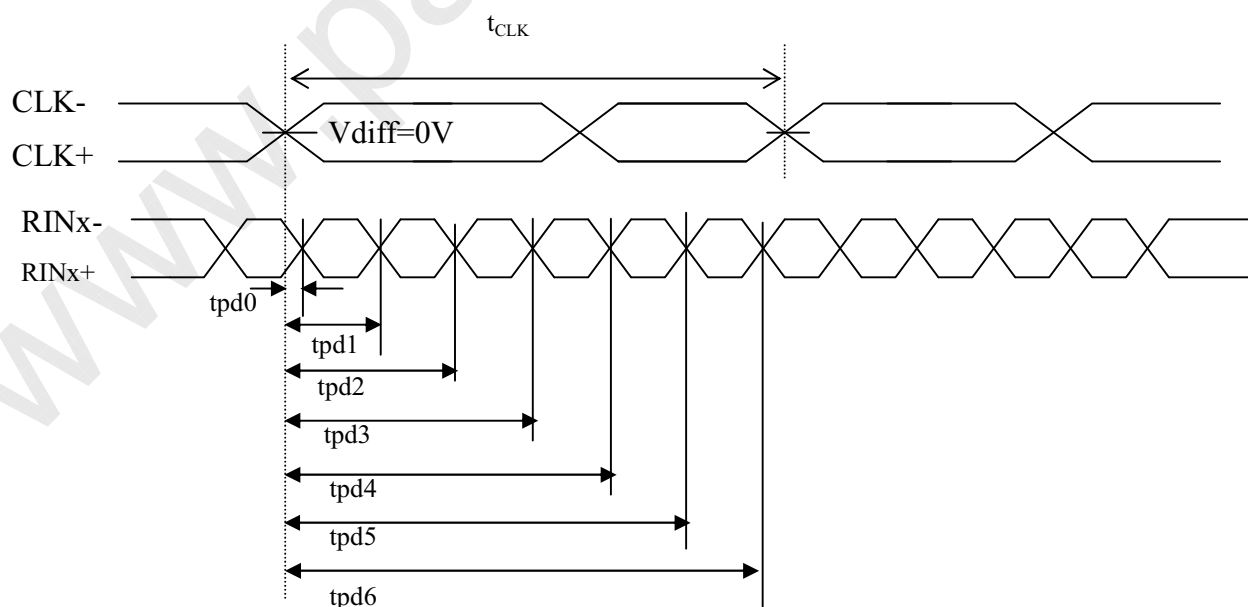
LVDS signal characteristics

	The item	Symbol	min.	typ.	Max.	unit
Data position	Delay time, CLK rising edge to serial bit position 0	tpd0	-0.40	0	0.40	ns
	Delay time, CLK rising edge to serial bit position 1	tpd1	typ-0.40	$1 * t_{CLK} /7$	typ+0.40	
	Delay time, CLK rising edge to serial bit position 2	tpd2	typ-0.40	$2 * t_{CLK} /7$	typ+0.40	
	Delay time, CLK rising edge to serial bit position 3	tpd3	typ-0.40	$3 * t_{CLK} /7$	typ+0.40	
	Delay time, CLK rising edge to serial bit position 4	tpd4	typ-0.40	$4 * t_{CLK} /7$	typ+0.40	
	Delay time, CLK rising edge to serial bit position 5	tpd5	typ-0.40	$5 * t_{CLK} /7$	typ+0.40	
	Delay time, CLK rising edge to serial bit position 6	tpd6	typ-0.40	$6 * t_{CLK} /7$	typ+0.40	

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6. OPTICAL CHARACTERISTICS
6.1 OPTICAL SPECIFICATION

Ta=25°C

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Luminance	Central Luminance	Lwc	θ =0 deg.	280	320		cd/m²	[Note 1,4]
	Uniformity	Lw	θ =0 deg.	75			%	[Note 1,5]
Viewing angle range	Horizontal	θ 21 θ 22	CR≥10		88	-	Deg.	[Note 1,4]
	Vertical	θ 11 θ 12			88	-	Deg.	
Contrast ratio		CR			3000	-	-	[Note 2,4]
Response time		τDRV		-	7	-	ms	[Note 3,4]
Chromaticity of white	x		θ =0 deg.	Typ-0.03	0.269	Typ+0.03	-	[Note 4]
	y				0.270		-	
Chromaticity of red	x				0.634		-	
	y				0.352		-	
Chromaticity of green	x				0.309		-	
	y				0.628		-	
Chromaticity of blue	x				0.149		-	
	y				0.064		-	
Color Gamut		C.G.		-	72	-	%	

*The measurement shall be executed 30 minutes after lighting at rating.

*The optical characteristics are measured using the following equipment.
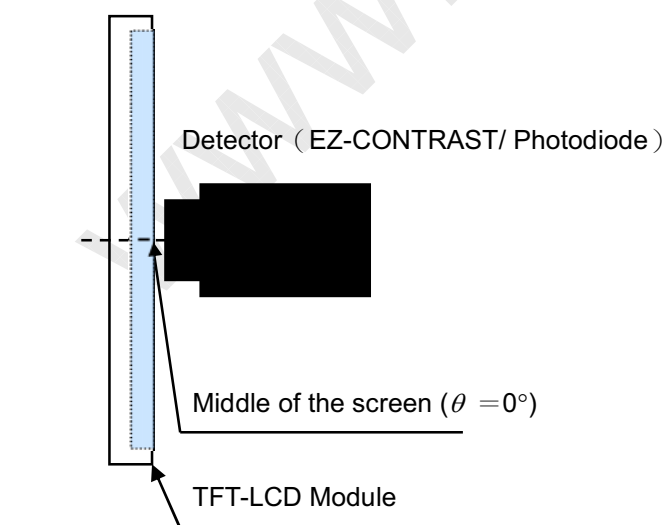
Measurement of viewing angle range, Response time.
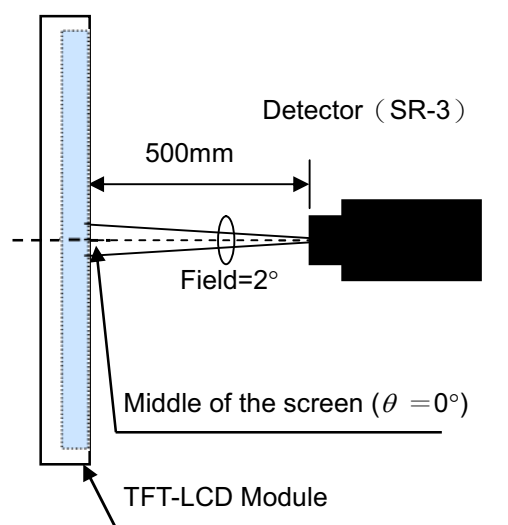
Measurement of Contrast, Luminance, Chromaticity.

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[Note 1] Definitions of viewing angle range:

Normal line

$\theta 22$ $\theta 11$ $\theta 21$

$\theta 12$

6 o'clock direction
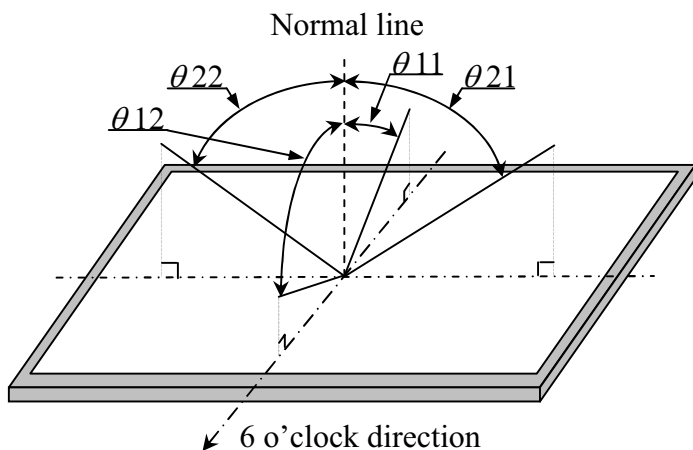
[Note 2] Definition of contrast ratio:

The contrast ratio is defined as the following.

$$\text{Contrast Ratio} = \frac{\text{Luminance (Brightness) with white screen}}{\text{Luminance (Brightness) with black screen}}$$

[Note 3] Definition of response time

The response time (τ_{DRV}) is defined as the following figure and shall be measured by switching the input signal for "any level of gray (0%, 25%, 50%, 75% and 100%) and "any level of gray (0%, 25%, 50%, 75% and 100%).

	0%	25%	50%	75%	100%
0%		τ_r:0%–25%	τ_r:0%–50%	τ_r:0%–75%	τ_r:0%–100%
25%	τ_d:25%–0%		τ_r:25%–50%	τ_r:25%–75%	τ_r:25%–100%
50%	τ_d:50%–0%	τ_d:50%–25%		τ_r:50%–75%	τ_r:50%–100%
75%	τ_d:75%–0%	τ_d:75%–25%	τ_d:75%–50%		τ_r:75%–100%
100%	τ_d:100%–0%	τ_d:100%–25%	τ_d:100%–50%	τ_d:100%–75%	

τ^*:x-y...response time from level of gray(x) to level of gray(y)

$\tau_{DRV} = \Sigma\ (\tau^*\text{:x-y})/20$

any level of gray (bright)   any level of gray (dark)   any level of gray (bright)

Photodetector Output (Relative Value)

100%
90%

10%
0%

τ_d:* τ_r:*

time
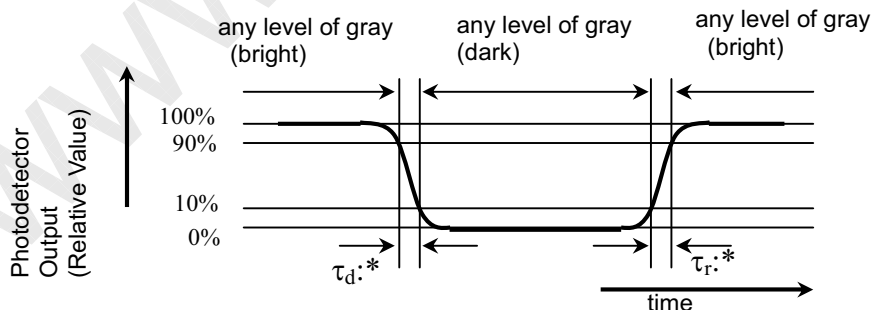
[Note 4] This shall be measured at center of the screen.

[Note 5] Definition of Luminance and Luminance uniformity：

Luminance：To measure at the center position "5" on the screen (NO.5).

Luminance uniformity：Lw (MAX) and Lw(MIN) are the maximum and minimum luminance value measure at the position "1~5" on the screen (NO.1~5) and the equation：

$\Delta Lw = Lw(MIN) / Lw(MAX) \times 100\%$

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Horizontal Line / Active Area diagram with measurement points 1-5
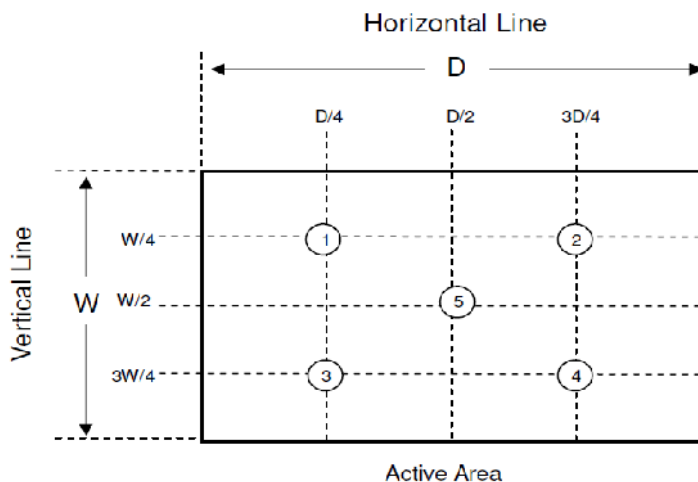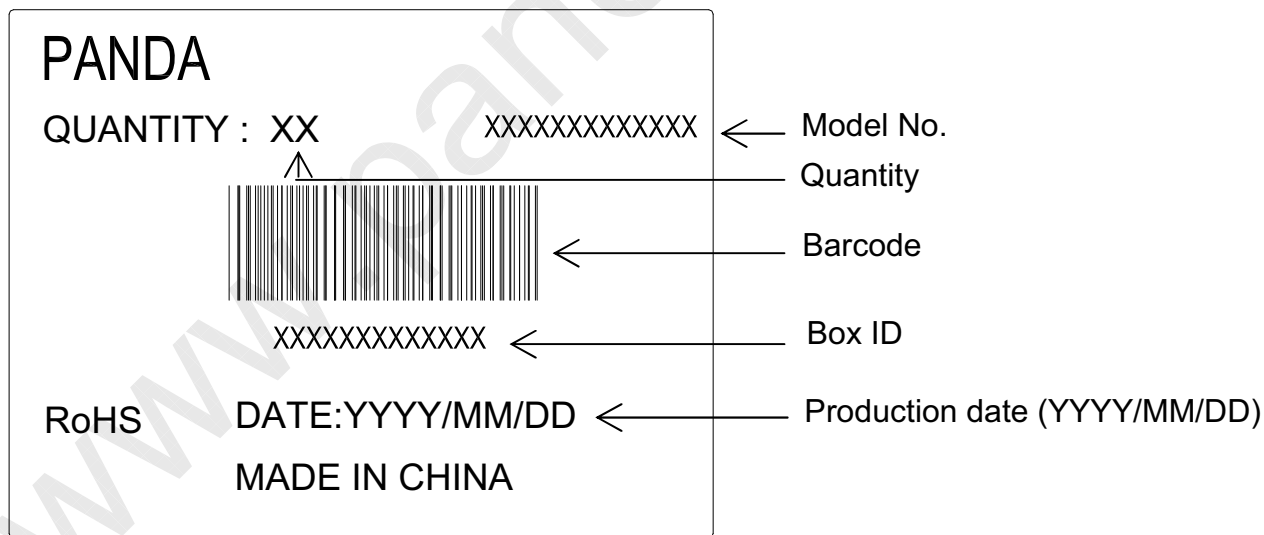
7. DEFINITION OF LABELS

7.1 MODULE LABEL

The label of displays, product model (LM315TA6A), and product label is stuck on the back side of the Module.

PANDA XXXXXXXXXXXX → Model No.
→ Barcode
RoHS
XXXXXXXXXXXXXX Made In China

→ Serial number

7.2 PACKING LABEL

PANDA
QUANTITY : XX   XXXXXXXXXXXX ← Model No.
← Quantity
← Barcode
XXXXXXXXXXXX ← Box ID
RoHS DATE:YYYY/MM/DD ← Production date (YYYY/MM/DD)
MADE IN CHINA

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8. PACKING
8.1 PACKING SPECIFICATIONS

(a)Piling number of carton   : 9
(b)Packing quantity in one carton   : 6 pcs
(c)Carton size   : 815(L)x425(W)x530(H) mm
(d)Pallet size   : 1350(L)x850(W)x150(H) mm
(e)Total mass of one carton filled with full module   : Approximately 35.5 kg

8.2 PACKING METHOD
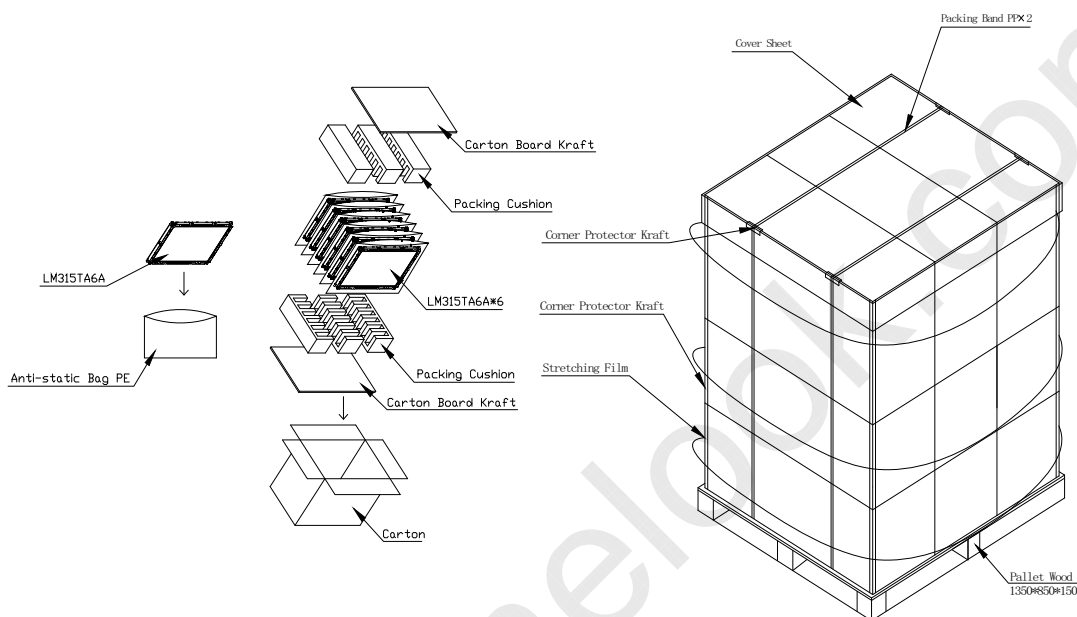
9. CARTON STORAGE CONDITION

Temperature: 0°C to 40°C
Humidity: 80%RH or less
Reference condition: 20°C to 35°C, 80%RH or less (summer)
                                      5°C to 15°C, 80%RH or less (winter)
                                      The total storage time (40°C, 80%RH): 240h or less
Sunlight: Be sure to shelter a product from the direct sunlight.
Atmosphere: Harmful gas, such as acid and alkali which bites electronic components and/or wires must not be detected.

Be sure to put cartons on palette or base, don't put it on floor, and store them with removing from wall. Please take care of ventilation in storehouse and around cartons, and control changing temperature is within limits of natural environment.

10. PRECAUTIONS
10.1 ASSEMBLY AND HANDLING PRECAUTIONS

(a) Do not apply rough force such as bending or twisting to the module during assembly.

(b) It is recommended to assemble or to install a module into the user's system in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.

(c) Since the LCM consists of TFT and electronic circuits with CMOS-ICs, which are very weak to electrostatic discharge, person who is handling an LCM should be grounded though adequate methods such as an anti-static wrist band. Connector pins should not be touched directly with bare hands.

Reference: Process control standard is shown as follow,

	item	Management standard value and performance standard
1	Anti-static mat(shelf)	1to50 [Mega ohm]
2	Anti-static mat(floor, desk)	1to100 [Mega ohm]
3	Ionizer	Attenuate from ±1000V to ±100V within two seconds.
4	Anti-static wrist band	0.8 to 10 [Mega ohm]
5	Anti-static wrist band entry and ground resistance	Below 1000 [ohm]
6	Temperature	22 to 26 [ ]
7	Humidity	60 to 70 [%]

(d) Do not apply pressure or impulse to the module to prevent the damage of LCD panel and backlight.

(e) Always follow the correct power-on sequence when the LCD module is turned on. This can prevent the damage and latch-up of the CMOS LSI chips.

(f) Be sure to turn off the power supply when inserting or disconnecting the cable.

(g) Do not disassemble the module.

(h) Front polarizer can easily be damaged, so please pay attention on it.

(i) Using a absorbent cotton or other soft cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.

(j) Since long contact with drops of water may cause discoloration or spots, please wipe off them as soon as possible.

(k) The Panel will be broken or chipped when it is dropped or bumped against a hard substance.

(l) Applying too much force and stress to PWBs and drivers may cause a malfunction electrically and mechanically.

(m) Please be careful since image retention may occur when a fixed pattern is displayed for a long time.

(n) Moisture can easily penetrate into LCD module and may cause the damage during operation.

(o) High temperature or humidity may deteriorate the performance of LCD module. Please store LCD modules in the specified storage conditions.

(p) This LCM is corresponded to ROHS.

(q) When any question or issue occurs, it shall be solved by mutual discussion.

10.2 SAFETY PRECAUTIONS

(a) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.

(b) After the module's end of life, it is not harmful in case of normal operation and storage.

11. RELIABILITY

(a) Environment test condition

Test item	Condition
High temperature storage test	Ta= 60°C, 240h
Low temperature storage test	Ta= -20°C, 240h
High temperature and high humidity storage test	Ta= 50°C, 80%RH, 240h (No condensation)
High temperature operation test	Ta= 50°C, 240h
Low temperature operation test	Ta= 0°C,240h

(b) Shock & Vibration

Test item	Condition
Shock (Non-Operation)	Shock level：50 Grms Waveform：half sine wave, 11ms Direction：±X,±Y,±Z One time each direction
Vibration (Non-Operation)	Wave form：Random Vibration level：1.0 Grms Bandwidth：10-300 Hz Duration：X,Y,Z, 30 min Each direction per 10 min

[Result evaluation criteria]
Under the display quality test condition with normal operation state, there shall be no change, which may affect practical display function.

12. MECHANICAL DRAWING
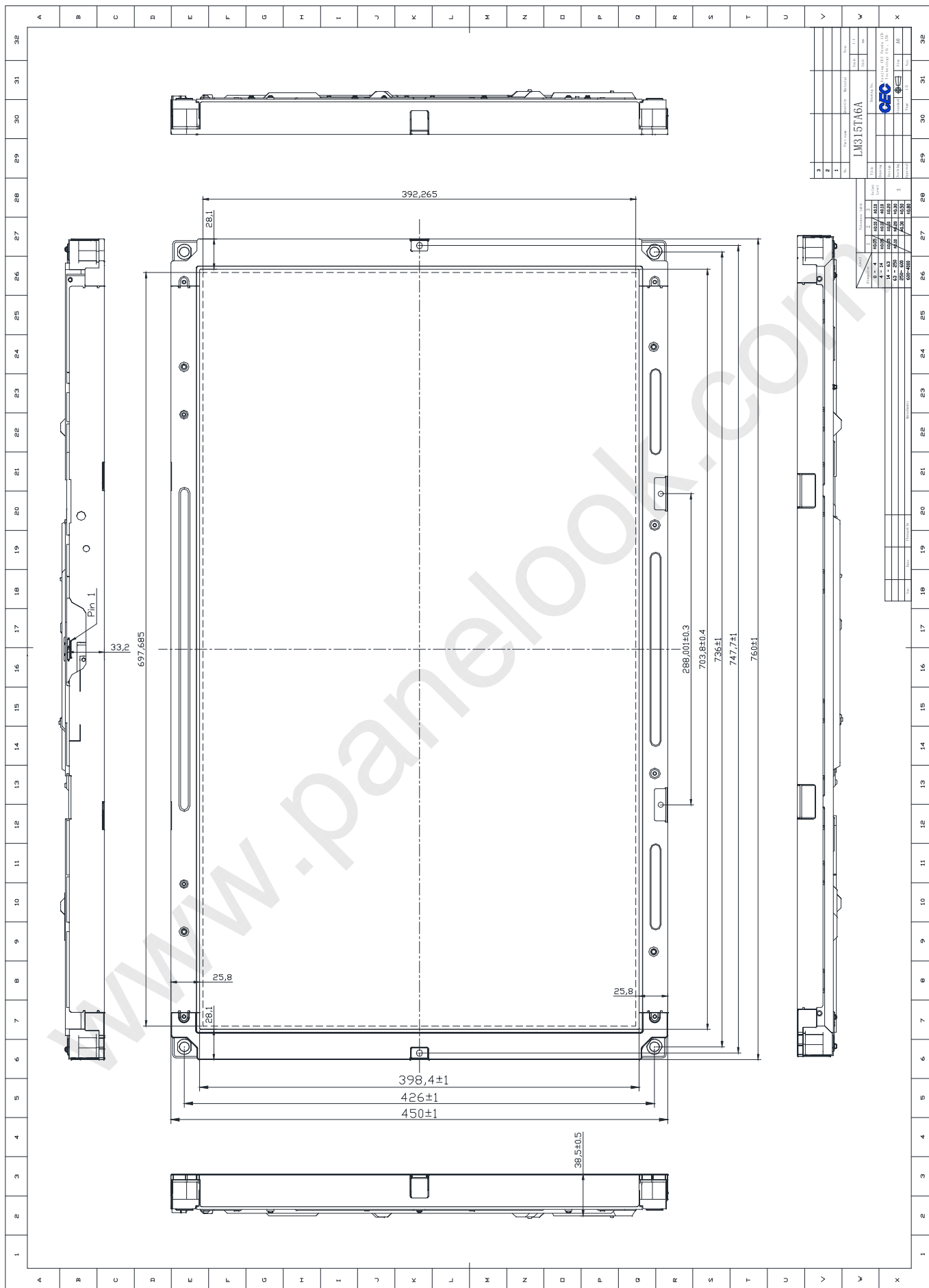
Fig.12-1 Front outline drawing

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Fig.12-2 Back outline drawing

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