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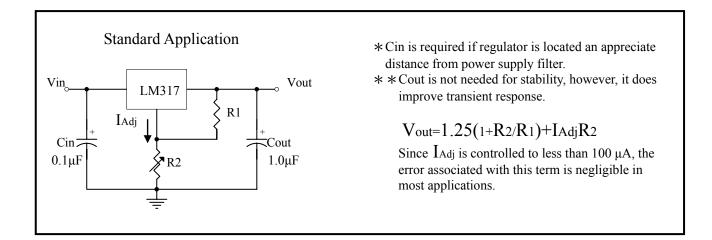
Three Terminal Adjustable Output Positive Voltage Regulators

# LM317L3

The LM317L3 is an adjustable 3-terminal positive voltage regulator capable of supplying in excess of 1.5A over an output voltage range of 1.2 V to 37 V. This voltage regulator is exceptionally easy to use and requires only two external resistors to set the output voltage. Further, it employs internal current limiting, thermal shutdown and safe area compensation, making it essentially blow-out proof.

The LM317L3 serves a wide variety of applications including local, on-card regulation. This device can also be used to make a programmable output regulator, or by connecting a fixed resistor between the adjustment and output, the LM317L3 can be used as a precision current regulator.

- Output Current in Excess of 1.5 A
- Output Adjustable between 1.2 V and 37 V
- Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting Constant with Temperature
- Output Transistor Safe–Area Compensation
- Floating Operation for High Voltage Applications
- Eliminates Stocking many Fixed Voltages
- Pb-free lead plating and halogen-free package.



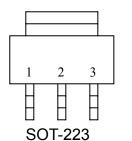
# **Ordering Information**

Device	Package	Shipping	
LM317L3-0-T3-G	SOT-223 (Pb-free lead plating and halogen-free package)	2500 pcs / tape & reel	
	Environment friendly grade : S for RoHS compliant products, green compound products Packing spec, T3 : 2500 pcs / tape & reel, 13" reel Product rank, zero for no rank products Product name	G for RoHS compliant and	



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# **Package Information**



Pin	Function
1	ADJ
2	OUTPUT
3	INPUT

# Maximum Ratings TA=25°C

Rating	Symbol	Value	Unit
Input-Output Voltage Differential	VI-Vo	40	V
Power Dissipation	Pd	1.5	W
Operating Junction Temperature Range	TJ	-40 to +125	°C
Storage Temperature Range	Tstg	-65 to +150	°C

### Electrical Characteristics (VI-Vo=5V, Io=0.5A, TJ=0 to 125°C, unless otherwise noted)

Characteristics	Symbol	Conditions	Min	Тур	Max	Unit
Line Regulation(Note 1)	Reg line	TA=25°C,3.0V≤VI-Vo≤40V	-	0.01	0.04	%/V
Load Regulation(Note 1)	Reg load	T <sub>A</sub> =25°C,10mA≤Io≤1.5A				
		Vo≤5.0V	-	5.0	25	mV
		Vo≥5.0V	-	0.1	0.5	%Vo
Thermal Regulation(Note 4)	$Reg \ {\it therm}$	$T_A=25^{\circ}C$ , 20ms pulse	-	0.03	0.07	%Vo/W
Adjust Pin Current	IAdj		-	50	100	μΑ
Adjust Pin Current Change	$\Delta I$ Adj	2.5V≤V1-V0≤40V,10mA≤IL≤1.5A PD≤5W	-	0.2	5.0	μΑ
Reference Voltage	Vref	3.0V≤V1-V0≤40V,10mA≤I0≤1.5A PD≤5W	1.2	1.25	1.3	V
Temperature Stability	Ts	0≤TJ≤125°C	-	0.7	-	%Vo
Minimum Load Current to maintain Regulation	ILmin	VI-Vo=40V	-	3.5	10	mA
Maximum Output Current	Imax	VI-Vo≤15V,PD≤5W	1.5	2.2	-	А
_		VI-Vo≤40V,PD≤5W,TA=25°C	0.15	0.4	-	
RMS Noise	Ν	% of Vo,TA=25°C,10Hz≤f≤10kHz	-	0.003	-	%Vo
Ripple Rejection(Note 2)	RR	Vo=10V,f=120Hz				dB
		Without CAdj	-	65	-	
		CAdj=10µF	66	80	-	
Long-Term Stability(Note 3)	S	$T_J=125^{\circ}C, T_A=25^{\circ}C$ for endpoint	-	0.3	1.0	%/1.0k
		measurements				Hrs

Notes: 1. Load and line regulation are specified at constant junction temperature. Changes in VO due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

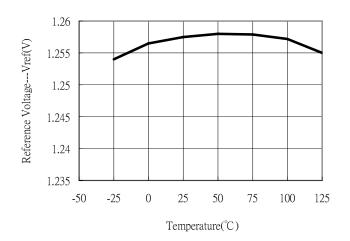
2.CAdj, when used, is connected between the adjustment pin and ground.



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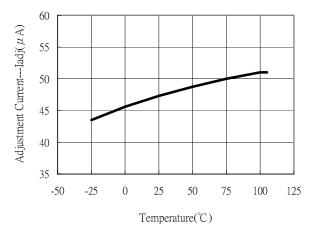
- 3. Since long-term stability cannot be measured on each device before shipment, this specification is an engineering estimate of average stability from lot to lot.
- 4.Power dissipation within an IC voltage regulator produces a temperature gradient on the die, affecting individual IC components on the die. These effects can be minimized by proper integrated circuit design and layout techniques. Thermal regulation is the effect of these temperature gradients on the output voltage and is expressed in percentage of output change per watt of power change in a specified time.

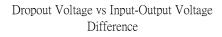
# **Typical Characteristics**

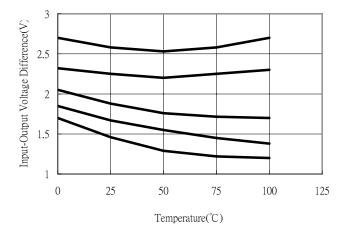


Reference Voltage vs Temperature

Adjustment Current vs Temperature

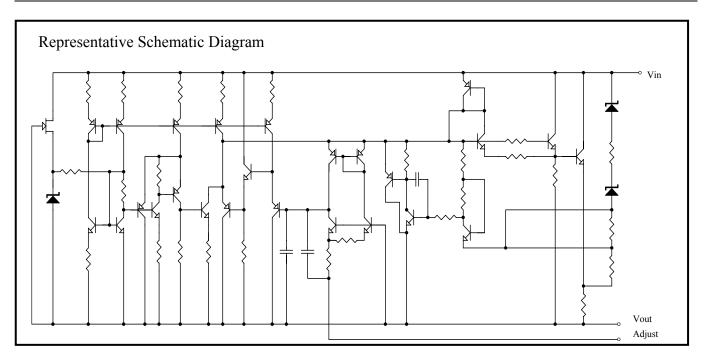


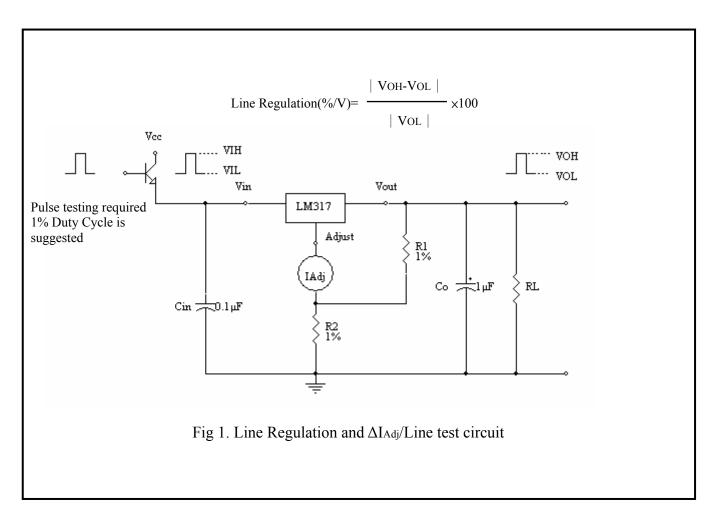






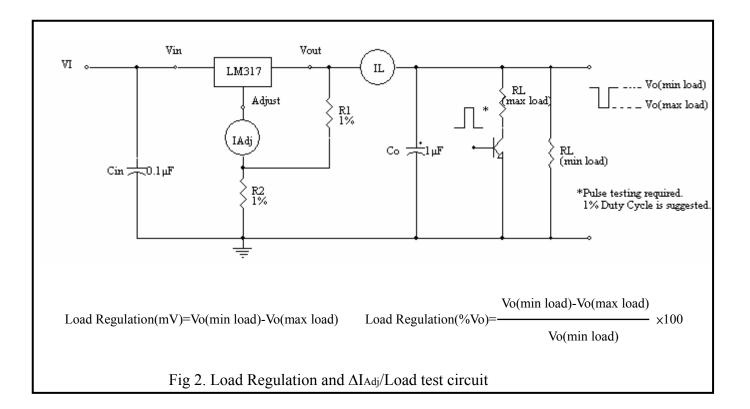
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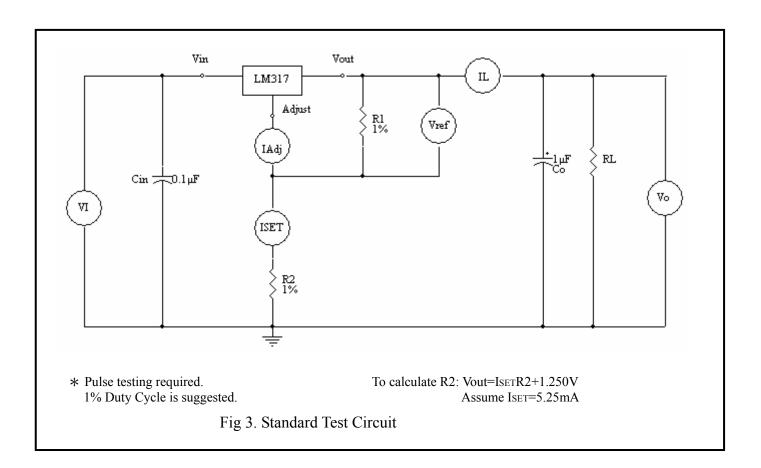






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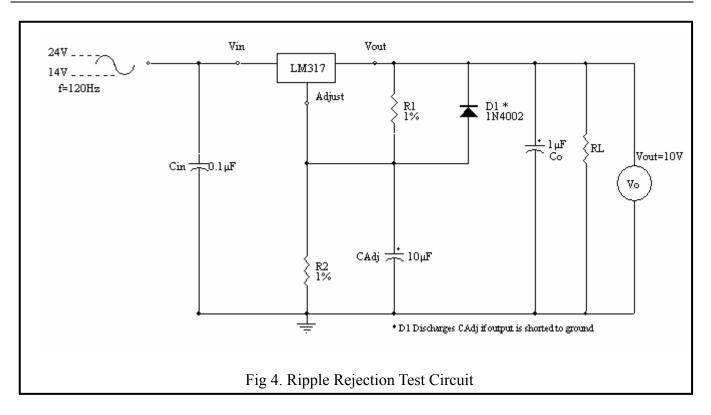






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# **Application Information**

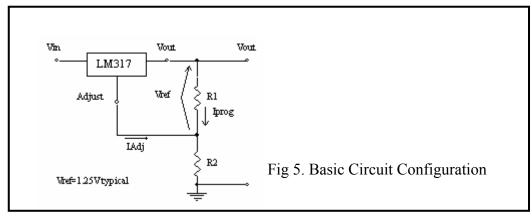
### **Basic Circuit Operation**

The LM317 is a 3-terminal floating regulator. In operation, the LM317 develops and maintains a nominal 1.25V reference (Vref) between its output and adjustment terminals. This reference voltage is converted to a programming current (Iprog) by R1(see Fig 5), and this constant current flows through R2 to ground. The regulated output voltage is given by:

Vout=Vref  $(1+R_2/R_1)+I_{Adj}R_2$ 

Since the current from the adjustment terminal (IAdj) represents an error term in the equation, the LM317 was designed to control IAdj to less than 100  $\mu$ A and keep it constant. To do this, all quiescent operating current is returned to the output terminal. This imposes the requirement for a minimum load current. If the load current is less than this minimum, the output voltage will rise.

Since the LM317 is a floating regulator, it is only the voltage differential across the circuit which is important to performance, and operation at high voltages with respect to ground is possible.





#### Load Regulation

The LM317 is capable of providing extremely good load regulation, but a few precautions are needed to obtain maximum performance. For best performance, the programming resistor (R1) should be connected as close to the regulator as possible to minimize line drops which effectively appear in series with the reference, thereby degrading regulation. The ground end of R2 can be returned near the load ground to provide remote ground sensing and improve load regulation.

#### **External Capacitors**

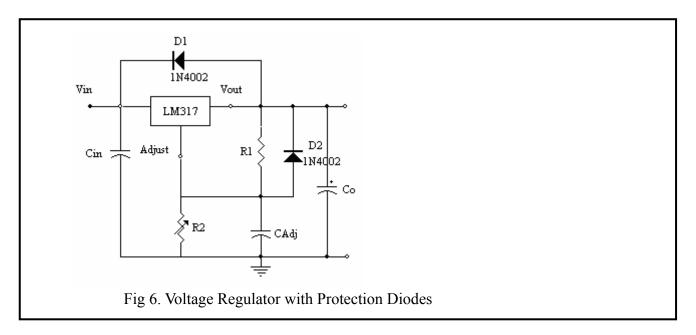
A  $0.1\mu$ F disc or  $1.0\mu$ F tantalum input bypass capacitor (Cin) is recommended to reduce the sensitivity to input line impedance.

The adjustment terminal may be bypassed to ground to improve ripple rejection. This capacitor  $(C_{Adj})$  prevents ripple from being amplified as the output voltage is increased. A 10µF capacitor should improve ripple rejection about 15 dB at 120 Hz in a 10V application.

Although the LM317 is stable with no output capacitance, like any feedback circuit, certain values of external capacitance can cause excessive ringing. An output capacitance (Co) in the form of a  $1.0\mu$ F tantalum or  $25\mu$ F aluminum electrolytic capacitor on the output swamps this effect and insures stability.

#### **Protection Diodes**

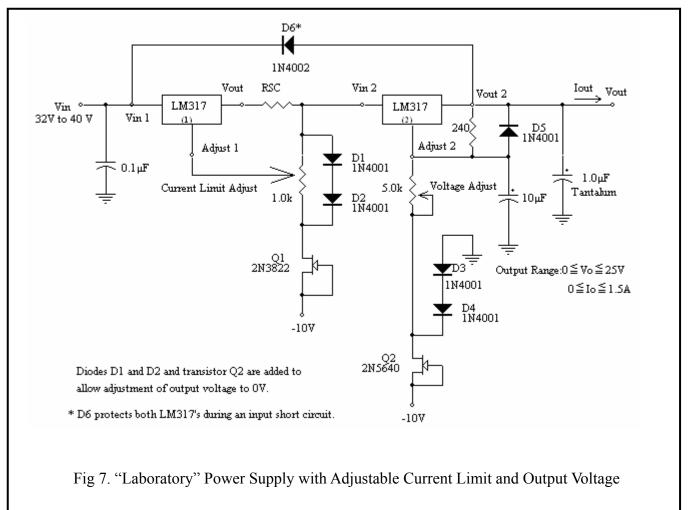
When external capacitors are used with any IC regulator, it is sometimes necessary to add protection diodes to prevent the capacitors from discharging through low current points into the regulator. Fig 6 shows the LM317 with the recommended protection diodes for output voltages in excess of 25V or high capacitance values (Co>25 $\mu$ F,CAdj>10 $\mu$ F). Diode D1 prevents Co from discharging thru the IC during an input short circuit. Diode D2 protects against capacitor CAdj discharging through the IC during an output short circuit. The combination of diodes D1 and D2 prevents CAdj from discharging through the IC during an input short circuit.

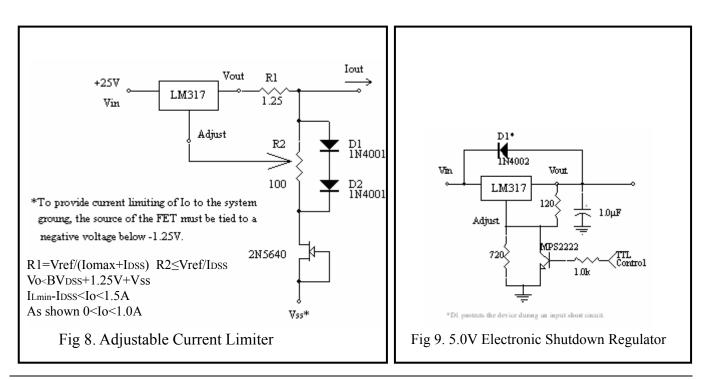




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# **Application Circuits**

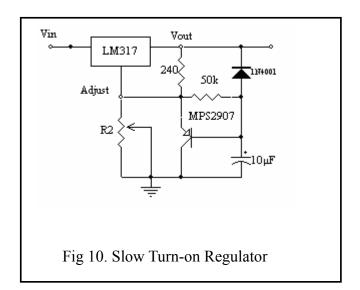






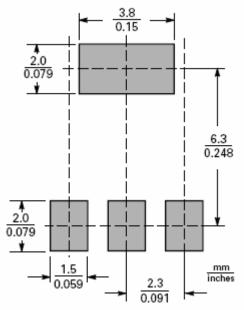
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Vin Vout Iout Adjust IAdj Iout=(Vref/R1)+IAdj =1.25 V/R1 10mA  $\leq$  Iout  $\leq$  1.5 A Fig 11. Current Regulator

# **Recommended soldering footprint**

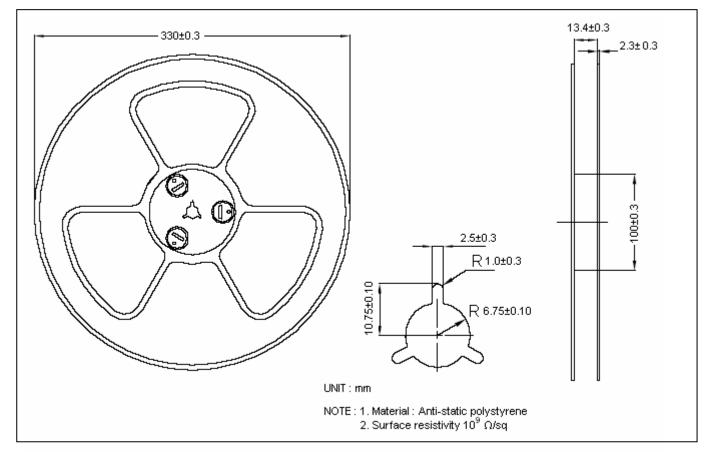




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# **Reel Dimension**



# **Carrier Tape Dimension**

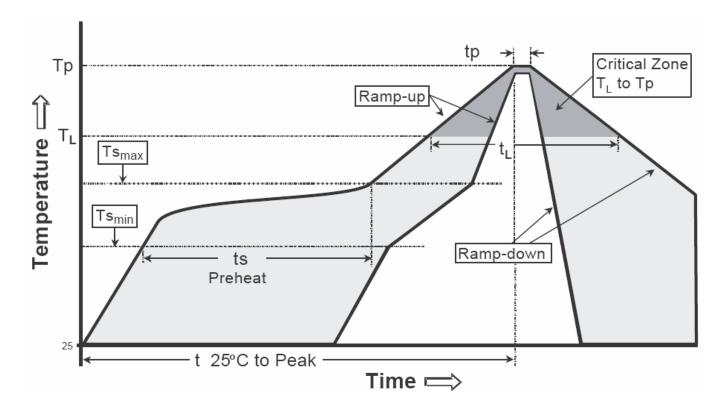
$A-A SECTION \qquad \qquad$								
Symbol	Ao	Bo	Ko	Po	P1	P2	Т	
Spec	6.83±0.1	7.42±0.1	1.88±0.1	4.0±0.1	8.0±0.10	2.0±0.05	0.292±0.02	
Symbol	E	F	Do	D1	W	10Po		
Spec 1.75±0.1 5.5±0.05 1.60±0.1 1.5±0.25 12 <sup>+0.3</sup> 0.1 40.0±0.2								
Unit : mm								



#### **Recommended wave soldering condition**

Product	Peak Temperature	Soldering Time		
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds		

## **Recommended temperature profile for IR reflow**

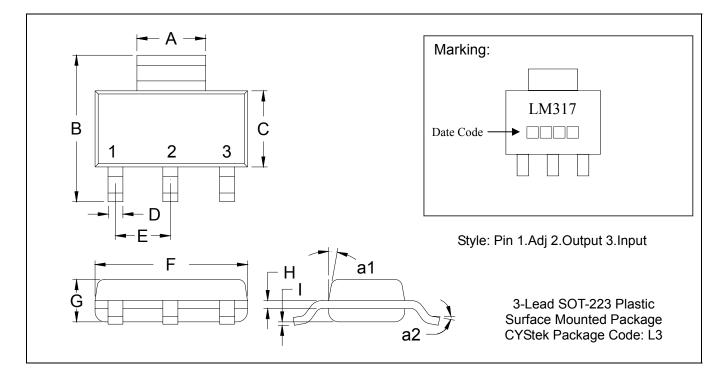


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly		
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.		
Preheat -Temperature Min(Ts min) -Temperature Max(Ts max) -Time(ts min to ts max)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds		
Time maintained above: – Temperature (T∟) – Time (t∟)	183°C 60-150 seconds	217°C 60-150 seconds		
Peak Temperature(T <sub>P</sub> )	240 +0/-5 °C	260 +0/-5 °C		
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds		
Ramp down rate	6°C/second max.	6°C/second max.		
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.		

Note : All temperatures refer to topside of the package, measured on the package body surface.



# SOT-223 Dimension



\*: Typical

. Typical									. Typical
DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.	DIN	Min.	Max.	Min.	Max.
Α	0.1142	0.1220	2.90	3.10	G	0.0551	0.0709	1.40	1.80
В	0.2638	0.2874	6.70	7.30	Н	0.0098	0.0138	0.25	0.35
С	0.1299	0.1457	3.30	3.70	I	0.0008	0.0039	0.02	0.10
D	0.0236	0.0315	0.60	0.80	a1	*13°	-	*13°	-
E	*0.0906	-	*2.30	-	a2	0 °	10°	0 °	10°
F	0.2480	0.2638	6.30	6.70					

Notes: 1.Controlling dimension: millimeters.

2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material. 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

#### Material:

• Lead: Pure tin plated.

• Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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