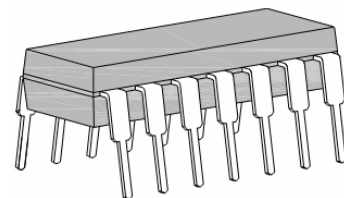


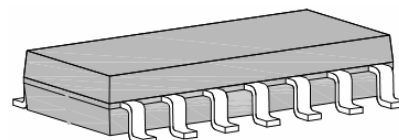
Low Power Low Offset Voltage Quad Comparator

General Description

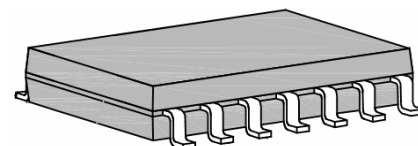
- The LM339/339A series consist of four independent precision voltage comparators with a typical offset voltage of 2.0mV and high gain.
- The LM339/339A series are specifically designed to operate from a single power supply over wide range of voltages. Operation from split power supply is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage.
- The LM339/339A series are compatible with industry standard 339.
- The LM339/339A series are available in standard packages of DIP-14, SOP-14N and TSSOP-14.



DIP-14



SOP-14N



TSSOP-14

Features

- Wide supply voltage range -Single supply: 2.0V to 36V
-Dual supplies: $\pm 1.0V$ to $\pm 18V$
- Low supply current drain: 0.9mA
- Low input bias current: 25nA (typical)
- Low input offset current: $\pm 5.0nA$ (typical)
- Low input offset voltage: 2.0mV (typical)
- Input common mode voltage range includes ground
- Differential input voltage equals to the power supply voltage
- Low output saturation voltage: 200mV at 4mA
- Open collector output
- RoHS compliant and halogen free

Applications

- Battery Charger
- Cordless Telephone
- Switching Power Supply
- DC-DC Module
- PC Motherboard
- Communication Equipment

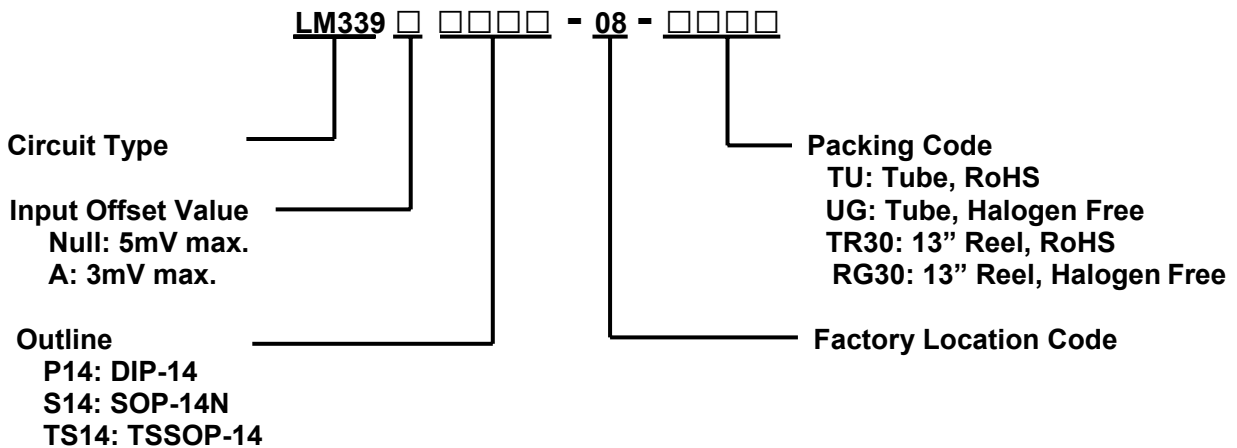


**HALOGEN
FREE**

Low Power Low Offset Voltage Quad Comparator

LM339/339A Series

Ordering Information



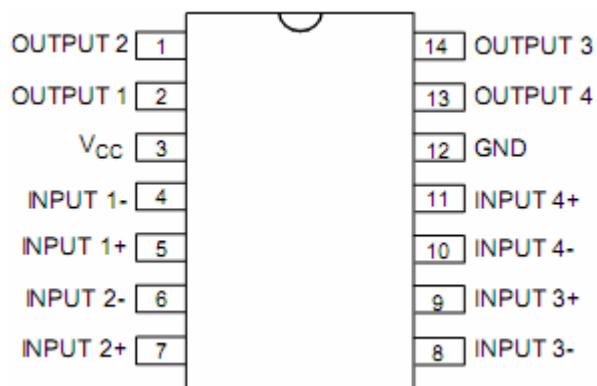
Marking Information

Outline	Temperature Range	PN	Marking Code	Packing Type
SOP-14N	-40 to 85°C	LM339S14-08-TU	AS339M-E1	Tube
		LM339S14-08-UG	AS339M-G1	
		LM339AS14-08-TU	AS339AM-E1	
		LM339AS14-08-UG	AS339AM-G1	
		LM339S14-08-TR30	AS339M-E1	Tape & Reel
		LM339S14-08-RG30	AS339M-G1	
		LM339AS14-08- TR30	AS339AM-E1	
TSSOP-14	-40 to 85°C	LM339AS14-08- RG30	AS339AM-G1	
		LM339TS14-08-TU	EGS339	Tube
		LM339TS14-08-UG	GGG339	Tape & Reel
		LM339TS14-08-TR30	EGS339	
DIP-14	-40 to 85°C	LM339TS14-08-RG30	GGG339	
		LM339P14-08-TU	AS339P-E1	Tube
		LM339P14-08-UG	AS339P-G1	

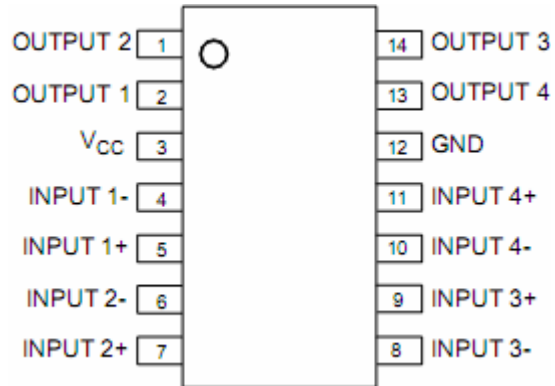
Low Power Low Offset Voltage Quad Comparator

LM339/339A Series

Pin Configuration

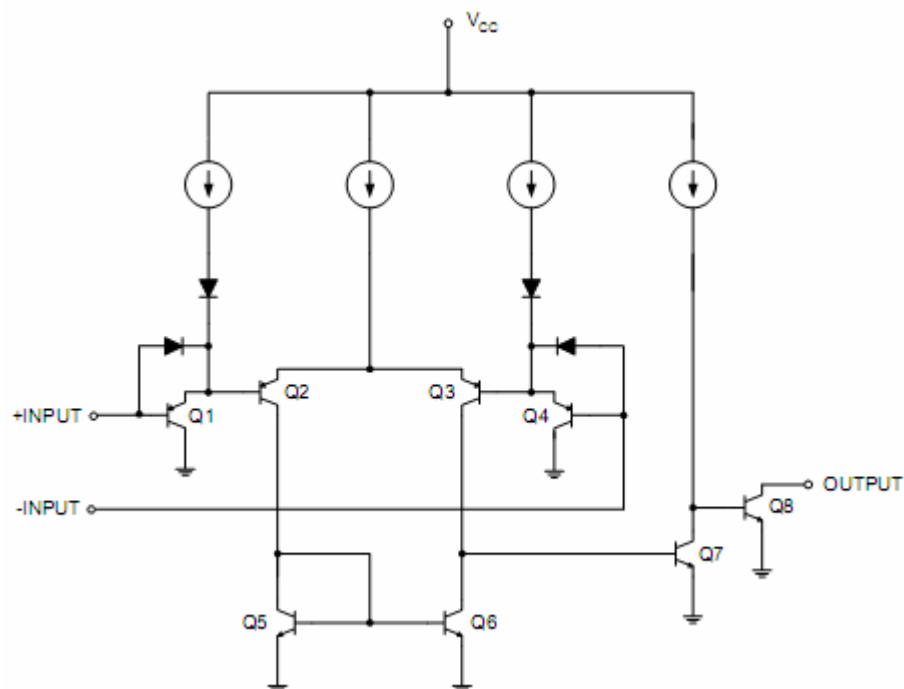


DIP-14



SOP-14N & TSSOP-14

Block Diagram (Each Comparator)



Low Power Low Offset Voltage Quad Comparator

LM339/339A Series

Absolute Maximum Ratings (Note 1)

Symbol	Description	Ratings	Unit	
V _{CC}	Supply Voltage	40	V	
V _{ID}	Differential Input Voltage	40	V	
V _{IN}	Input Voltage	-0.3 to 40	V	
I _{IN}	Input Current (V _{IN} <-0.3V) (Note 2)	50	mA	
	Output Short-Circuit to Ground	Continuous		
P _D	Power Dissipation (T _A =25°C)	DIP-14	1050	mW
		SOP-14N	890	
		TSSOP-14	790	
T _J	Operating Junction Temperature	150	°C	
T _{STG}	Storage Temperature Range	-65 ~ +150	°C	
T _{LEAD}	Lead Temperature (Soldering, 10 sec.)	260	°C	

Note: 1. Stresses greater than those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “Recommended Operating Conditions” is not implied. Exposure to “Absolute Maximum Ratings” for extended period may affect device reliability.

2. This input current will only exist when the voltage at any of the input leads is driven negative. It is due to the collector-base junction of the input PNP transistors becoming forward biased and thereby acting as input diode clamps. In addition to this diode action, there is also lateral NPN parasitic transistor action on the IC chip. This transistor action can cause the output voltages of the comparators to go to the V₊ voltage level (or to ground for a large overdrive) for the time duration that an input is driven negative. This is not destructive and normal output states will re-establish when the input voltage, which was negative, again returns to a value greater than -0.3 V_{DC} (at 25°C).

Recommended Operating Conditions

Symbol	Description	Min.	Max.	Unit
V _{CC}	Supply Voltage	2	36	V
T _A	Operating Temperature Range	-40	85	°C

Low Power Low Offset Voltage Quad Comparator

LM339/339A Series

Electrical Characteristics

$T_A=25^{\circ}\text{C}$, $V_{CC}=5\text{V}$, $GND=0\text{V}$, Unless otherwise specified $-40^{\circ}\text{C}\leq T_A\leq 85^{\circ}\text{C}$ (Note 3)

Description		Min.	Typ.	Max.	Unit	Conditions	
Input Offset Voltage	LM339	-	2	5	mV		$V_O=1.4\text{V}$, $R_S=0\Omega$, $V_{CC}=5\text{V to }30\text{V}$
		-	-	7		$-40^{\circ}\text{C}\leq T_A\leq 85^{\circ}\text{C}$	
	LM339A	-	2	3			
		-	-	5		$-40^{\circ}\text{C}\leq T_A\leq 85^{\circ}\text{C}$	
Input Bias Current		-	25	250	nA		I_{IN+} or I_{IN-} with output in linear range. $V_{CM}=0\text{V}$
		-	-	400		$-40^{\circ}\text{C}\leq T_A\leq 85^{\circ}\text{C}$	
Input Offset Current		-	5	50	nA		$V_{CM}=0\text{V}$
		-	-	200		$-40^{\circ}\text{C}\leq T_A\leq 85^{\circ}\text{C}$	
Input Common Mode Voltage Range (Note 4)		0	-	$V_{CC}-1.5$	V	$V_{CC}=30\text{V}$	
Supply Current		-	0.9	2	mA		$V_{CC}=5\text{V}$, $R_L=\infty$
		-	-	3		$-40^{\circ}\text{C}\leq T_A\leq 85^{\circ}\text{C}$	
		-	1.2	2.5			$V_{CC}=30\text{V}$, $R_L=\infty$
		-	-	3.5		$-40^{\circ}\text{C}\leq T_A\leq 85^{\circ}\text{C}$	
Voltage Gain		50	200	-	V/mV	$V_{CC}=15\text{V}$, $R_L\geq 15\text{K}\Omega$, $V_O=1\text{V to }11\text{V}$	
Large Signal Response Time		-	200	-	ns	$V_{IN}=\text{TTL}$, Logic Swing, $V_{REF}=1.4\text{V}$, $V_{RL}=5\text{V}$, $R_L=5.1\text{K}$	
Response Time		-	1.3	-	μs	$V_{RL}=5\text{V}$, $R_L=5.1\text{K}$	
Output Sink Current		6	16	-	mA	$V_{IN+}=0\text{V}$, $V_{IN-}=1\text{V}$, $V_O=1.5\text{V}$	
Output Leakage Current		-	0.1	-	nA	$V_{IN+}=1\text{V}$, $V_{IN-}=0\text{V}$, $V_O=5\text{V}$	
		-	-	1	μA	$V_{IN+}=1\text{V}$, $V_{IN-}=0\text{V}$, $V_O=30\text{V}$, $-40^{\circ}\text{C}\leq T_A\leq 85^{\circ}\text{C}$	
Saturation Voltage		-	200	400	mV		$V_{IN+}=0\text{V}$, $V_{IN-}=1\text{V}$, $I_{SINK}\leq 4\text{mA}$
		-	-	500		$-40^{\circ}\text{C}\leq T_A\leq 85^{\circ}\text{C}$	

Note: 3. Limits over the full temperature are guaranteed by design, but not tested in production.

4. The input common-mode voltage of either input signal voltage should not be allowed to go negatively by more than 0.3V (at 25°C). The upper end of the common-mode voltage range is $V_{CC}-1.5\text{V}$ (at 25°C), but either or both inputs can go to +36V without damages, independent of the magnitude of the V_{CC} .

Low Power Low Offset Voltage Quad Comparator

LM339/339A Series

Typical Characteristics Curves

Fig.1- Supply Voltage vs. Supply Current

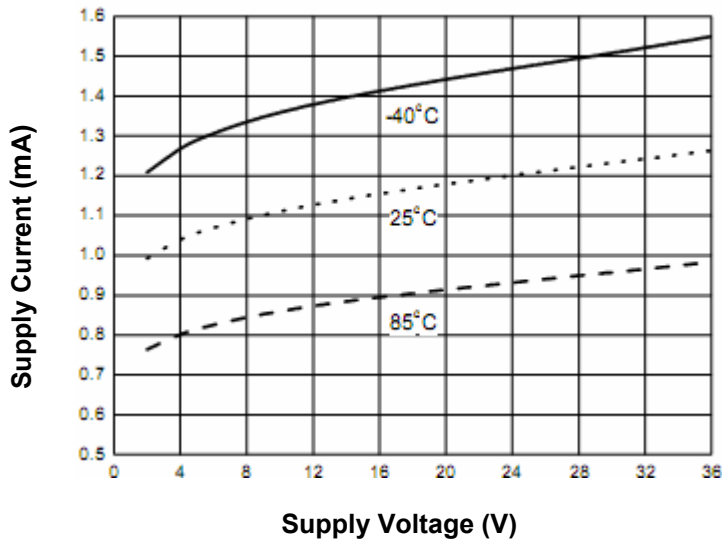


Fig.2- Supply Voltage vs. Input Bias Current

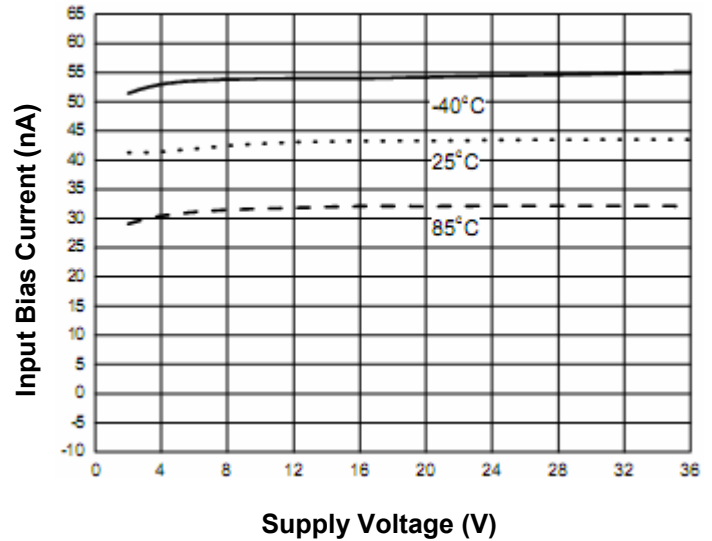


Fig.3- Output Sink Current vs. Saturation Voltage

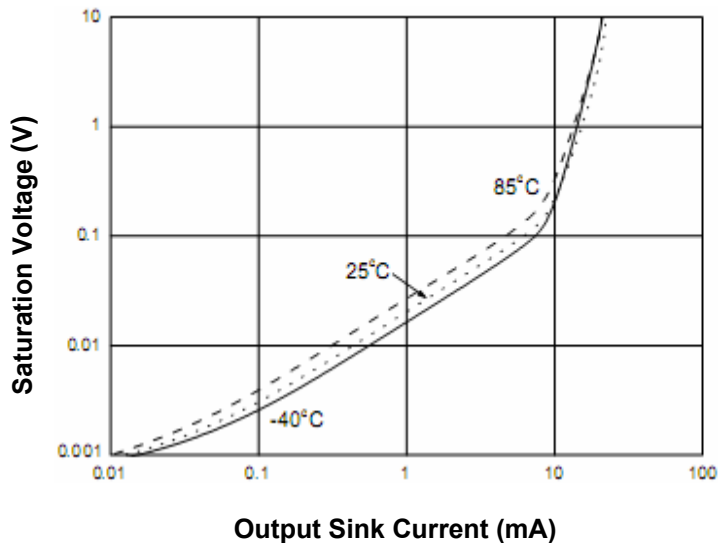
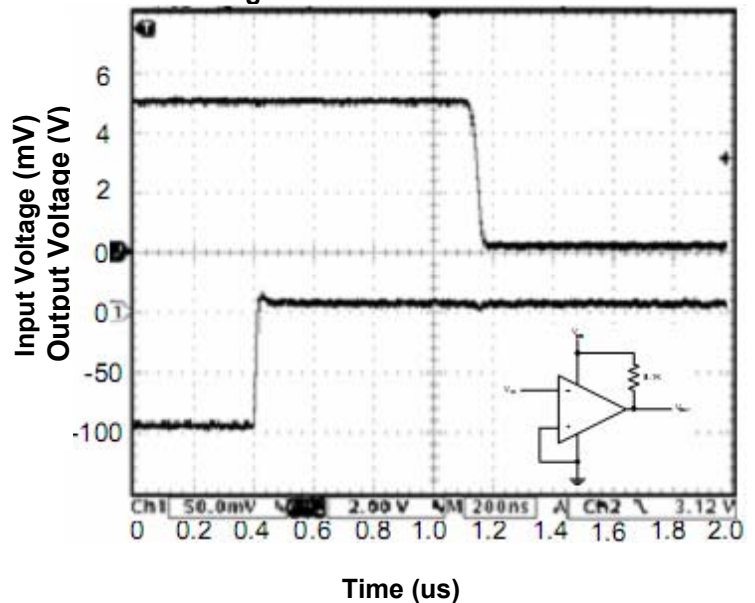


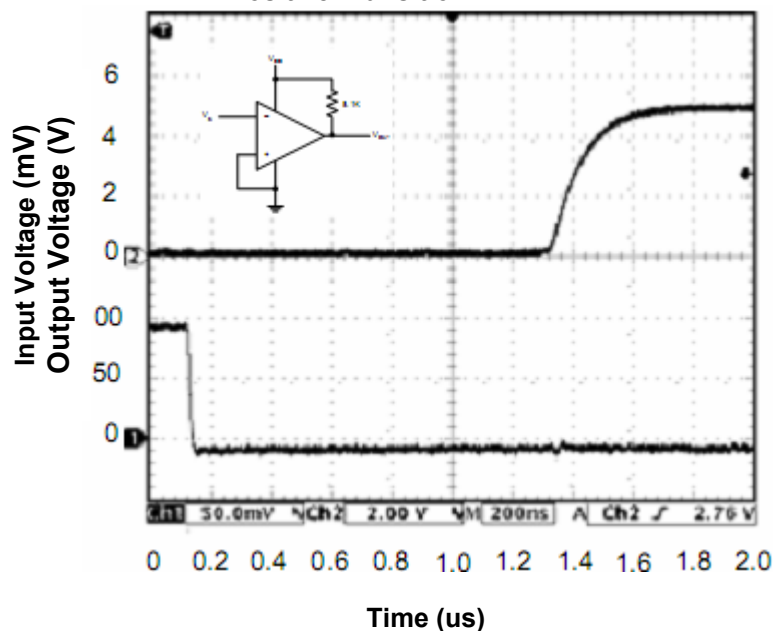
Fig.4- Response Time for 5mV Input Overdrive - Negative Transition



Low Power Low Offset Voltage Quad Comparator

LM339/339A Series

Fig.5- Response Time for 5mV Input Overdrive - Positive Transition



Typical Applications

Fig.6- Basic Comparator

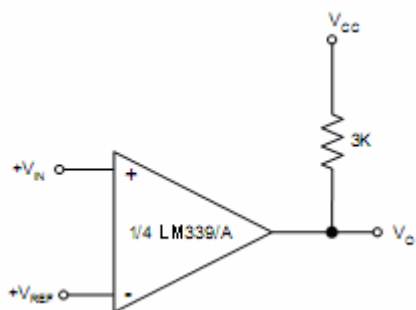
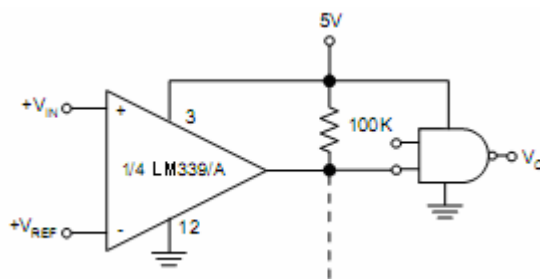


Fig.7- Driving CMOS



Low Power Low Offset Voltage Quad Comparator

LM339/339A Series

Fig.8- One Shot Multi-vibrator

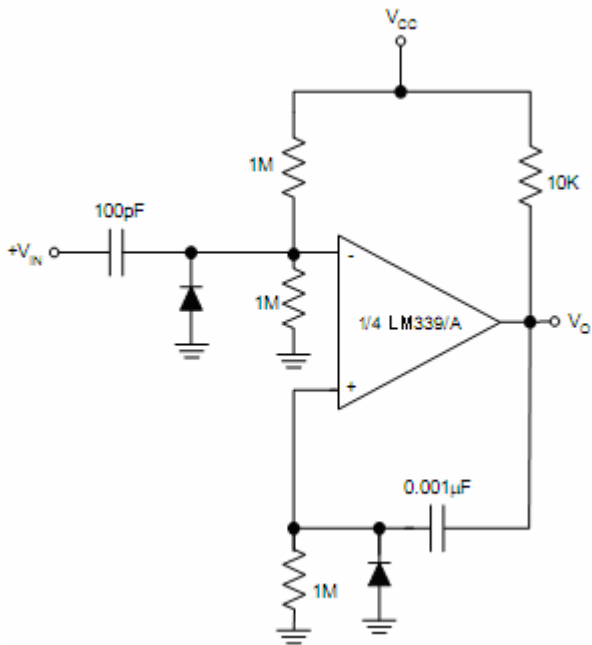
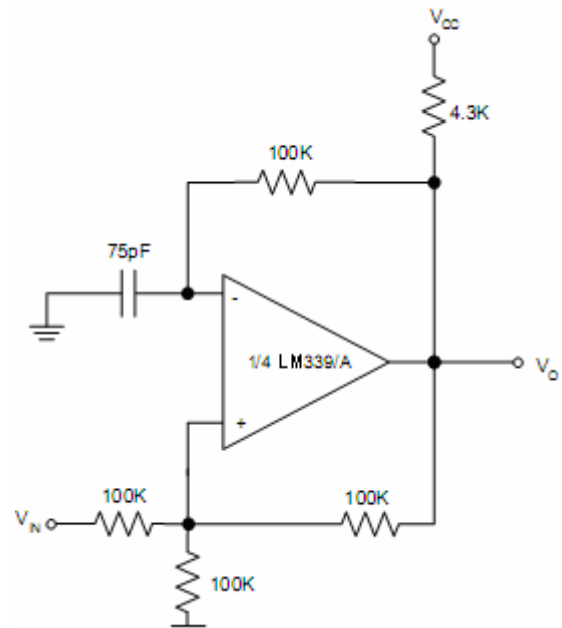


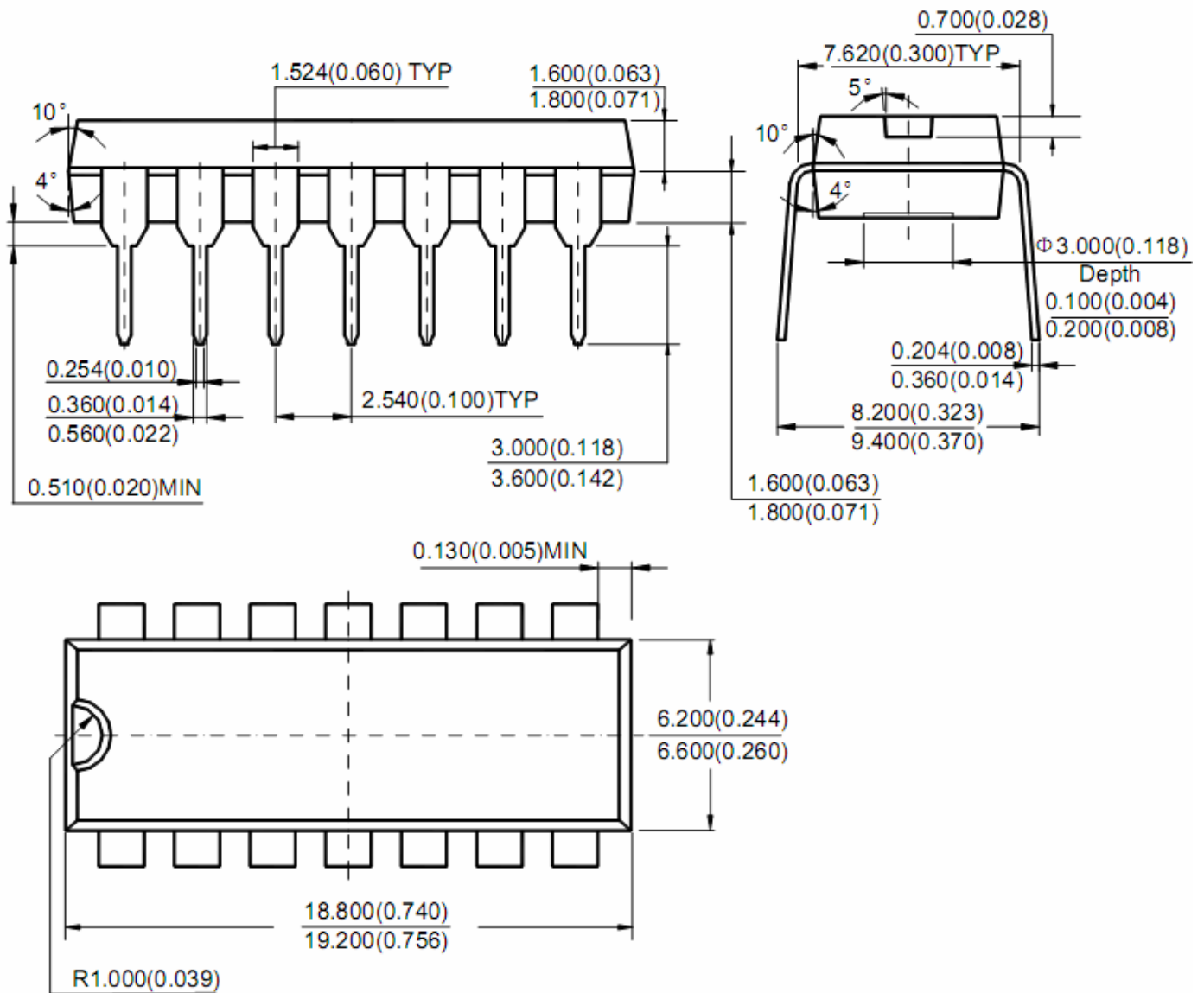
Fig.9- Square-wave Oscillator



Low Power Low Offset Voltage Quad Comparator

LM339/339A Series

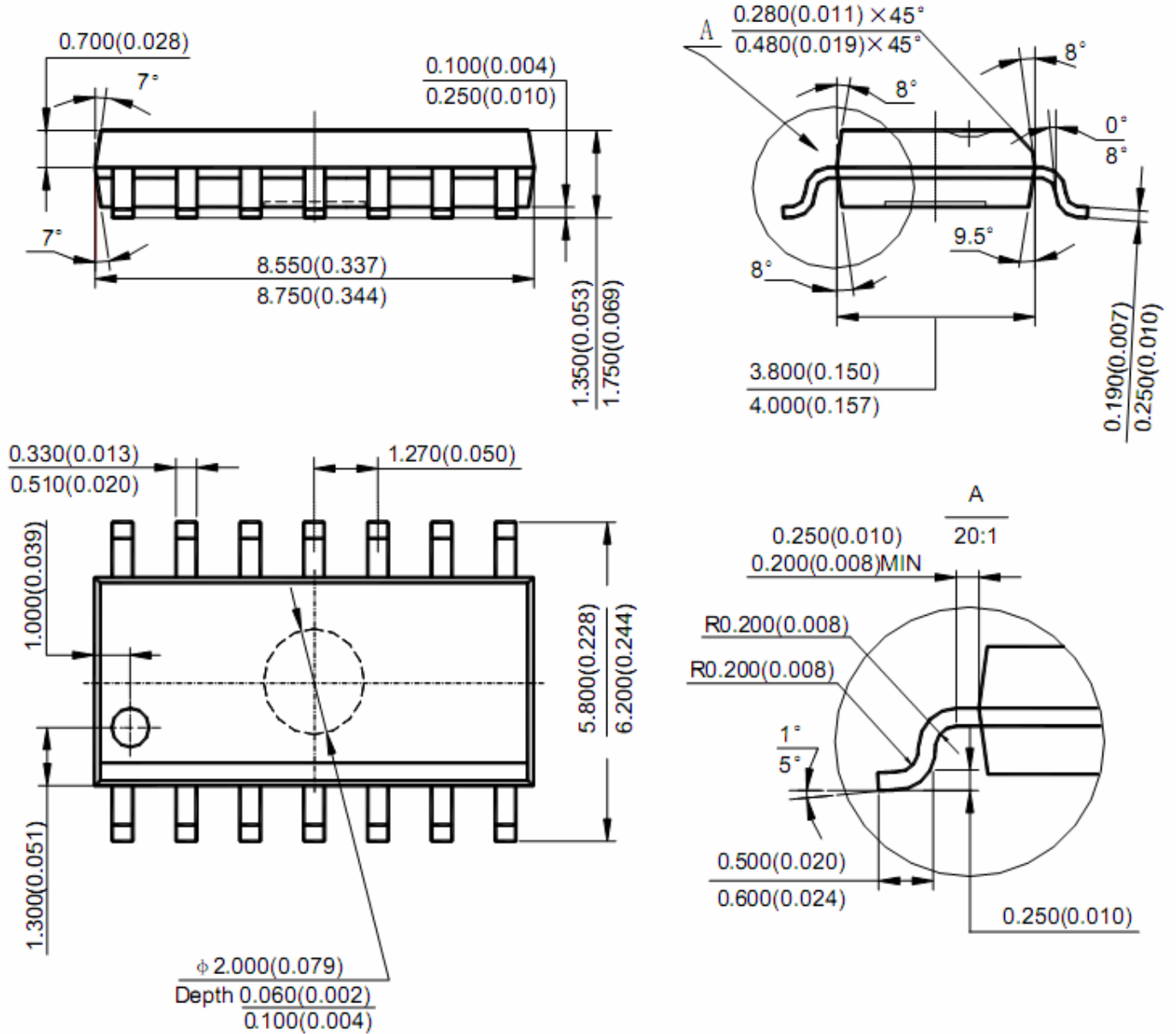
Dimensions in mm(inch)



DIP-14

Low Power Low Offset Voltage Quad Comparator

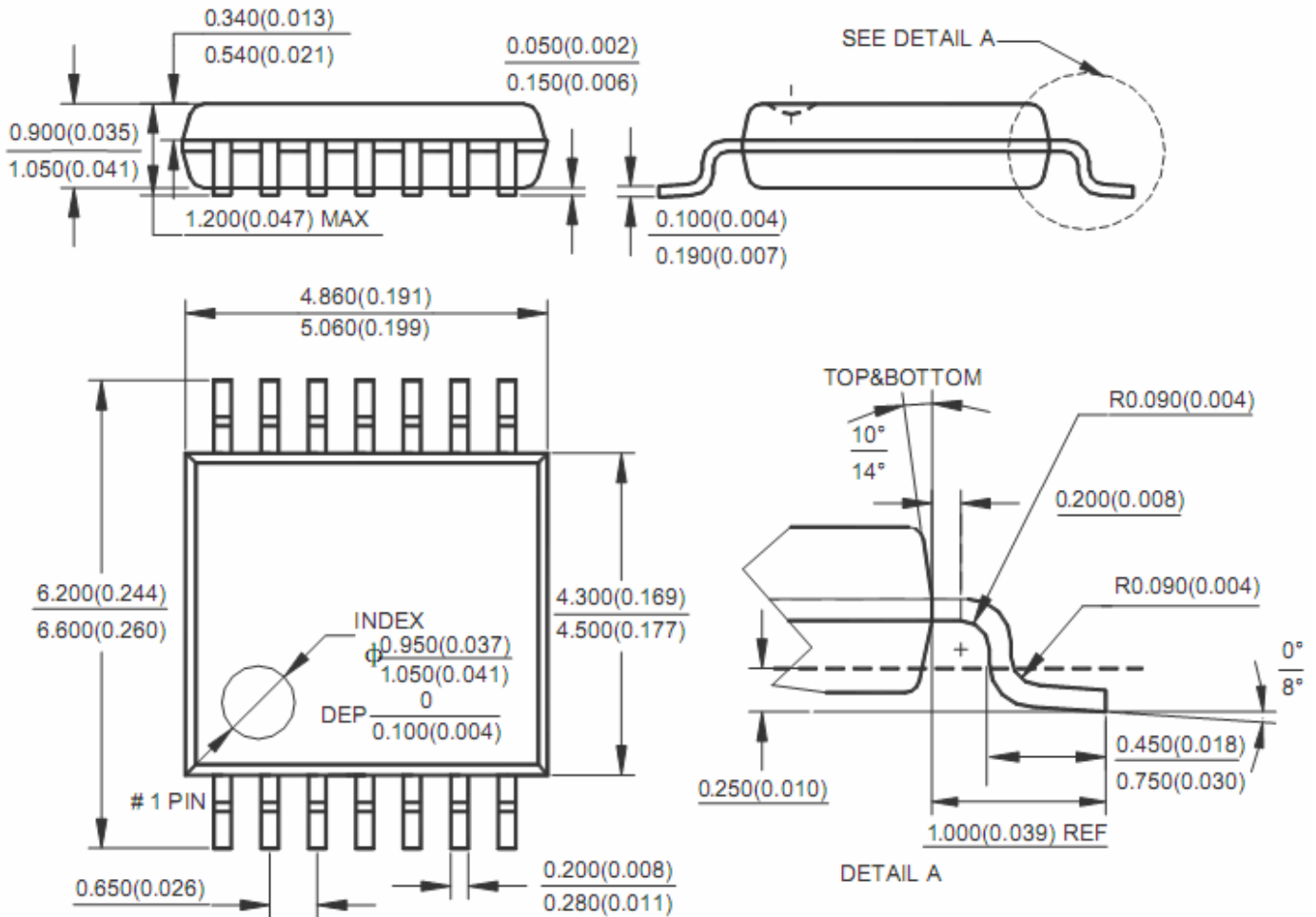
LM339/339A Series



SOP-14N

Low Power Low Offset Voltage Quad Comparator

LM339/339A Series



TSSOP-14

Low Power Low Offset Voltage Quad Comparator

LM339/339A Series

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