

isc Three Terminal Positive Voltage Regulator

LM7812LS

FEATURES

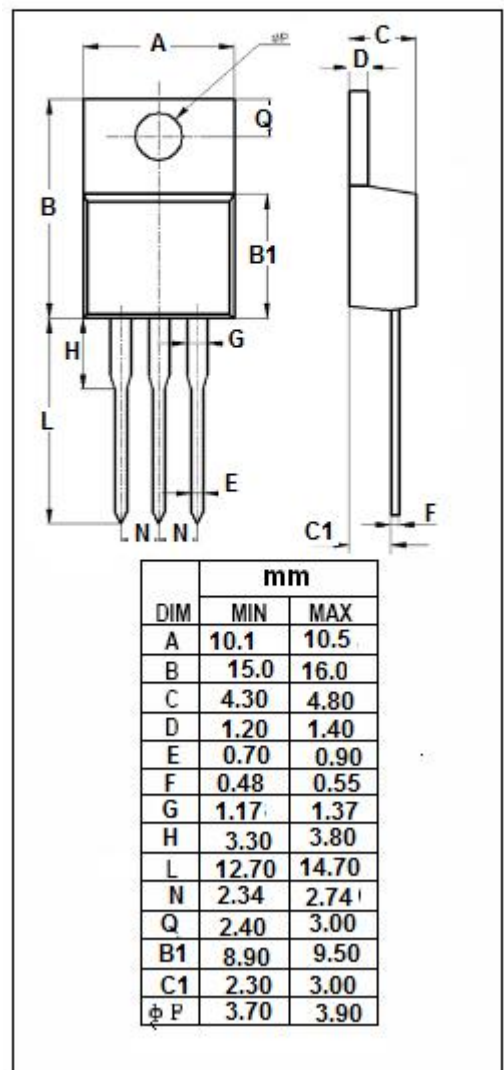
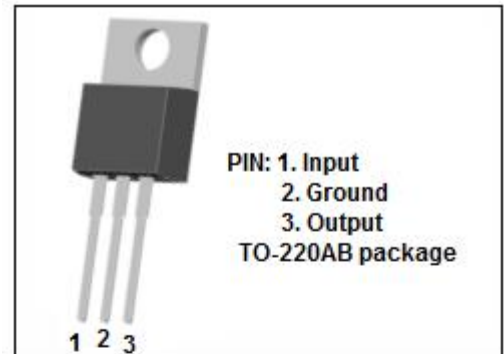
- Output current in excess of 1.5A
- Output voltage of 12V
- Internal thermal overload protection
- Output transition Safe-Area compensation
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)

SYMBOL	PARAMETER	RATING	UNIT
V_i	DC input voltage	35	V
I_o	Output current	internally limited	
P_{tot}	Power dissipation	internally limited	
T_{OP}	Operating junction temperature	0~150	$^{\circ}\text{C}$
T_{stg}	Storage temperature	-55~150	$^{\circ}\text{C}$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j-c}$	Thermal Resistance, Junction to Case	5	$^{\circ}\text{C/W}$
$R_{th\ j-a}$	Thermal Resistance, Junction to Ambient	50	$^{\circ}\text{C/W}$



isc Three Terminal Positive Voltage Regulator**LM7812LS****• ELECTRICAL CHARACTERISTICS**

$T_j=25^{\circ}\text{C}$ ($V_i=19\text{V}$, $I_o=0.5\text{A}$, $C_i=0.33\ \mu\text{F}$, $C_o=0.1\ \mu\text{F}$ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V_o	Output Voltage	$V_{in}=19\text{V}$; $I_o=500\text{mA}$	11.5	12.5	V
ΔV_v	Line Regulation	$14.5\text{V} \leq V_{in} \leq 30\text{V}$; $I_o=500\text{mA}$		120	mV
ΔV_i	Load Regulation	$5.0\text{mA} \leq I_o \leq 1.5\text{A}$; $V_{in}=19\text{V}$		100	mV
I_b	Quiescent Current	$V_{in}=19\text{V}$; $I_o=1.0\text{A}$		6.0	mA
Δ_{b1}	Quiescent Current Change	$5.0\text{mA} \leq I_o \leq 1.0\text{A}$; $V_{in}=19\text{V}$		0.5	mA
Δ_{b2}	Quiescent Current Change	$15\text{V} \leq V_{in} \leq 30\text{V}$; $I_o=500\text{mA}$		0.8	mA

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