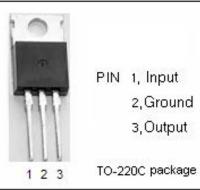


isc Three Terminal Positive Voltage Regulator

LM7824

FEATURES

- Output current in excess of 1.5A
- Output voltage of 24V
- Internal thermal overload protection
- Short circuit protection
- Output transition Safe-Area compensation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

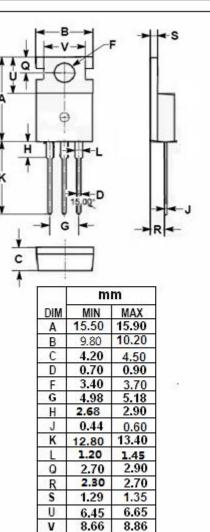


ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	RATING	UNIT
Vi	DC input voltage	40	V
lo	Output current	internally limited	
P _{tot}	Power dissipation	internally limited	
T _{OP}	Operating junction temperature	0~150	°C
T _{stg}	Storage temperature	-55~150	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal Resistance, Junction to Case	3	°C/W
Rth j-a	h j-a Thermal Resistance, Junction to Ambient		°C/W





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• ELECTRICAL CHARACTERISTICS

Tj=25°C (Vi= 33V, I_0=0.5A, Ci= 0.33 μ F, C_0= 0.1 μ F unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
Vo	Output Voltage	V _{in} =33V; I ₀ =500mA	23	25	V
$ riangle V_V$	Line Regulation	27V≪V _{in} ≪38V; I₀=500mA		240	mV
∆Vi	Load Regulation	5.0mA≪I ₀ ≪1.5A;V _{in} =33V		240	mV
lb	Quiescent Current	V _{in} =33V; I ₀ =1.0A		8.0	mA
\triangle_{b1}	Quiescent Current Change	5.0mA≤I₀≤1.0A;Vin=33V		0.5	mA
∆ _{b2}	Quiescent Current Change	28V≪V _{in} ≪38V; I₀=500mA		1.0	mA

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