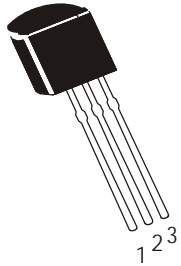


VOLTAGE REGULATOR



Lead 1.Output
2.Ground
3.Input

LM78L12

**TO-92
Plastic Package**

The voltages available allow these Regulators to be used in Logic Systems, Instrumentation, Hi-Fi Audio Circuits and other Solid State Electronic Equipment

ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ\text{C}$)

DESCRIPTION	SYMBOL	VALUE	UNIT
Input Voltage	V_{IN}	35	V
Power Dissipation	P_D	625	mW
Junction to Ambient in free air	T_{amb}	0 to +150	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	- 55 to +150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_a=25^\circ\text{C}$ unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Output Voltage	V_O	$V_I=19\text{V}, I_O=40\text{mA}$	11.50		12.50	V
		$I_O=1\text{mA to }40\text{mA}, 0^\circ\text{C to }125^\circ\text{C}$	11.40		12.60	V
		$V_I=14\text{V to }27\text{V}, 0^\circ\text{C to }125^\circ\text{C}$	11.40		12.60	V
Ripple Rejection	R_R	$V_I=15\text{V to }25\text{V}, f=120\text{Hz}$	37			dB
Line Regulation	R_{BGIN}	$V_I=14.5\text{V to }27\text{V}, I_O=40\text{mA}$			250	mV
		$V_I=16\text{ to }27\text{V}$			200	mV
Load Regulation	R_{BGL}	$I_O=1\text{mA to }100\text{mA}, V_I=19\text{V}$			100	mV
		$I_O=1\text{mA to }40\text{mA}$			50	mV
Output Noise Voltage	V_{NO}	$f=10\text{Hz to }100\text{KHz}$		70		μV
Dropout Voltage	$V_{DIF (min)}$	$V_I=19\text{V}, I_O=40\text{mA}$		1.7		V
Quiescent Current	I_Q	$V_I=19\text{V}, I_O=40\text{mA}, T_a=25^\circ\text{C}$			6.5	mA
		$V_I=19\text{V}, I_O=40\text{mA}, T_a=125^\circ\text{C}$			6.0	mA
Quiescent Current Change	ΔI_{QIN}	$V_I=16\text{V to }27\text{V}, I_O=40\text{mA}, T_a=0^\circ\text{C to }125^\circ\text{C}$			1.5	mA
	ΔI_{QL}	$I_O=1\text{mA to }40\text{mA}, V_I=19\text{V}, T_a=0^\circ\text{C to }125^\circ\text{C}$			0.1	mA

LM78L12Rev060905D

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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Continental Device India Limited

C-120 Naraina Industrial Area, New Delhi 110 028, India.

Telephone + 91-11-2579 6150, 5141 1112 Fax + 91-11-2579 5290, 5141 1119

email@cdil.com www.cdilsemi.com