

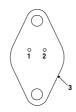
LM120A-05 LM120-05 LM7905A LM7905

1.5 AMP **NEGATIVE VOLTAGE REGULATOR**

0

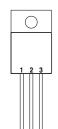
Pin 1 - Ground Pin 2 – V_{OUT} Case - V_{IN}

K Package - TO-3



Pin 1 – Ground Pin 2 – V_{OUT} Case - V_{IN}

R Package - TO-66



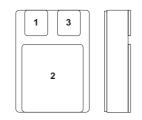
Pin 1 - Ground

Pin $2 - V_{IN}$

Pin 3 – V_{OUT}

Case - V_{IN}

G Package - TO-257 IG Package- TO-257* * isolated Case on IG package



Pin 1 - Ground Pin 2 – V_{IN}

Pin 3 – V_{OUT}

SMD Package - SMD1 Ceramic Surface Mount

FEATURES

- OUTPUT VOLTAGE OF -5V
- 0.7% / V LINE REGULATION AVAILABLE
- 0.5% / A LOAD REGULATION AVAILABLE
- THERMAL OVERLOAD PROTECTION
- SHORT CIRCUIT PROTECTION
- OUTPUT TRANSISTOR SOA PROTECTION
- 1% VOLTAGE TOLERANCE OPTION (-A VERSIONS)

DESCRIPTION

The A suffix devices provide 0.7% / V line regulation, 0.5% / A load regulation and ±1% output voltage tolerance at room temperature.

Protection features include Safe Operating Area current limiting and thermal shutdown.

ABSOLUTE MAXIMUM RATINGS (T_{case} = 25°C unless otherwise stated)

$\overline{V_{I}}$	DC Input Voltage	35V
P_{D}	Power Dissipation	Internally limited
T_j	Operating Junction Temperature Range	−55 to 150°C
T _{stg}	Storage Temperature	−65 to 150°C

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LM120A-05 LM120-05 LM7905A LM7905

			LM7905A LM120A-05		LM7905 , LM120-05 LM120-05						
Parameter		Test Conditions		Min.	Тур.	Max.	Min.	Тур.	Max.	Units	
	Output Voltage	I _O = 500mA	V _{IN} = -10V	-4.95	-5	-5.05	-4.9	-5	-5.1		
Vo		$I_O = 5$ mA to I_{MAX}	$V_{IN} = -7.5V \text{ to } -20V$				4.0		- F 0	v	
		$P_D \le P_{MAX}$	$T_J = -55 \text{ to } 150^{\circ}\text{C}$	-4.85		-5.15	-4.8		-5.2		
ΔV _O		I _O = 0.5 I _{MAX}	$V_{IN} = -7V \text{ to } -25V$		3	10		3	25	mV	
			$V_{IN} = -7.5V \text{ to } -20V$		3	10		3	50		
	Line Regulation		$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$	3	3						
		V _{IN} = -8V to -12V			1	4		1	25	1	
		I _O ≤ I _{MAX}	$T_{\rm J} = -55 \text{ to } 150^{\circ}\text{C}$		1	12		2	50	1	
ΔV _O	Load Regulation		$I_{O} = 5 \text{mA to } 1.5 \text{A}$		25	35		25	100		
		V _{IN} = -10V	$I_O = 5$ mA to I_{MAX}	25		35	25	25	100	mV	
			$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$					23			
IQ	Quiescent Current	I _O ≤ 0.5 I _{MAX}			1	1.9		1	1.9		
	Quiescent Ourient	V _{IN} = -10V	$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$		1	2		1	2	- mA	
ΔI_{Q}	Quiescent Current	$I_O = 5$ mA to I_{MAX}			0.2	0.4		0.2	0.4	А	
∆iQ	Change	V _{IN} = -10V	$T_{\rm J} = -55 \text{ to } 150^{\circ}\text{C}$		0.2	0.5		0.2	0.5	mA	
V _N	Output Noise	f = 10Hz to $100kHzV_{IN} = -10V$		100	100			100		μV	
* IN	Voltage			100			100		μν		
ΔV_{IN}	Ripple Rejection	f = 120Hz	$I_{O} \leq I_{MAX}$	58			54			dB	
$\frac{\Delta V_{\text{IN}}}{\Delta V_{\text{O}}}$		$V_{IN} = -8V \text{ to } -18V$	$I_O \le 0.5 I_{MAX}$	50		5	E 1	54			
740			$T_{J} = -55 \text{ to } 150^{\circ}\text{C}$	58			54				
	Dropout Voltage	$I_{O} = I_{MAX}$			1.4			1.4		V	
R _O	Output Resistance	f = 1 kHz			5			5		mΩ	
I _{sc}	Short Circuit	V _{IN} = -35V			0.6	1.2		0.6	1.2		
	Current				0.0	1.2		0.6	1.2	A	
I _{pk}	Peak Output	V _{IN} = -10V		2.4	2.4	2.4 3.3	2.4	2.4	3.3	^	
	Current Average				2.4	3.3		2.4	3.3		
Temperature		_ 5m^			0.2			0.2		mV	
Coefficient of V _O		I _O = 5mA			0.2			0.2			
Input Voltage required to		 a <		-7.3			-7.3			V	
maintain line regulation		I _O ≤ I _{MAX}		-7.3			-1.3			\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	

- 1) All characteristics are measured with a capacitor across the input of 0.22μF and a capacitor across the output of 0.1μF. All characteristics except noise voltage and ripple rejection ratio are measured using pulse techniques ($t_p \le 10$ ms, $\delta \le 5$ %). Output voltage changes due to changes in internal temperature must be taken into account separately.
- 2) Test Conditions unless otherwise stated: $P_{MAX} = 10W$ for SMD , $P_{MAX} = 20W$ for all other package devices

$$I_{MAX} = 1.0A$$
, $T_{J} = 25^{\circ}C$

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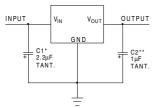
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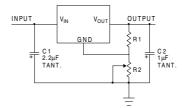
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APPLICATIONS INFORMATION



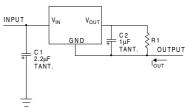
Fixed Output Regulator

- * Required if the regulator is located far from the power supply.
- ** Required for stability. $25\mu F$ electrolytic may be substituted.



Adjustable Output Regulator

$$V_{OUT} \approx V_{REG} \frac{(R1+R2)}{R1}$$



Current Regulator

$$I_{OUT} = \frac{V_{REG}}{R1} + I_{Q}$$

Order Information

Part	K-Pack	R-Pack	G/IG-Pack	SMD-Pack	Temp.	Note:
Number	(TO-3)	(TO-66)	(TO-257)	SMD1	Range	To order, add the
LM7905A	~	V	V	~	-55 to +150°C	package identifier to the
LM7905	~	✓	~	~	"	part number.
LM120A-05	/	V	V	~	"	eg. LM7905AK
LM120-05	>	✓	✓	~	"	LM120SMD-05

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