NAN YA PLASTICS CORPORATION

SPECIFICATION OF

LCD MODULE

PRODUCT NO.: LMBGANA32S49CKS

SPEC. NO.: LMA32-49A-△0

CUSTOMER
APPROVED BY
DATE:

LCD DEPARTMENT
ELECTRONIC MATERIALS DIVISION
NAN YA PLASTICS CORPORATION
201, TUNG HWA N. ROAD, TAIPEI
TEL: 886-2-27122211 EXT. 5993~5995
FAX: 886-2-27178253
E-mail: lcdsales@npc.com.tw

EDITED ON : AUGUST.23.2007

Q.C. DEPT.	DESIGN MANAGER	DESIGN CHECK	DESIGNER
			C.Y.CHAN

RECORDS OF REVISION

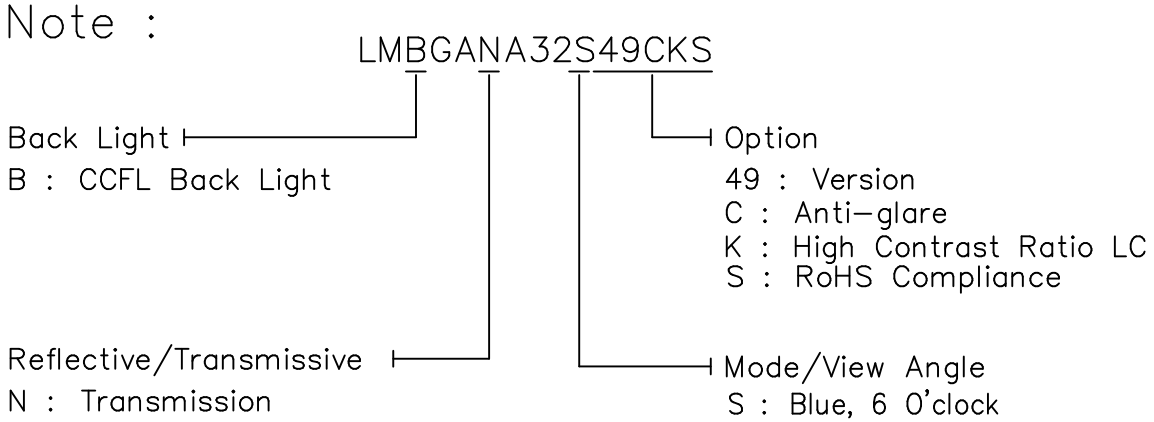
SPEC. NO.: LMA32-49A

DATE	REVISED NO.	REF. PAGE	SUMMARY	DESIGN	CHECK
08.23.2007	0	1/25~25/25	First Issue	C.Y.CHAN	

1. MECHANICAL DATA

NO	ITEM	CONTENTS	UNIT
1	Product No.	LMBGANA32S49CKS	–
2	Module Size	167.1 (W) x 109 (H) x Max 11.0 (D)	mm
3	Dot Size	0.33 (W) x 0.33 (H)	mm
4	Dot Pitch	0.36 (W) x 0.36 (H)	mm
5	Number of Dots	320 (W) x 240 (H)	Dot
6	Duty	1/240	–
7	LCD Display Mode	Blue Mode	–
8	Rear Polarizer	Transmission	–
9	Viewing Direction	6	O'clock
10	Backlight	CCFL	–
11	Controller	S1D13305F00A100 OR COMPATIBLE	–
12	DC/DC Converter	Included	–
13	Touch Panel	Excluded	–
14	Weight	200 (Approx.)	g

Note :



RoHS Compliance. Nan Ya guarantees that this project doesn't include any materials (6 materials) or includes less than specified quantities which are regulated by RoHS Compliance.

2. ABSOLUTE MAXIMUM RATINGS

(1) ELECTRICAL ABSOLUTE RATINGS

VSS=0V

ITEM	SYMBOL	MIN	MAX	UNIT	COMMENT
Power Supply for Logic	VDD-VSS	-0.3	7.0	V	
Input Voltage	VI	-0.3	VDD+0.3	V	
Static Electricity	-	-	-	-	Note 1

Note 1 LCM should be grounded during handling LCM.

(2) ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	WIDE TEMP.			
	OPERATING		STORAGE	
	MIN.	MAX.	MIN.	MAX.
Ambient Temperature	-20	70	-40	80
Humidity (Without Condensation)	Note 2,4		Note 3,4	

Note 2 Ta ≦ 70°C : 75%RH max

Note 3 Please refer to item of reliability test

Note 4 Background color will change slightly depending on ambient temperature. That phenomenon is reversible.

3. ELECTRICAL CHARACTERISTICS

3-1. ELECTRICAL CHARACTERISTICS OF LCM

ITEM	SYMBOL	CONDITION		MIN.	TYP.	MAX.	UNIT
Power Supply for Logic	VDD-VSS	-		4.75	5.0	5.25	V
Recommended LC Driving Voltage	VDD-VO	Duty=1/240	-20°C	25.3	25.7	26.1	V
			0°C	23.7	24.1	24.5	
			25°C	22.8	23.2	23.6	
			50°C	21.4	21.8	22.2	
			70°C	20.4	20.8	21.2	
Input Voltage	VIH	H level		0.8VDD	-	VDD	V
	VIL	L level		0	-	0.2VDD	V
Power Supply Current	IDD	Ta=25°C VDD=5.0V VDD-VO=23.2V PATTERN : □ ■ □ ■ □ ■ ■ □ ■ □ ■ □		-	70	110	mA
Surface Luminance of LCM	L	Ta=25°C (Lamp current : 5mA) (Dots All On)		120	160	-	cd/m²
		Ta=25°C (Lamp current : 5mA) (Dots All Off)		-	30	-	

3-2.ELECTRICAL CHARACTERISTICS OF BACKLIGHT

Used Lamp Rating Temp.=25°C

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	REMARK
Lamp Voltage	V_L	–	260	–	Vrms	–
Lamp current	I_L	4	5	6	mArms	–
Lamp power consumption	P_L	–	1.3	–	W	(*1)
Starting voltage	Vs	–	–	420	Vrms	Ta=25°C
Lamp life time	L_L	–	20000	–	hrs	at I_L = 5 mArms Ta=25°C (*2)

(*1) Power consumption excluded inverter loss .

(*2) Lamp life time is defined as follows : The final brightness is at 50% of original brightness .

(*3) a. Please follow the table of Lamp Characteristics shown above if not to use the inverter recommended by Nan Ya .

b. If customers want to design inverter by themselves , please inform Nan Ya to offer the detail lamp specification .

3-3.ELECTRICAL CHARACTERISTICS OF TESTED INVERTER

TDK CXA-L10L

If the inverter output "CN2" couldn't mating CCFL connector , please refer to specification "INTERNAL PIN CONNECTION" page to fit it.

3-3-1 GENERAL SPECIFICATIONS

OPERATING TEMPERATURE : -10°C~60°C
STORAGE TEMPERATURE : -20°C~85°C
DIMENSION : 44.0(L)mm x 21.0(W)mm x MAX 18.0(H)mm

3-3-2 PIN ASSIGNMENTS

INPUT (CN1) CONNECTOR :

NO.	FUNCTION
1	VIN
2	GND

OUTPUT (CN2) CONNECTOR :

NO.	FUNCTION
3	OUT1
4	OUT2
5	OUT GND

3-3-3 RELATIONSHIP BETWEEN VIN & TUBE CURRENT

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	REMARK
Input Voltage	VIN	-	10.1	-	V	
Tube Current	IL	-	5	-	mA	

4. OPTICAL CHARACTERISTICS

AT V_{OP}

ITEM / MODE		Cr(Contrast Ratio)										θ(Viewing Angle)		ø(Viewing Angle)	
		-20℃		0℃		25℃		50℃		70℃		25℃		25℃	
		MIN.	TYP.	MIN.	TYP.	MIN.	TYP.	MIN.	TYP.	MIN.	TYP.	MIN.	TYP.	MIN.	TYP.
N	S	3.0	4.0	3.5	5.0	3.5	5.0	3.0	4.5	2.0	3.0	–	(F)35 (R)25	–	(L)30 (R)30
NOTE		NOTE 3,6										NOTE 3,5			

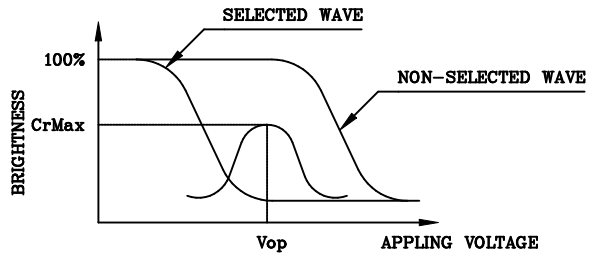
(NOTE) N : TRANSMISSION
S : Blue Mode

AT ø=0° θ=0°

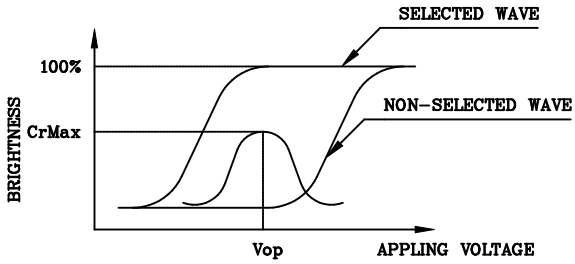
ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
Response Time (rise)	Tr	-20℃	3200	4000	6000	ms	NOTE 2,3
		0℃	400	500	750		
		25℃	200	250	375		
		50℃	80	100	150		
		70℃	65	80	120		
Response Time (fall)	Tf	-20℃	1600	2000	3000	ms	NOTE 2,3
		0℃	360	450	680		
		25℃	160	200	300		
		50℃	50	60	90		
		70℃	40	50	75		

(NOTE 1)

Definition of Operation Voltage(Vop)



(positive type)



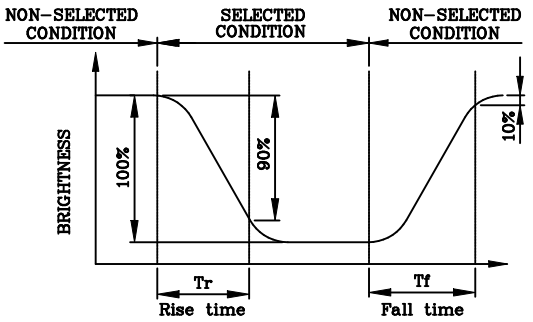
(negative type)

*Conditions

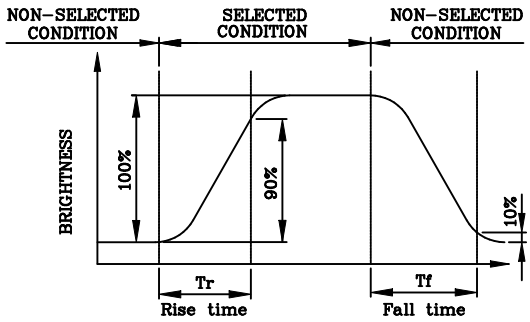
Viewing Angle : 0
Frame Frequency : 70Hz
Applying Waveform : 1/N duty 1/a bias

(NOTE 2)

Definition of Response Time(Tr,Tf)



(positive type)



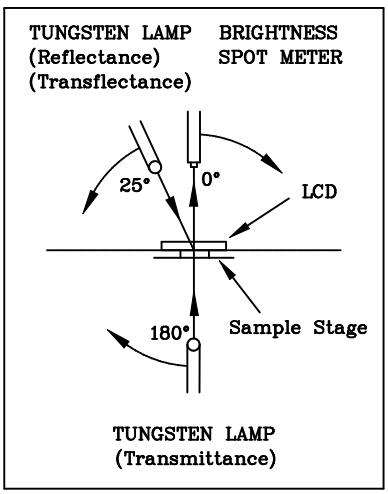
(negative type)

*Conditions

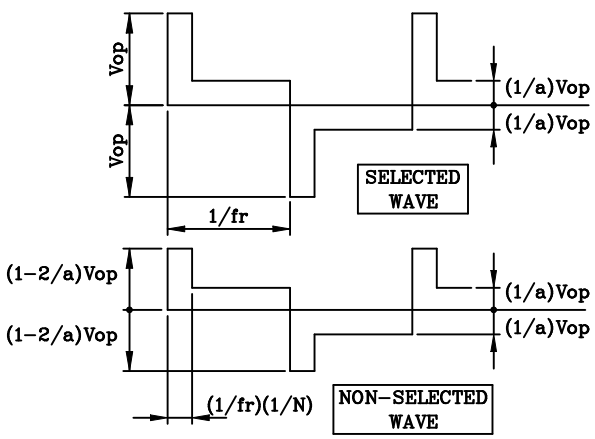
Operating Voltage : Vop
Viewing Angle (θ ,ø) : (0,0)
Frame Frequency : 70Hz
Applying Waveform : 1/N duty 1/a bias

(NOTE 3)

Description of Measuring Equipment and Driving Waveforms

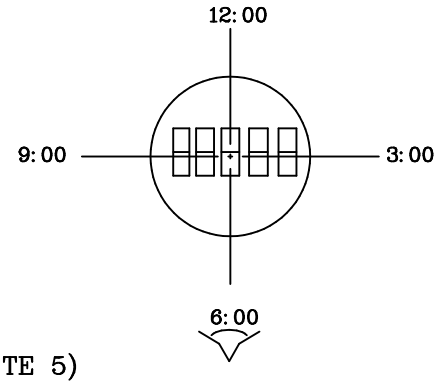


Multiplex Driving (1/N duty 1/a bias)



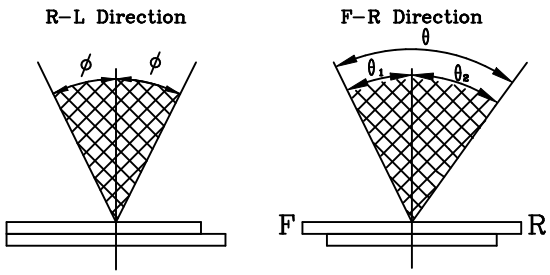
(NOTE 4)

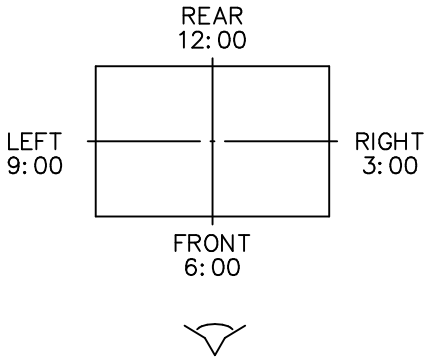
Definition of Viewing Direction



(NOTE 5)

Definition of Viewing Angle





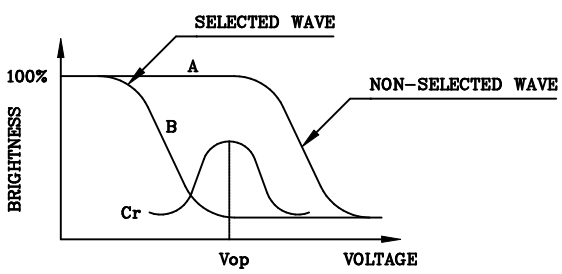
$\theta = \theta_1 + \theta_2$

*Conditions

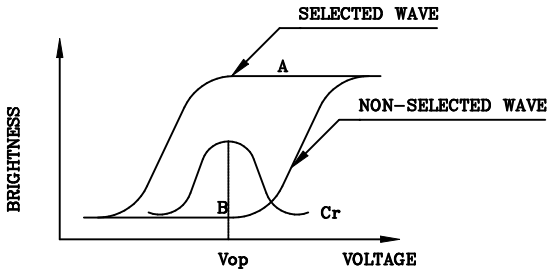
Operating Voltage : Vop
Frame Frequency : 70Hz
Applying Waveform : 1/N duty 1/a bias
Contrast Ratio : larger than 2

(NOTE 6)

Definition of Contrast Ratio (Cr)



(positive type)



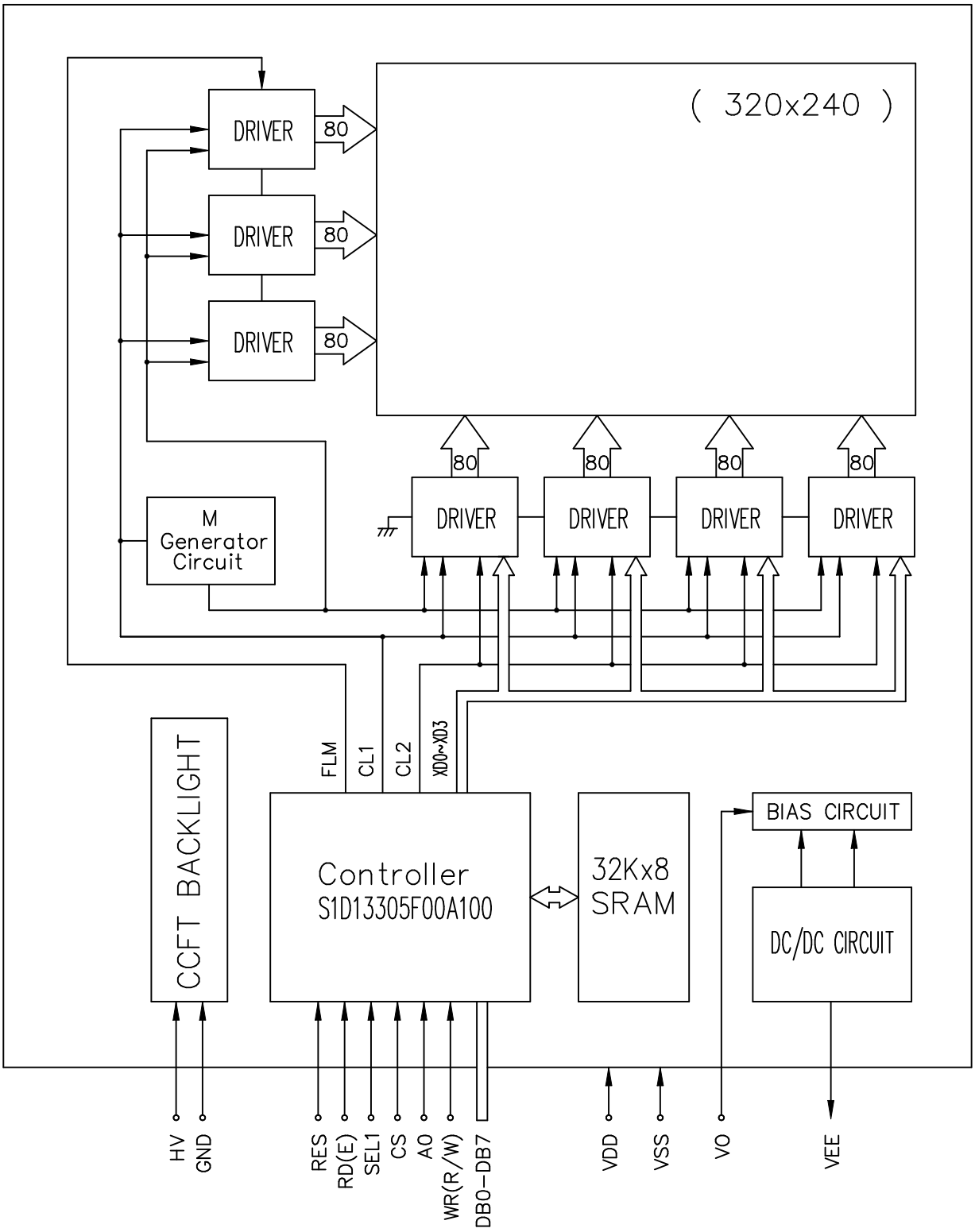
(negative type)

Contrast Ratio : Cr=A/B

*Conditions

Viewing Angle : 0
Frame Frequency : 70Hz
Applying Waveform : 1/N duty 1/a bias

5. BLOCK DIAGRAM



6. INTERNAL PIN CONNECTION

CN1: Pitch 1.0mm

Pin No.	Symbol	Function
1	VSS	Ground
2	VDD	Power supply for Logic
3	V0	Negative voltage power supply (Tuned from VDD-VEE)
4	A0	Data type select
5	WR (R/W)	8080 Family : Write signal 6800 Family : R/W signal
6	RD(E)	8080 Family : Read signal 6800 Family : Enable clock
7 S 14	DB0 S DB7	3-State I/O data bus
15	CS	Chip select
16	RES	This active Low input performs hardware reset on the S1D13305F00A100
17	VEE	Supply voltage for LCD panel (Generated from internal DC/DC converter)
18	SEL1	'0' FOR 8080 Family MPU , '1' for 6080 family MPU
19 S 22	N.C.	No connection

MATING CONNECTOR: ELCO 00-6200-227-022-800+

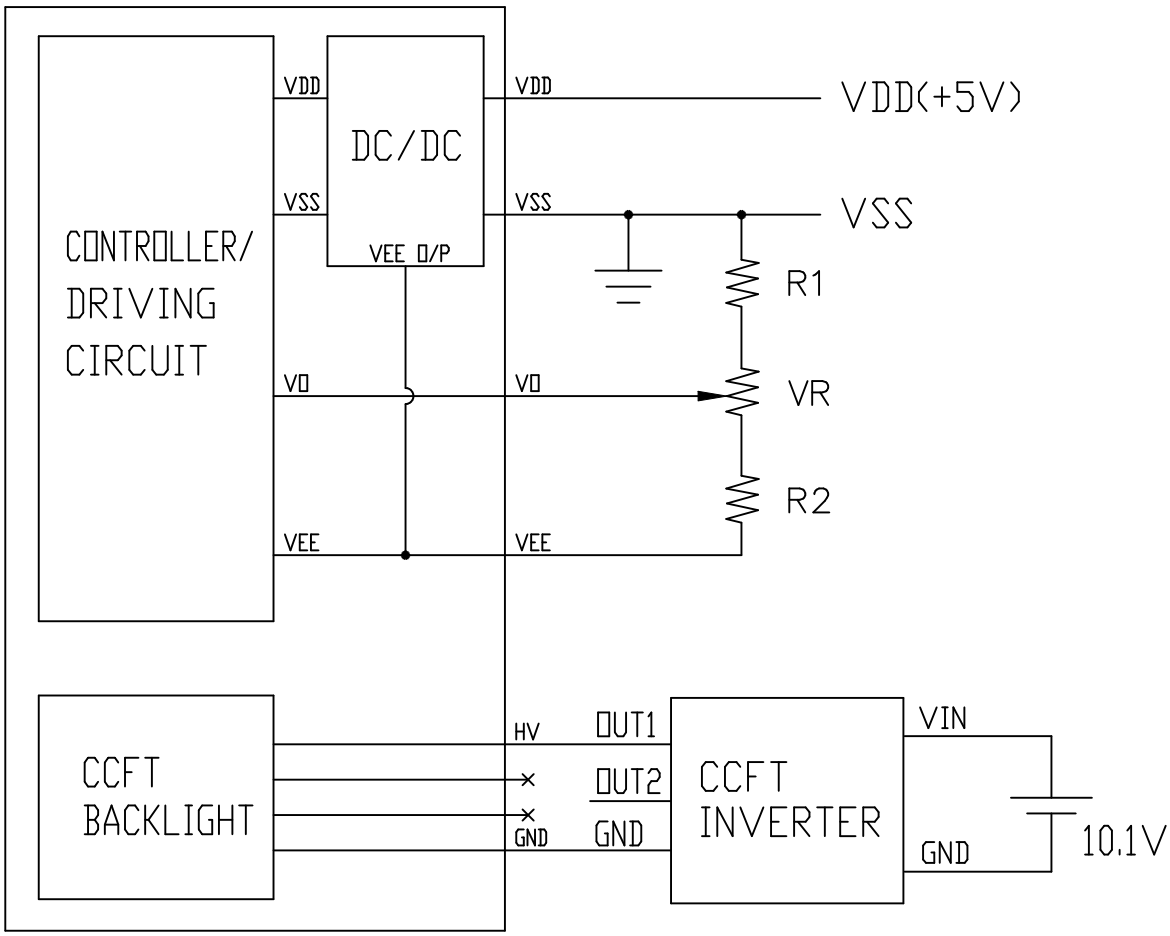
CN2 : J.A.E./IL-G-4S-S3C2 (or compatible)

Pin No.	Symbol	Level	Function
1	HV	-	Power supply for CCFT
2	NC	-	-
3	NC	-	-
4	GND	-	Ground line (from inverter)

Recommended Mating Connector: J.A.E./IL-G-4P-S3T2-SA
or J.A.E./IL-G-4P-S3L2-SA

7. POWER SUPPLY

LCD MODULE



(NOTE)

1. R1+VR+R2 ≒ 20KΩ
2. TESTED CCFT INVERTER : TDK CXA-L10L
   (OPERATING TEMP. -10℃~60℃)

8. TIMING CHARACTERISTICS

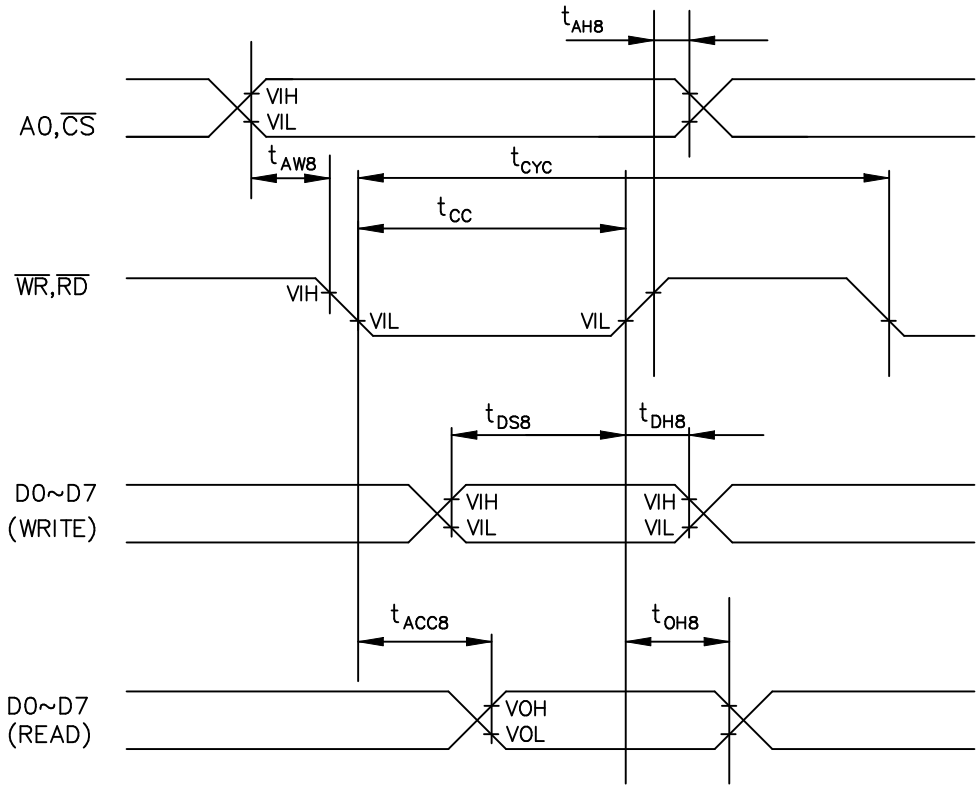
8-1.READ/WRITE CHARACTERISTICS(8080 FAMILY MPU)

VDD=5.0V±5%

ITEM	ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
A0,$\overline{CS}$	ADDRESS HOLD TIME	t_{AH8}	10	–	–	ns
	ADDRESS SETUP TIME	t_{AW8}	0	–	–	ns
$\overline{WR}$,$\overline{RD}$	SYSTEM CYCLE TIME	t_{CYC8}	(Note)	–	–	ns
	STROBE PULSE WIDTH	t_{CC}	120	–	–	ns
D0 to D7	DATA HOLD TIME	t_{DH8}	5	–	–	ns
	DATA SETUP TIME	t_{DS8}	120	–	–	ns
	$\overline{RD}$ ACCESS TIME	t_{ACC8}	–	–	50	ns
	OUTPUT DISABLE TIME	t_{OH8}	10	–	50	ns

Note: (1) For memory control and system control commands:
tCYC8 = 2tC + tCC + tCEA + 75 > tACV + 245

(2) For all other commands:
tCYC8 = 4tC + tCC + 30



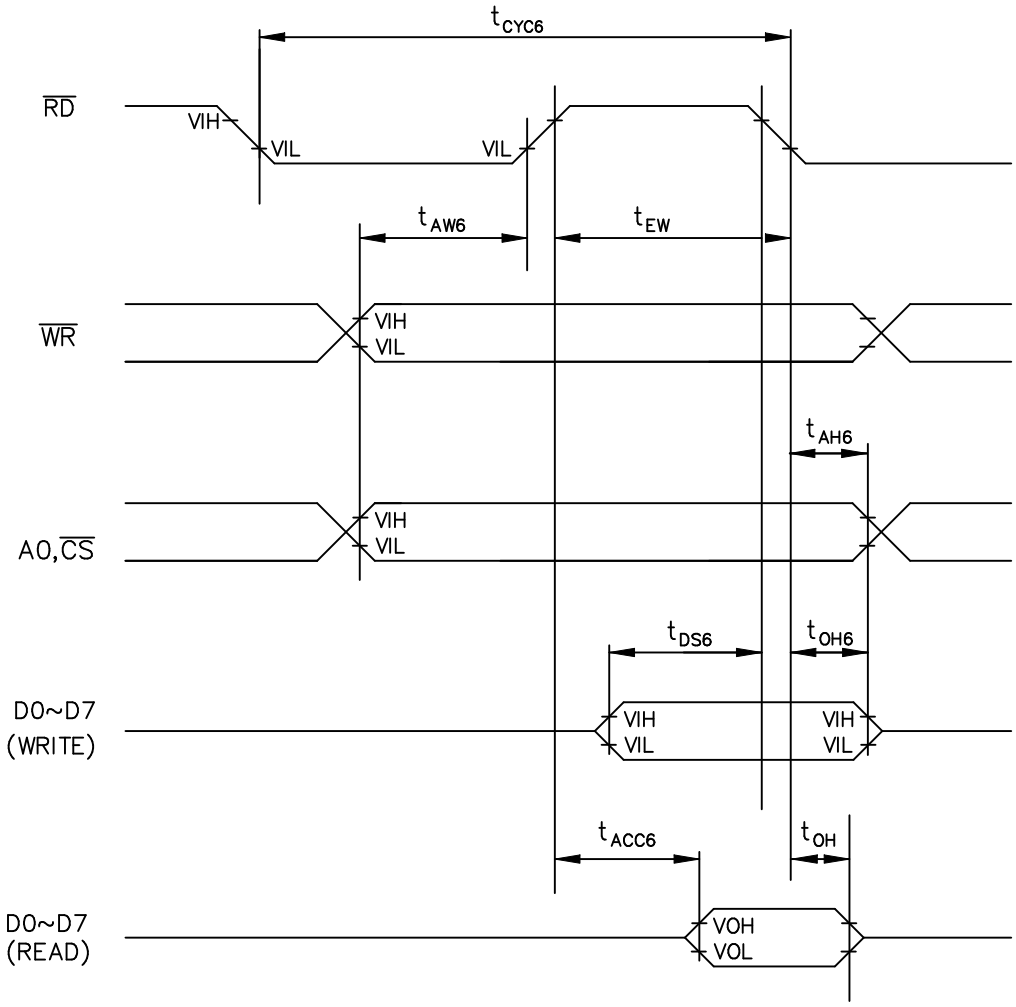
8-2.READ/WRITE CHARACTERISTICS(6800 FAMILY MPU)

VDD=5.0V±5%

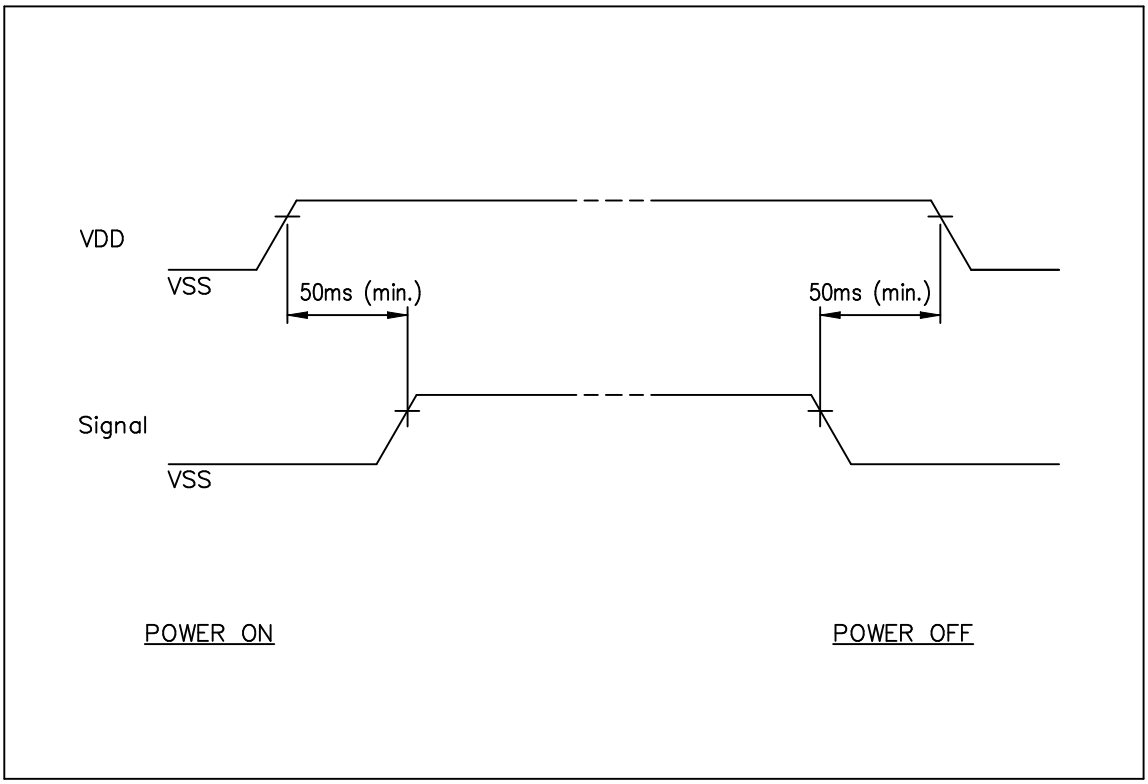
ITEM	ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
A0,$\overline{CS}$,$\overline{WR}$	ADDRESS HOLD TIME	t_{AH6}	0	–	–	ns
	ADDRESS SETUP TIME	t_{AW6}	0	–	–	ns
	SYSTEM CYCLE TIME	t_{CYC6}	(Note)	–	–	ns
D0 to D7	DATA HOLD TIME	t_{DH6}	0	–	–	ns
	DATA SETUP TIME	t_{DS6}	100	–	–	ns
	ACCESS TIME	t_{ACC6}	–	–	85	ns
	OUTPUT DISABLE TIME	t_{OH6}	10	–	50	ns
$\overline{RD}$	ENABLE PULSE WIDTH	t_{RDW}	120	–	50	ns

Note: (1) For memory control and system control commands:
tCYC6 = 2tC + tEW + tCEA + 75 > tACV + 245

(2) For all other commands:
tCYC6 = 4tC + tEW + 30



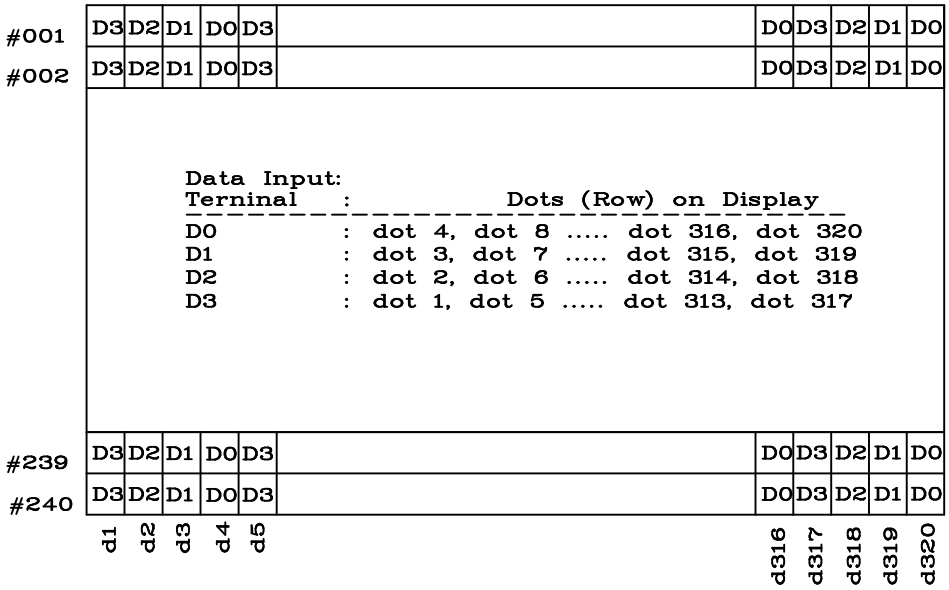
8-3.POWER ON/OFF TIMING



(Note)

The missing pixels may occur when the LCM is driven beyond above power interface timing sequence.

8.4 DISPLAY PATTERN



9. RELIABILITY TEST

WIDE TEMPERATURE RELIABILITY TEST

NO	ITEM	CONDITION			STANDARD	NOTE
1	High Temp. Storage	80°C	120Hrs		Appearance without defect	
2	Low Temp. Storage	−40°C	120Hrs		Appearance without defect	
3	High Temp. & High Humi. Storage	60°C 90%RH	120Hrs		Appearance without defect	
4	High Temp. Operating Display	70°C	120Hrs		Appearance without defect	
5	Low Temp. Operating Display	−20°C	120Hrs		Appearance without defect	
6	Thermal Shock	−20°C,30min → 70°C,30min ↑ (1cycle)			Appearance without defect	10 cycles

Inspection Provision

1.Purpose

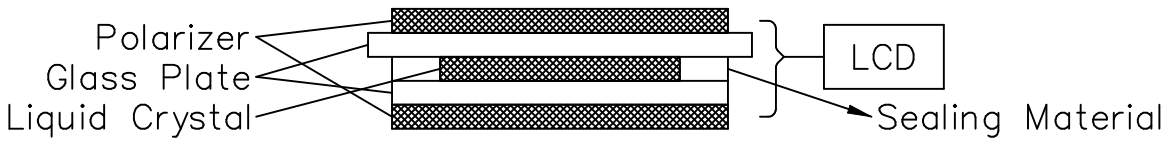
The NAN YA inspection provision provides outgoing inspection provision and its expected quality level based on our outgoing inspection of NAN YA LCD produces.

2.Applicable Scope

The NAN YA inspection provision is applicable to the arrangement in regard to outgoing inspection and quality assurance after outgoing.

3.Technical Terms

3-1 NAN YA Technical Terms



4.Outgoing Inspection

4-1 Inspection Method

MIL-STD-105E Level Ⅱ Regular inspection

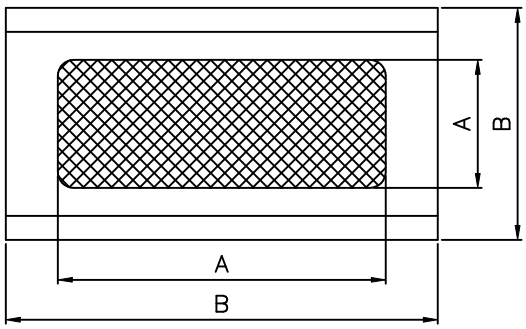
4-2 Inspection Standard

	Item		AQL(%)	Remarks
Major Defect	Dots	Opens Shorts Erroneous operation	0.4	faults which substantially lower the practicality and the initial purpose difficult to achieve.
	Solder appearance	Shorts Loose		
	Cracks	Display surface cracks		

	Dimensions	External from Dimensions	0.4	
Minor Defect	Inside the glass	Black spots	0.65	faults which appear to pose almost no obstacle to the practicality, effective use, and operation.
	Polarizing plate	Scratches, foreign Matter, air bubbles, and peeling		
	Dots	Pinhole, deformation		
	Color tone	Color unevenness		
	Solder appearance	Cold solder Solder projections		

4-3 Inspection Provisions
*Viewing Area Definition

Fig. 1



A : Zone Viewing Area
B : Zone Glass Plate Outline

*Inspection place to be 500 to 1000 lux illuminance uniformly without glaring.
The distance between luminous source(daylight fluorescent lamp and cool white fluorescent lamp) and sample to be 30cm to 50cm.

*Test and measurement are performed under the following conditions, unless otherwise specified.

Temperature	20± 15°C
Humidity	65± 20%R.H.
Pressure	860~1060hPa(mmbar)

In case of doubtful judgment, it is performed under the following conditions.

Temperature	20± 2°C
Humidity	65± 5%R.H.
Pressure	860~1060hPa(mmbar)

5. Specification for quality check

5-1 Electrical characteristics

NO.	Item	Criterion
1	Non operational	Fail
2	Miss operating	Fail
3	Missing dot	Fail
4	Contrast irregular	Fail
5	Response time	Within Specified value
6	Backlight turn on/off	Within Specified value

5-2 External Appearance Defect

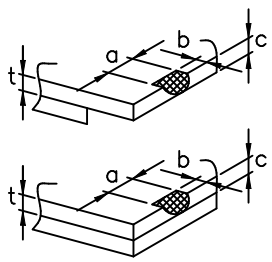
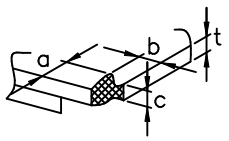
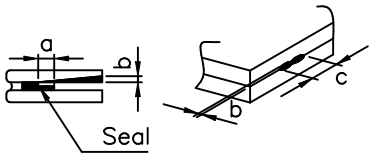
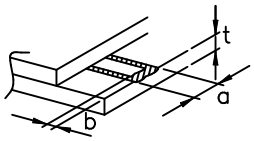
<table>
<tr><th>NO.</th><th>Item</th><th>Criterion</th></tr>
<tr><td>1</td><td>Black spots, foreign matter, and white spots (Including light leakage due to pinholes of polarizing plates, etc.)</td><td>(1)-1-Spots
<table><tr><th>Average Diameter(mm): D</th><th>Number of pieces permitted</th></tr><tr><td>D≦0.1</td><td>Ignore</td></tr><tr><td>0.1<D≦0.2</td><td>5</td></tr><tr><td>0.2<D≦0.3</td><td>2</td></tr><tr><td>0.3<D</td><td>0</td></tr></table>Number of total pieces is set to within 5 pieces.

Note that when there are 2 pieces or more, they are not to be concentrated. Set as: Average diameter = (Long diameter + Short diameter)/2

(1)-2-Blurred Spots(At lighting condition)
<table><tr><th>Average Diameter(mm): D</th><th>Number of pieces permitted</th></tr><tr><td>D≦0.3</td><td>Ignore</td></tr><tr><td>0.3<D≦0.75</td><td>5</td></tr><tr><td>0.75<D</td><td>0</td></tr></table>Number of total pieces is set to within 5 pieces.

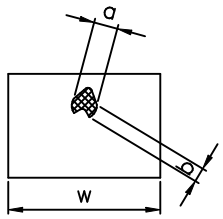
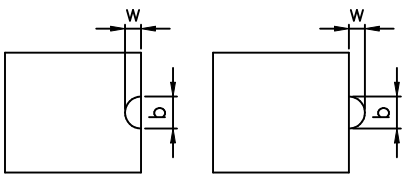
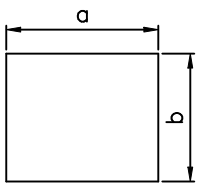
Note that when there are 2 pieces or more, they are not to be concentrated. Set as: Average diameter = (Long diameter + Short diameter)/2</td></tr>
</table>

<table>
<tr><td>1</td><td>Line</td><td>(1)-1 Lines
<table>
<tr><th>Width(mm): W</th><th>Length(mm): L</th><th>Number of pieces permitted</th></tr>
<tr><td>W≦0.03</td><td>Ignore</td><td>Ignore</td></tr>
<tr><td>0.03<W≦0.08</td><td>L≦4</td><td>2</td></tr>
<tr><td>0.08<W≦0.1</td><td>L≦1</td><td>1</td></tr>
</table>
Object exceeding 0.1mm follow the standards of the spots form.
Note that when there are 2 pieces or more, they are not to be concentrated.
(1)-2-Blurred Lines(At lighting condition)
<table>
<tr><th>Width(mm): W</th><th>Length(mm): L</th><th>Number of pieces permitted</th></tr>
<tr><td>W≦0.03</td><td>Ignore</td><td>Ignore</td></tr>
<tr><td>0.03<W≦0.08</td><td>L≦3</td><td>6</td></tr>
<tr><td>0.08<W</td><td>3<L</td><td>None</td></tr>
</table>
Object exceeding 0.1mm follow the standards of the spots form.
Note that when there are 2 pieces or more, they are not to be concentrated.</td></tr>
<tr><td>2</td><td>Scratches(Glass, reflection plates, and polarizing plates)</td><td>In accordance with black spots.
(At non lighting condition)</td></tr>
<tr><td>3</td><td>Color irregular</td><td>Not remarkable color irregular.</td></tr>
</table>

<table>
<tr><td>4</td><td>Air bubbles polarizing plates, and reflection plates</td><td colspan="2">
<table>
<tr><th>Average Diameter (mm): D</th><th>Number of pieces permitted</th></tr>
<tr><td>D≦0.3</td><td>Ignore</td></tr>
<tr><td>0.3<D</td><td>0</td></tr>
</table>
Average diameter = (Long diameter + Short diameter)/2
Note that when there are 4 pieces or more, they are not to be concentrated.
</td></tr>
<tr><td rowspan="5">5</td><td rowspan="5">Cracks</td><td>(1)General crack
 
</td><td>a≦5
b≦2
c≦t
Where, a and b are ignored when less than or equal to 0.5. The numbers of pieces are set at up to 5 pieces.</td></tr>
<tr><td>(2)Corner crack
 
</td><td>a≦2.5
b≦2.5
c≦t
a+b≦4</td></tr>
<tr><td>(3)Seal portion crack
 
</td><td>a≦The seal width x1/3
b≦t x2/3
c≦5
The numbers of pieces are set at up to 5 pieces.</td></tr>
<tr><td>(4)ITO Pin crack
 
</td><td>a≦5
b≦1/3 pin length
c≦t</td></tr>
<tr><td>(5)Progressive cracks</td><td>All taken to be unacceptable.</td></tr>
</table>

6	Outer dimensions	Should be within the tolerance.
7	Newton ring(touch panel)	Orbicular of interference fringes is not allowed in the optimum contrast within the active area under viewing angle.
8	Soldering	Should be no defective soldering such as shorting, loose terminal cold solder, peeling of printed circuit board pattern, improper mounting position, etc.

5-3 Dot Appearance Defect

NO.	Item	Criteria
1	Pinhole	 Dot display a and b are each ≦0.2mm The overall total is taken be with in 10 units. Note that they are not to be concentrated.
2	Missing	 Dot display a and b are each ≦0.2mm The overall total is taken to be with in 10 units.
3	Thick and thin display	 Taken to be within ±1.5% of display character width(a) and height(b).

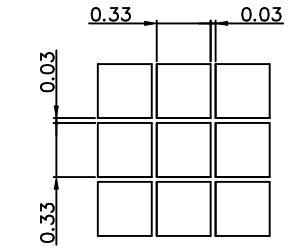
NOTICE:

- SAFETY
  1. If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
  2. If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

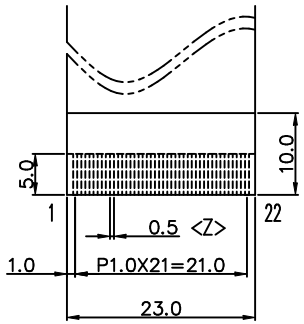
- HANDLING
  1. Avoid static electricity which can damage the CMOS LSI.
  2. Do not remove the panel or frame from the module.
  3. The polarizing plate of the display is very fragile. So, please handle it very carefully.
  4. Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
  5. Do not use ketonics solvent & Aromatic solvent. Use a soft cloth soaked with a cleaning naphtha solvent.

- STORAGE
  1. Store the panel or module in a dark place where the temperature is 25°C±5°C and the humidity is below 65% RH.
  2. Do not place the module near organics solvents or corrosive gases.
  3. Do not crush, shake, or jolt the module.

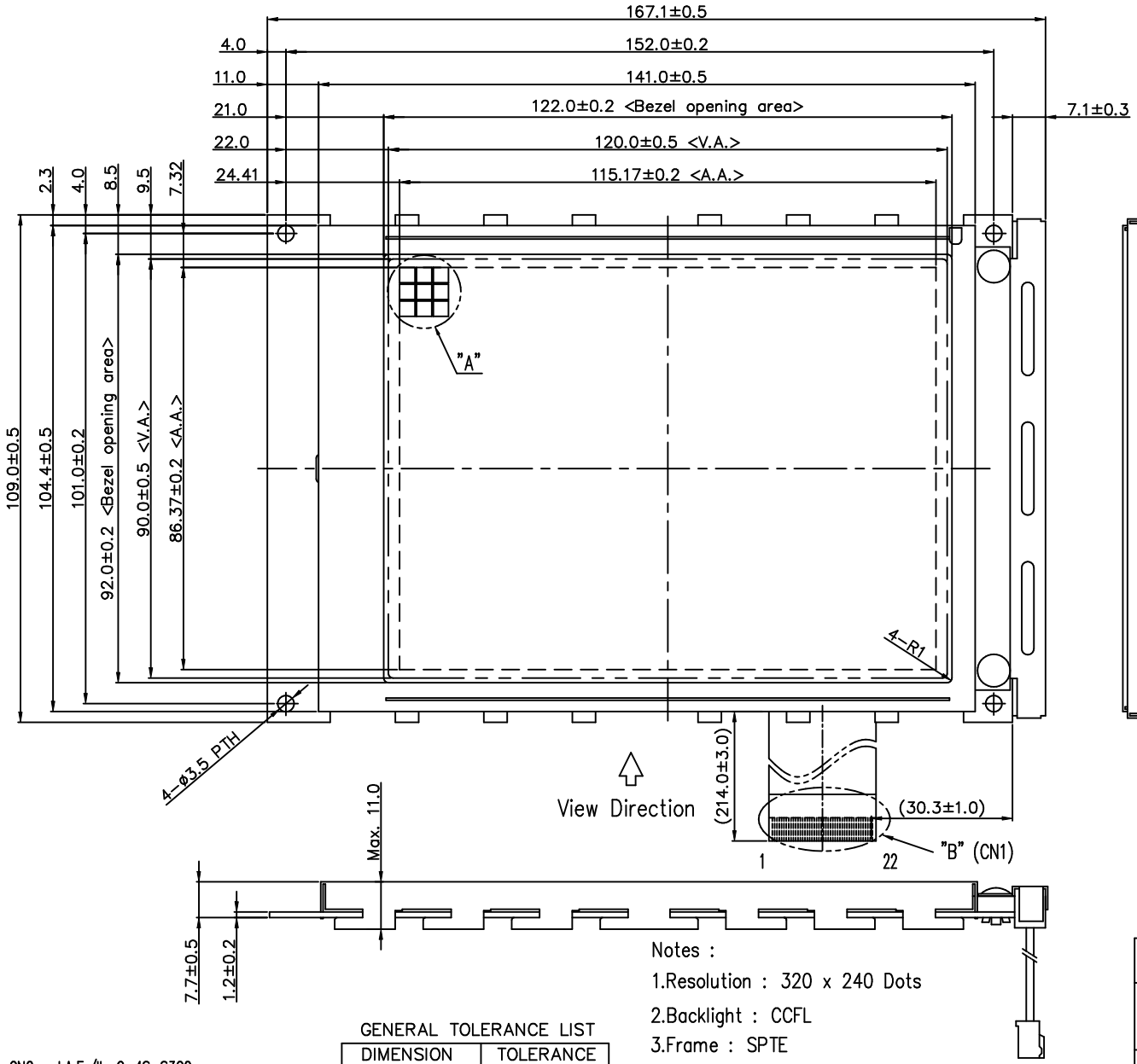
- TERMS OF WARRANT
  1. Acceptance inspection period
     The period is within one month after the arrival of contracted commodity at the buyer's factory site.
  2. Applicable warrant period
     The period is within twelve months since the date of shipping out under normal using and storage conditions.



Detail "A"
(Scale 30:1)



Detail "B"
(Scale 1.5:1)



CN1: FFC, Pitch= 1.0mm

Pin No.	Symbol	Function
1	VSS	Ground
2	VDD	Power supply for Logic
3	V0	Negative voltage power supply
4	A0	Data type select
5	WR (R/W)	8080 Family : Write signal 6800 Family : R/W signal
6	RD(E)	8080 Family : Read signal 6800 Family : Enable clock
7 S 14	DB0 S DB7	3-State I/O data bus
15	CS	Chip select
16	RES	This active Low input performs hardware reset on the S1D13305F00A100
17	VEE	Supply voltage for LCD panel
18	SEL1	'0' FOR 8080 Family MPU , '1' for 6080 family MPU
19 S 22	N.C.	No connection

CN2 : J.A.E./IL-G-4S-S3C2

Pin No.	Symbol	Level	Function
1	HV	–	Power supply for CCFT
2	NC	–	–
3	NC	–	–
4	GND	–	Ground line (from inverter)

GENERAL TOLERANCE LIST

DIMENSION	TOLERANCE
L ≦ 6	±0.25 (mm)
6 < L ≦ 18	±0.3 (mm)
18 < L ≦ 50	±0.4 (mm)
50 < L ≦ 125	±0.5 (mm)
125 < L	±0.6 (mm)
ANGLE	±1° (DEG)

Notes :
1.Resolution : 320 x 240 Dots
2.Backlight : CCFL
3.Frame : SPTE

REV. NO.	DESCRIPTION	DATE	DESIGN	CHECK	APPROVE
5					
4					
3					
2					
1					

南亞塑膠工業股份有限公司
NAN YA PLASTICS CORPORATION

製 品 圖

LMBGANA32S49CKS

	NAME	DATE	THIRD ANGLE P.
APPROVE			
CHECK			
DESIGN	CLOUDE	96.08.13	SCALE: UNIT
DRAWN	CLOUDE	96.08.13	1/1 mm

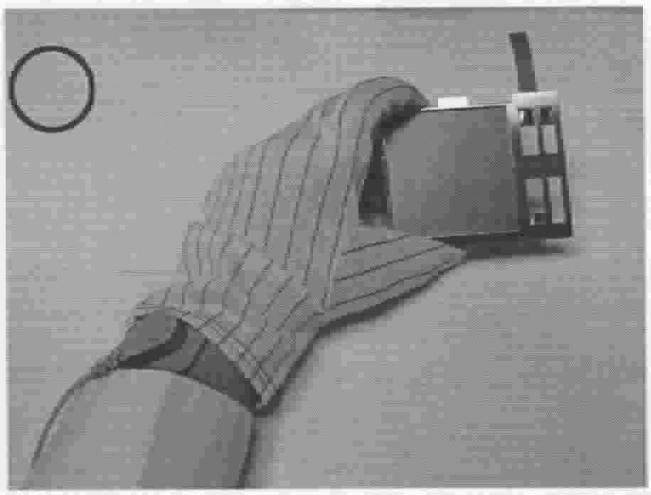
DWG NO. MA32AD49A 1/1 0

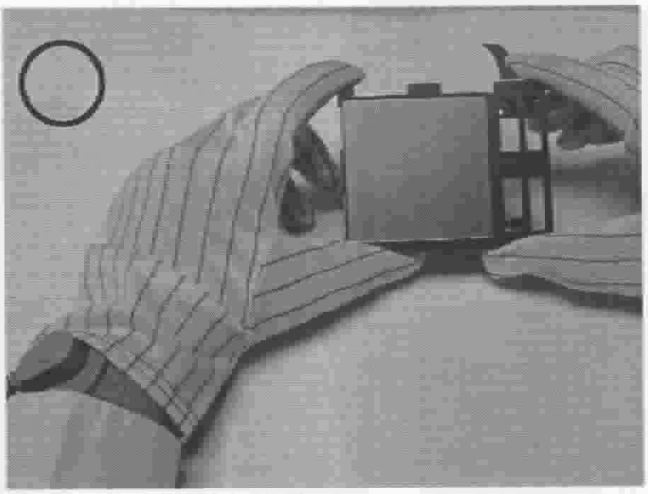
THE NOTES OF LCM USING

LCM is easy to damage.

Please follow the notes as bellows, and be careful of handling!

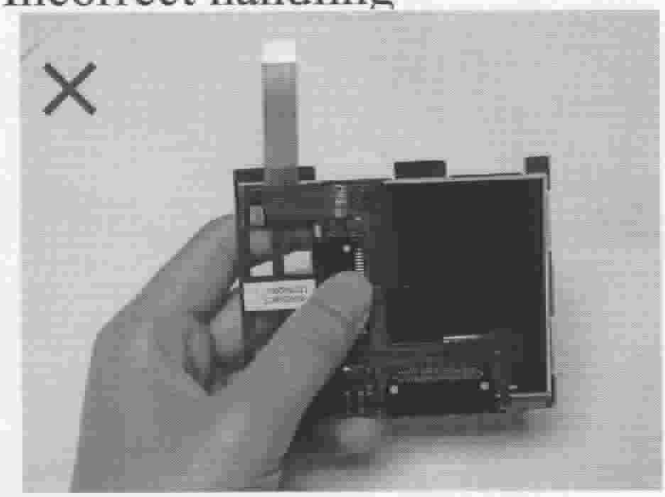
Correct handling



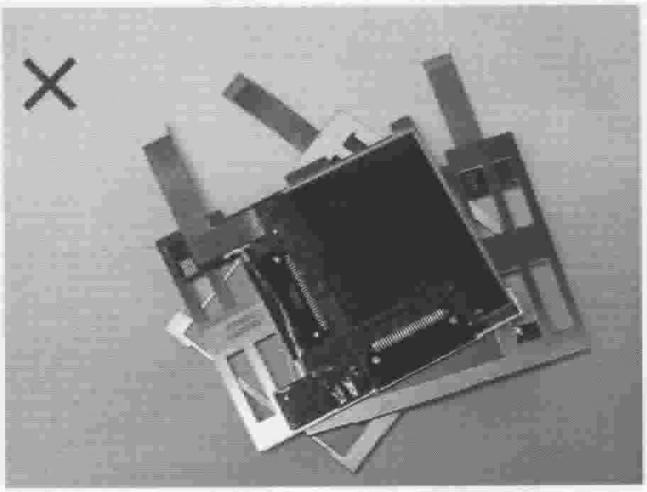


As above picture, please handle with glove by LCM edges and full EOS/ESD protection.

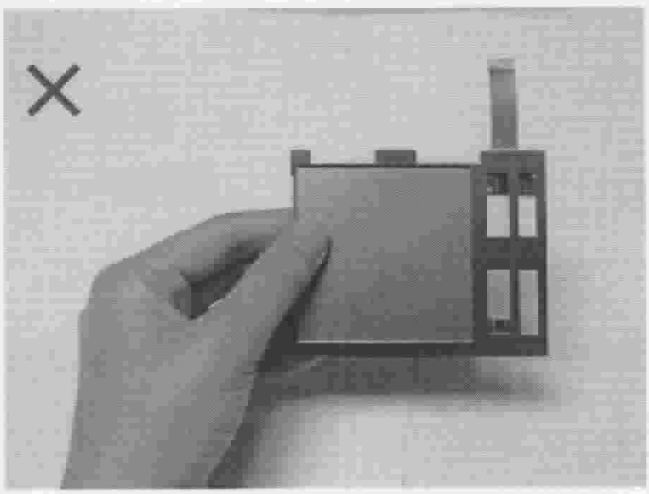
Incorrect handling



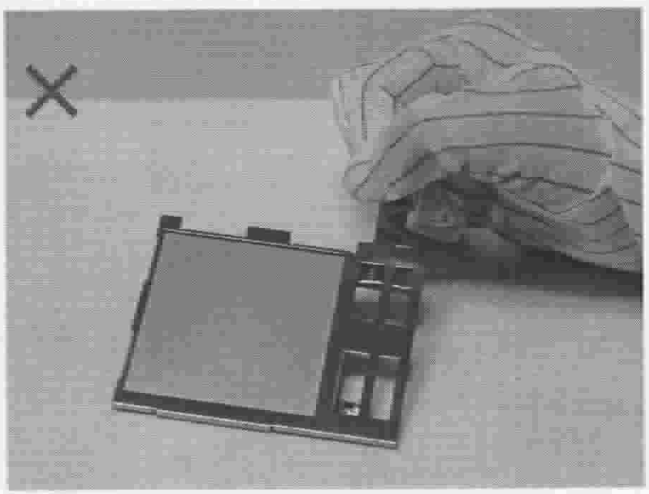
Please don't touch IC directly.



Please don't put one on another LCM.



Please don't hold the surface of LCM.

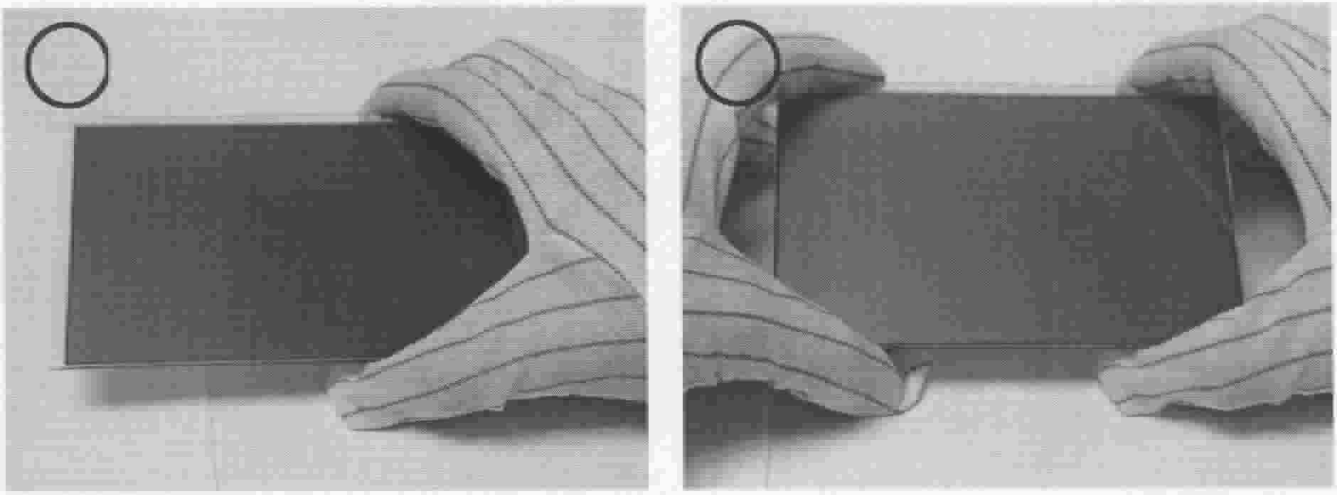


Please don't stretch interface of output.

THE NOTES OF LCD USING

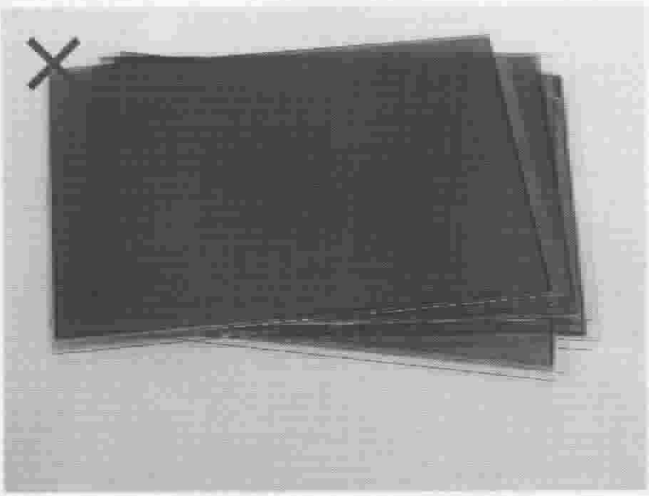
LCD is easy damage.
Please follow notes as bellows, and be careful of handling!

Correct handling

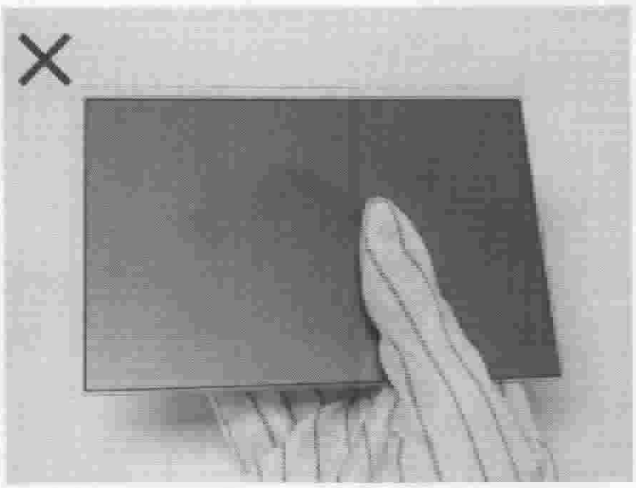


As above picture, please handle with glove by LCD edges and full EOS/ESD protection.

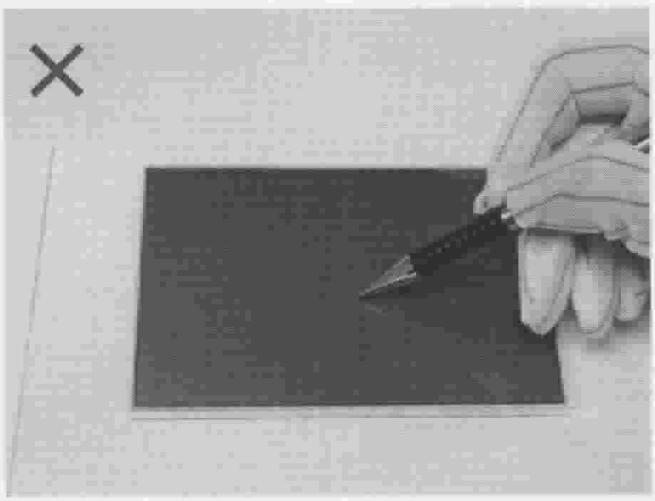
Incorrect handling



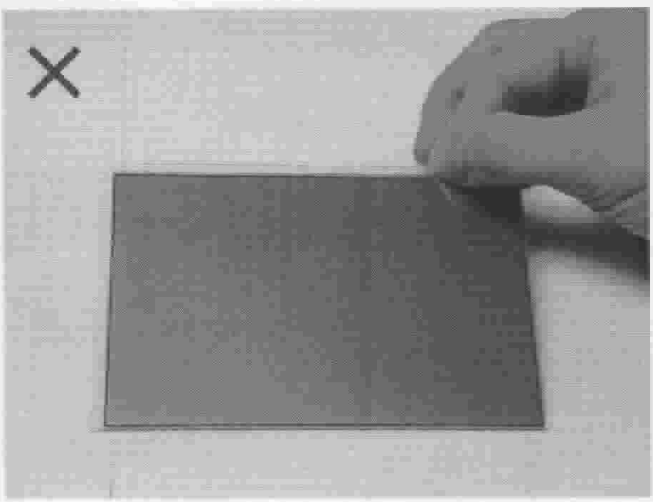
Please don't put one on another LCD.



Please don't hold the surface of LCD.



Please don't operate with sharp stick such as sharp pencil.



Please don't touch ITO glass without anti-static gloves.