

# 10.1 inch TFT LCD with capacitive touch panel SPECIFICATION

**MODEL NAME: LMCYA101XXN1-CCG1** 

Date: 2014 / 04 / 21

Customer Signature									
Customer									
<b>Approved Date</b>	Approved By	Reviewed By							



# 1. Revision History

Sample Version	DOC. Version	DATE	DESCRIPTION		CHANGED BY
A0	00	2014-04-21	FULL SPEC	First issue	Fanny / Jimmy
_					3 3
		ļ			
		<u> </u>			



# **2. Table of Contents:**

NO	CONTENTS	PAGE
1	Revision History	2
2	Table of Contents	3
3	General Specification	4
4	LCM drawing	5
5	Electrical Characteristics	6
6	Optical Characteristics	13
7	Interface Pin Assignment	15
8	Backlight	17
9	Standard Specification for Reliability	18
10	Handing Precaution	20
11	Inspection Specifications	21
12	Warranty	21
13	RMA	21

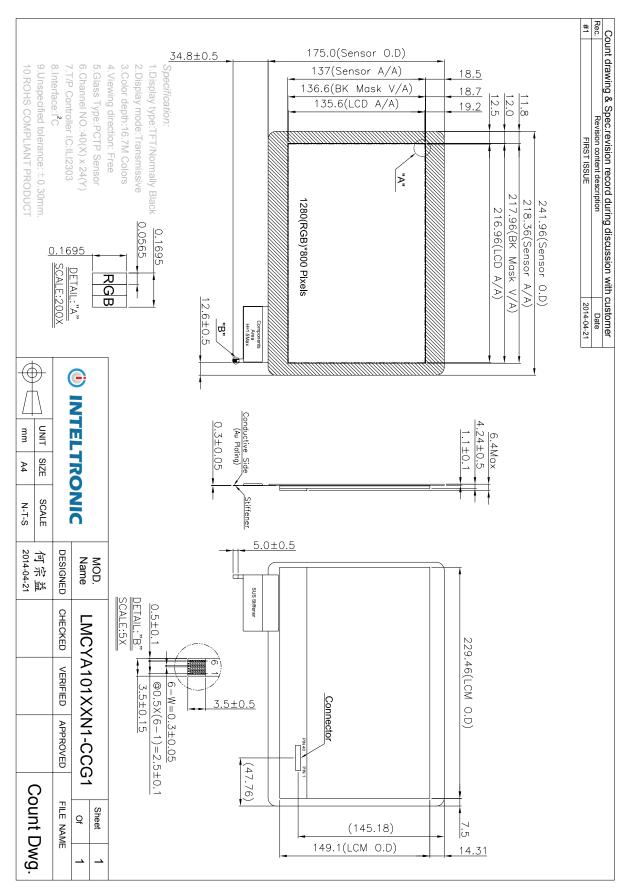


# 3. General Specification:

ITEM	CONTENTS
Module Size	241.96(W) * 175.0 (H) * 6.4(T) mm
Module Size(With FPC)	241.96(W) *209.8 (H) * 6.4 (T) mm
Display Size(Diagonal)	10.1 inch
Display Format	1280(RGB)* 800 Pixels
Active Area	216.96(W) * 135.6(H) mm
Pixel Pitch	0.1695 * 0.1695 mm
LCD Type	TFT(16.7M) / Transmissive / Normal Black/Glare
Touch panel Type	OLGS
View Angle	Free
CTP IC	ILI2303
Weight	303.5g



#### 4. LCM drawing:





# **5. Electrical Characteristics**

#### **5-1 Absolute Maximum Ratings**

#### (Ta=25°C VSS=0V)

Item	Symbol	Min.	Type	Max.	Unit	Remark
Power Supply voltage	VDD	-0.3	-	4.0	Volt	
	AVDD	-0.3	ı	14.0	Volt	
	VGH	-0.3	-	42.0	Volt	
	VGL	-19	-	0.3	Volt	
	LED+	7.8	ı	9.9	Volt	
Operating Temperature	Topr	0	ı	+50	$^{\circ}\mathbb{C}$	
Operating Humidity	Hopr	-		+90	%RH	
Storage Temperature	Tstg	-20	-	+60	$^{\circ}\!\mathbb{C}$	
Storage Humidity	Hstg	-		+90	%RH	

**Touch panel controller ILI2303** 

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Power Supply	$V_{ m DD}$	-	3.0	3.3	3.3	V





### **5-2 Operating Conditions**

5-2-1 Input Voltage

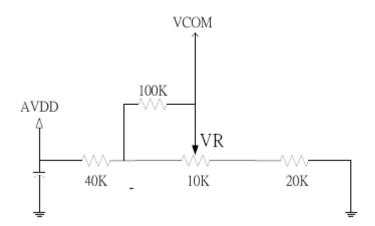
(Ta=25°C)

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Power Supply voltage	VDD	3.0	3.3	3.6	Volt	
Analog Power Supply	AVDD	7.9	8.2	8.5	Volt	Ripple=± 150mV
TFT Gate On Voltage	VGH	21.5	22.0	22.5	Volt	
TFT Gate off Voltage	VGL	-7.5	-7.0	-6.5	Volt	
Input Common voltage	Vcom	3.0	3.3	3.6	Volt	Note 1
Voltage for LED BL	$V_{\rm L}$	8.1	9.0	9.6	Volt	

**5-2-2 Supply Current** 

o z z buppiy Cuit						
Power Supply Current	IVdd	-	270	-	mA	VDD=3.3V
Analog Power Supply	IAV <sub>DD</sub>	-	250	-	mA	AVDD=8.2V
TFT Gate On Power	IGH	20	1	1	mA	VGH=22V
TFT Gate off Power	IGL	20	-	-	mA	VGL=-7V
Common Voltage Current	IVCOM	3	1	1	mA	
Current for LED BL	IL	-	220	-	mA	VL=9V

Note1: Specification is reference only ,the optimized VCOM value should be got by adjusting VR at flicker pattern. Below show suggested VR circuit







#### **5-3 LVDS Receiver**

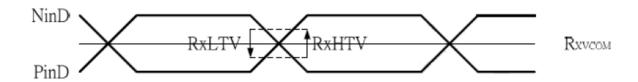
#### 5-3-1 Signal Electrical Characteristics For LVDS Receiver

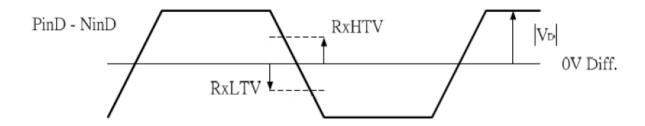
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
LVDS Differential signal high	RXHTV	-	_	+100	m∨	
Threshold voltage						RXVCOM=1.2V
LVDS Differential signal Low	RXLTV	-100	_		m∨	1.21
Threshold voltage	10.21	100			٧	
LVDS Differential signal	RXVCOM	0.7	_	1.6	V	_
Common voltage	100000111	0		1.0	Ť	
LVDS Differential signal voltage	VD	0.1	-	0.6	٧	-

Note (1) Input signals shall be low or Hi-Z state when VDD is off.

(2) All electrical characteristics for LVDS signal are defined and shall be measured at the interface connector of LCD.

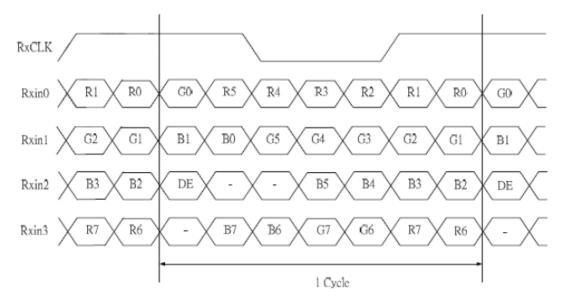
#### Voltage Definition







## LVDS Data Mapping



# 5-4 Interface Timing5-4-1 LVDS Interface Characteristics

**Interface Timings** 

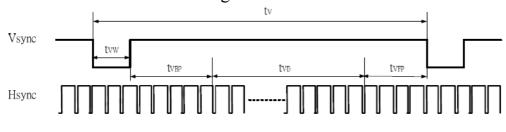
Parameter	Symbol	Unit	Min.	Min. Typ.		
Frame Rate		Hz	-	60	-	
Vertical Total Time	t∨	line	(815)	(823)	(1023)	
Vertical Display Time	t∨D	line	800			
Vertical Blanking Time	tvw+tvbp+tvfp	line	(15)	(23)	(33)	
Horizontal Total Time	tн	clock	(1340)	(1440)	(1470)	
Horizontal Display Time	thd	clock	1280			
Horizontal Blanking Time	thw+thBP+thFP	clock	(60)	(160)	(190)	
Clock Rate	1/TC	MHz	(68.9)	(71.1)	(73.4)	

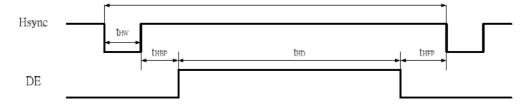




# **5-4-2 Timing Diagram of Interface Signal(DE mode)**

#### **Timing Characteristics**



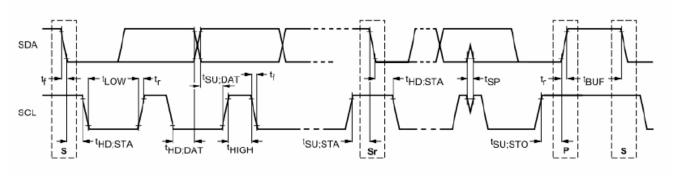






## 5-4-3 Touch panel controller ILI2303





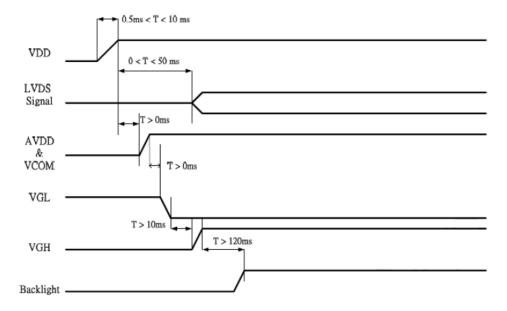
Cumbal	Parameter		100KHz		400KHz			
Symbol	Parameter	Min	Max	Unit	Min	Max	Unit	
f <sub>SCL</sub>	SCL clock frequency	0	100	kHz	0	400	kHz	
t <sub>HD;STA</sub>	Hold time (repeated) START condition.	4.0	_	μs	0.6	_	μs	
	After this period, the first clock pulse is							
	generated							
$t_{LOW}$	LOW period of the SCL clock	4.7	_	μs	1.3	_	μs	
t <sub>HIGH</sub>	HIGH period of the SCL clock	4.0	_	μs	0.6	_	μs	
t <sub>SU;STA</sub>	Set-up time for a repeated START	4.7	_	μs	0.6	-	μs	
	condition							
t <sub>HD;DAT</sub>	Data hold time	5.0	_	μs	0	0.9	μs	
t <sub>SU;DAT</sub>	Data set-up time	250	_	ns	100	_	ns	
t <sub>r</sub>	Rise time of both SDA and SCL signals	_	1000	ns	_	300	ns	
t <sub>f</sub>	Fall time of both SDA and SCL signals	-	300	ns	_	300	ns	
t <sub>su;sto</sub>	Set-up time for STOP condition	4.0	_	μs	0.6	_	μs	
t <sub>BUF</sub>	Bus free time between a STOP and	4.7	_	μs	1.3	_	μs	
	START condition							



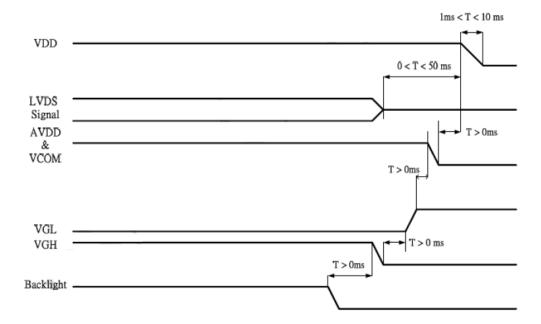
#### 5-5 Power ON/OFF Sequence

Power on/off sequence is as follows. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.

#### Power ON:



#### Power OFF:





# **6. Optical Characteristics:**

Item		Cymbol	Conditions	Spe	cificat	ions	Unit	Remark	
Item		Symbol	Conditions	Min	Тур	Max	Unit	Remark	
Contrast l	Ratio	CR	Normal	600	800	-		Note 3	
Response	time	Rising +Falling	θ=Φ=0°		(25)	(50)	ms	Note 2	
	Llon	θL	Ф=180°(9 o'clock)	75	85	-			
Viewing angle	Hor. θR	Φ=0°(3 o'clock)	75	85	-	dog	Note 1		
(CR≥10) B/L ON	Ver. θ <sub>B</sub>		Φ=90°(12 o'clock)	75	85	-	deg.	Note 1	
		Ф=270°(6 o'clock)	75	85	_				

#### Measuring Condition

1. Measuring surrounding: dark room

2. Ambient temperature: 25±2°C

3. 30 min. Warm-up time.

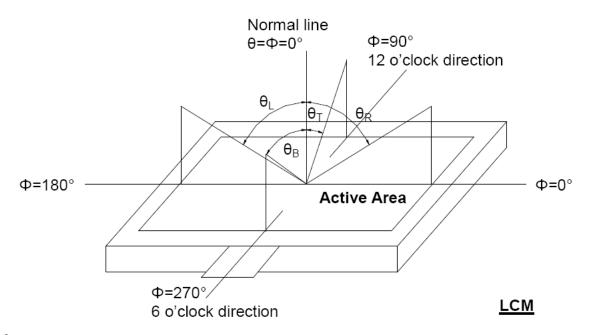
#### Color of CIE Coordinate:

Item	Symbol	Min.	Тур.	Max.	
	D 1	X	-	TBD	-
	Red	у	-	TBD	- - - - -
Chromaticity Coordinates (Transmissive)	C	X	-	TBD	-
	Green	у	- TB	TBD	-
	DI	X	1	TBD	-
	Blue	у	ı	TBD	-
	XX71-:4-	X	0.263	0.313	0.363
	White	y	0.279	0.329	0.379



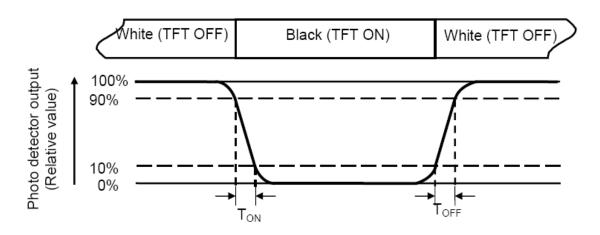


Note 1: Definition of viewing angle range



Note 2: Definition of Response Time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.



Note 3: Definition of contrast ratio

Luminance measured when LCD on the "White" state Contrast ratio (CR) = Luminance measured when LCD on the "Black" state





# 7. Interface Pin Assignment:

#### 7-1 LCM Interface Pin

Interface connector apply F62240-H1210A or equivalent

No.	Symbol	I/O	Function	Remark	
1	VCOM	P	Common Voltage		
2	VDD	P	Power Voltage for digital circuit		
3	VDD	P	Power Voltage for digital circuit		
4	NC		No Connection		
5	NC		No Connection		
6	NC		No Connection	Reserve for BIST	
7	GND	P	Ground		
8	RXIN0-	I	-LVDS differential data input	R0~R5,G0	
9	RXIN0+	I	+LVDS differential data input	K0~K3,G0	
10	GND	P	Ground		
11	RXIN1-	I	-LVDS differential data input	C1 C5 D0 D1	
12	RXIN1+	I	+LVDS differential data input	G1~G5,B0,B1	
13	GND	P	Ground		
14	RXIN2-	I	-LVDS differential data input	D2 D5 HC VC DE	
15	RXIN2+	I	+LVDS differential data input	B2~B5,HS,VS,DE	
16	GND	P	Ground		
17	RXCLKIN-	I	-LVDS differential clock input	LVDS alaak	
18	RXCLKIN+	I	+LVDS differential clock input	LVDS clock	
19	GND	P	Ground		
20	RXIN3-	I	-LVDS differential data input	R6~R7,G6~G7,B6~	
21	RXIN3+	I	+LVDS differential data input	В7	
22	GND	P	Ground		
23	NC		No Connection		
24	NC		No Connection		
25	GND	P	Ground		
26	NC		No Connection		
27	LED-PWM		PWM Input Signal for LED Driver	Note 1	
28	NC		No Connection		
29	AVDD	P	Power for Analog Circuit		
30	GND	P	Ground		

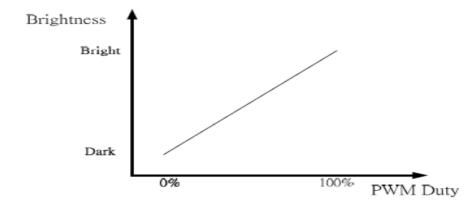




No.	Symbol	I/O	Function	Remark
31	LED-		LED Cathode	
32	LED-		LED Cathode	
33	NC		No Connection	
34	NC		No Connection	
35	VGL	P	TFT Gate OFF Voltage	
36	NC		No Connection	
37	CABC-EN		Content Adaptive Brightness Control Function Enable	*Enable: pull high *Disable: pull low or open
38	VGH	P	TFT Gate on Voltage	
39	LED+		LED Anode	
40	LED+		LED Anode	

I: input, O: output, P: Power

Note 1:



Note2: Interface connector apply 106A40-000000-G2-R(Starconn) or F62240-H1210A(Vigorconn) or equivalent,Pin 1 position see outline drawing)

#### 7-2 CTP Interface Pin

	/ 2 CII Invertuce I in				
No.	Symbol	I/O	Function		
1	VDD	P	Power Voltage for digital circuit		
2	RST	I	Reset		
3	INT	I	Interrupt		
4	SCL	I	I2C Serial Clock		
5	SDA	I	I2C Serial Data		
6	GND	P	Power Voltage for digital circuit		

Note: I2C interface





# 8. Backlight Characteristics:

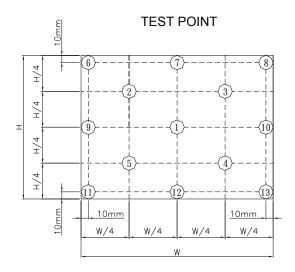
**Data About LED Backlight:** 

PARAMETER	Sym.	Min.	Тур.	Max.	Unit	Test Condition	Note
Supply Current	I	-	220	-	mA	V=9.0V	
Supply Voltage	V	81	9.0	9.6	V	lf=220mA	
Luminous Intensity for LCM	lv	-	300	-	Cd/m <sup>2</sup>	lf=220mA	1
Uniformity for LCM	-	70	-	-	%		2

#### NOTE:

- 1. Average Luminous Intensity of P1-P13
- 2. Uniformity = Min/Max \* 100%

#### Measured Method: (X\*Y: Light Area)





# **9.** Standard Specification for Reliability :: 9–1. Standard Specifications for Reliability of LCD Module

No	Item	Description		
01	High temperature operation	The sample should be allowed to stand at 50°C for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.		
02	Low temperature operation	The sample should be allowed to stand at $0^{\circ}$ C for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.		
03	High temperature storage	The sample should be allowed to stand at 60°C for 240 hours under no-load condition, and then returning it to normal temperature condition, and allowing it stand for 2 hours.		
04	Low temperature storage	The sample should be allowed to stand at -20°C for 240 hours under no-load condition, then returning it to normal temperature condition, and allowing it stand for 2 hours.		
05	Moisture storage	The sample should be allowed to stand at 60°C,90%RH MAX for 240 hours under no-load condition, then taking it out and drying it at normal temperature for 2 hours.		
06	Thermal shock storage	The sample should be allowed to stand the following 10 cycles : $-20^{\circ}\text{C}$ for 30 minutes $\rightarrow$ normal temperature for 5 minutes, as one cycle.		
07	Packing vibration	Frequency range: 10Hz ~ 55Hz Amplitude of vibration: 1.5mm X,Y,Z 2 hours for each direction.  Sweep time: 12 min		
08	Packing drop test	According to ISTA 1A 2001.		
09	Electrical Static	Air: $\pm 6$ KV $150$ pF/ $330\Omega$ 5 times		
	Discharge	Contact: $\pm 4KV \ 150pF/330\Omega \ 5$ time		

<sup>\*</sup>Sample size for each test item is 3~5pcs





#### 9-2. Testing Conditions and Inspection Criteria

For the final test the testing sample must be stored at room temperature for 24 hours, after the tests listed in Table 9.2, Standard specifications for Reliability have been executed in order to ensure stability.

No	Item	Test Model	In section Criteria
01	Current Consumption	Refer To Specification	The current consumption should conform to the product specification.
02	Contrast	Refer To Specification	After the tests have been executed, the contrast must be larger than half of its initial value prior to the tests.
03	Appearance	Visual inspection	Defect free.

#### 9-3. MTBF

MTBF	Functions, performance, appearance, etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature (25±5°C), normal humidity (50±10% RH), and in area not exposed to direct sun light.
------	---





#### 10. Handling Precaution:

#### 10-1 Handling of LCM

- Don't give external shock.
- Don't apply excessive force on the surface.
- Liquid in LCD is hazardous substance. Must not lick and swallow. when the liquid is attach to your hand, skin, cloth etc. Wash it out thoroughly and immediately.
- Don't operate it above the absolute maximum rating.
- Don't disassemble the LCM.
- The operators should be grounded whenever he/she comes into contact with the module. Never touch any of the conductive parts such as the LSI pads, the copper leads on the PCB and the interface terminals with any parts of the human body.
- The modules should be kept in antistatic bags or other containers resistant to static for storage.
- The module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

#### 10-2 Storage

- Store in an ambient temperature of  $25\pm10^{\circ}$ C, and in a relative humidity of  $50\pm10\%$  RH. Don't expose to sunlight or fluorescent light.
- Storage in a clean environment, free from dust, active gas, and solvent.
- Store in anti-static electricity container.
- Store without any physical load.

#### 10-3 Soldering

- Use only soldering irons with proper grounding and no leakage.
- Iron: No higher than 280±10°C and less than 3 sec during Hand soldering.
- Rewiring: no more than 2 times.





#### 11. Inspection Specifications

The buyer (customer) shall inspect the modules within twenty calendar days since the delivery date (the "inspection period") at its own cost. The results of the inspection (acceptance or rejection) shall be recorded in writing, and a copy of this writing will be promptly sent to the seller.

The buyer may, under commercially reasonable reject procedures, reject an entire lot in the delivery involved if, within the inspection period, such samples of modules within such lot show an unacceptable number of defects in accordance with this incoming inspection standards, provided however that the buyer must notify the seller in writing of any such rejection promptly, and not later than within three business days of the end of the inspection period.

Should the buyer fail to notify the seller within the inspection period, the buyer's right to reject the modules shall be lapsed and the modules shall be deemed to have been accepted by the buyer.

#### 12. Warranty

Inteltronic Inc. warrants to you, the original purchaser, that each of its products will be free from defects in materials and workmanship for one year from the date of purchase.

Inteltronic Inc. will be limited to replace or repair any of its module which is found and confirmed defective electrically or visually when inspected in accordance with Inteltronic Inc. general module inspection standard.

This warranty does not apply to any products which have been on customer's production line, repaired or altered by persons other than repair personnel authorized by Inteltronic Inc., or which have been subject to misuse, abuse, accident or improper installation. Inteltronic Inc. assumes no liability under the terms of this warranty as a consequence of such events.

If an Inteltronic Inc. product is defective, it will be repaired or replaced at no charge during the warranty period. For out-of-warranty repairs, you will be billed according to the cost of replacement materials, service time and freight. In returning the modules, they must be properly packaged with original package; there should be detailed description of the failures or defect.

#### 13. RMA

Products purchased through Inteltronic Inc. and under warranty may be returned for replacement. Contact support@inteltronicinc.com for RMA number and procedures





# Office Locations

# **(i)** INTELTRONIC

Inteltronic Inc.

www.inteltronicinc.com Office: 510-471-9900 Fax: 510-471-9901

Address: 29470 Union City Blvd

Union City, CA 94587



www.wahlee.com Wah Lee Industrial Corp. **HSINCHU OFFICE** 18F, No.8, Zihciang S. Rd., Jhubei, Hsinchu 302, Taiwan, R.O.C.

Tel: 886-3-6205880 FAX: 886-3-6205833

