

### LMN3660EJZF 30V N-Channel Enhancement Mode MOSFET

### Features

- Low Gate Charge
- ESD Protected
- SOT-23 package design

### **Product Description**

LMN3660E, N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent R<sub>DS(ON)</sub>, low gate charge.

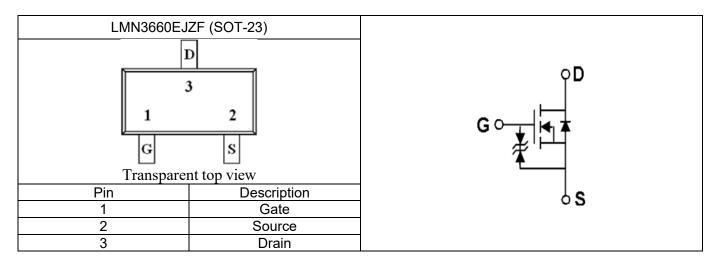
These devices are particularly suited for low

#### **Pin Configuration**

voltage power management, such as smart phone and notebook computer, and low in-line power loss are needed in commercial industrial surface mount applications.

### Applications

- Power Management in Note book
- Portable Equipment
- Load Switch





# **Ordering Information**

Ordering Information						
Part Number P/N		PKG code	Pb Free code	Package Quantit		
LMN3660EX7F	LMN3660E	JZ	F	SOT-23	3000 PCS	

# **Marking Information**

Marking Information					
Part Marking	Part Number	LFC code			
0XM	0	XM			

# Absolute Maximum Ratings

(T<sub>C</sub>=25°C Unless otherwise noted)

Symbol	Parameter	Typical	Unit
V <sub>DSS</sub>	Drain-Source Voltage	30	V
V <sub>GSS</sub>	Gate-Source Voltage	±12	V
I <sub>D</sub>	Continuous Drain Current T <sub>A</sub> =25°C <sup>1</sup>	0.57	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	2.0	А
ls	Continuous Source Current (Diode Conduction)	0.28	A
P <sub>D</sub>	Power Dissipation	0.3	W
TJ	Operating Junction Temperature	-55 to +150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to +150	°C
R <sub>0JA</sub>	Thermal Resistance-Junction to Ambient <sup>1</sup>	363	°C/W



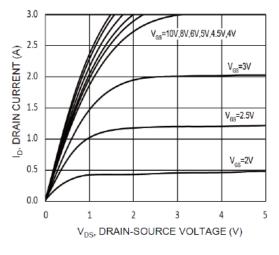
# **Electrical Characteristics**

### (T<sub>C</sub>=25°C Unless otherwise noted)

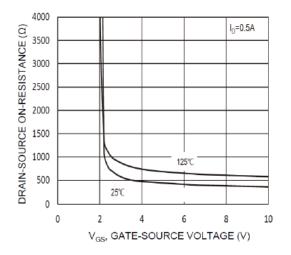
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30			V
$V_{GS(th)}$	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250Ua	0.5		1.5	
I <sub>GSS</sub>	Gate Leakage Current	$V_{DS}=0V, V_{GS}=\pm 12V$			10	uA
	Zero Gate Voltage Drain Current	$V_{DS}$ =24V, $V_{GS}$ =0V			100	nA
I <sub>DSS</sub>		V <sub>DS</sub> =16V, V <sub>GS</sub> =0V, TJ=85⁰C			30	uA
	Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =0.5A		340	600	mΩ
R <sub>DS(on)</sub>		V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.4A		412	650	
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =0.3A		625	1200	
<b>g</b> fs	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =0.5A		1.2		S
Vsd	Diode Forward Voltage	I <sub>S</sub> =0.5A, V <sub>GS</sub> =0V			1.35	V
		Dynamic				
Qg	Total Gate Charge	(-15)(-10)(-10)(-10)(-10)(-10)(-10)(-10)(-10		1.5		
$Q_gs$	Gate-Source Charge	V <sub>DS</sub> =15V, V <sub>GS</sub> =10V, I <sub>D</sub> =0.5A		0.2		nC
$Q_gd$	Gate-Drain Charge	ID-0.5A		0.2		
Ciss	Input Capacitance	(-1E)(-1)(-0)(-0)(-1)(-0)(-0)(-0)(-0)(-0)(-0)(-0)(-0)(-0)(-0		39		
Coss	Output Capacitance	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz		9		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			6		
t <sub>d(on)</sub>	Turn On Time			5.3		
tr	Turn-On Time	V <sub>DD</sub> =15V, I <sub>D</sub> =0.5A,		16		ns
t <sub>d(off)</sub>	Turn Off Time	V <sub>GS</sub> =10V, R <sub>G</sub> =2.5Ω		20		
t <sub>f</sub>	Turn-Off Time			18		



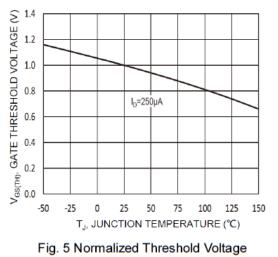
### **Typical Performance Characteristics**











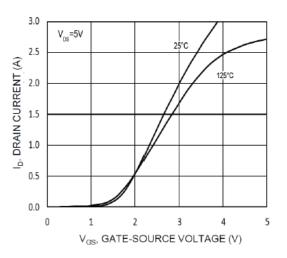


Fig. 2 Typical Transfer Characteristics

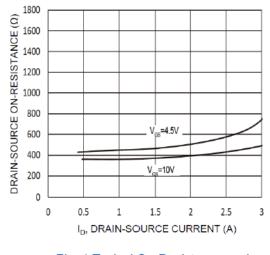
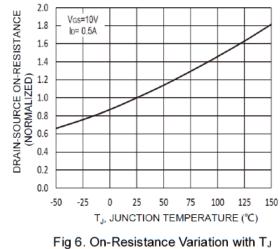


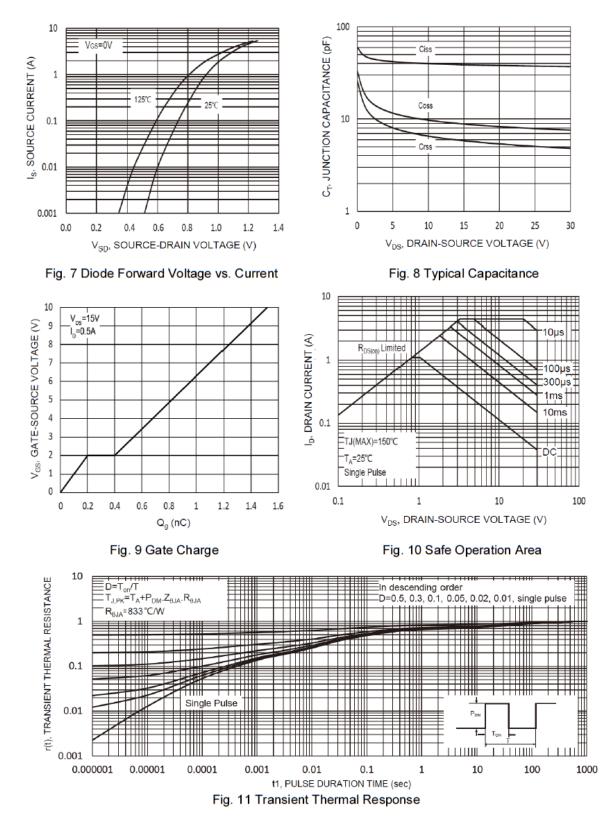
Fig. 4 Typical On-Resistance vs. ID



LMN3660EJZF

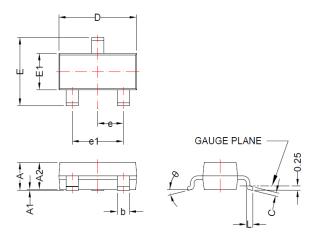


### Typical Performance Characteristics(continue)





# SOT-23



	Dimensions					
Symbol	Millimeters		Inches			
	Min	Max	Min	Max		
Α	0.75	1.17	0.030	0.046		
A1	0.01	0.15	0.000	0.006		
A2	0.70	1.02	0.028	0.040		
b	0.30	0.50	0.012	0.020		
С	0.08	0.20	0.003	0.008		
D	2.80	3.04	0.110	0.120		
E	2.10	2.64	0.083	0.104		
E1	1.20	1.40	0.047	0.055		
е	0.95 BSC		0.037 BSC			
e1	1.90 BSC		0.075 BSC			
L	0.3	0.6	0.012	0.024		
θ	0°	8°	0°	8°		



#### NOTICE:

LFC Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all LFC Semiconductor products described or contained herein. LFC Semiconductor products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.

Applications shown on the herein document are examples of standard use and operation. Customers are responsible in comprehending the suitable use in particular applications. LFC Semiconductor makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Information furnished is believed to be accurate and reliable. However LFC Semiconductor assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties, which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of LFC Semiconductor. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information without express written approval of LFC Semiconductor.