

### LMNN7002KY Dual N-Channel Enhancement Mode MOSFET

#### **Features**

- 60V/0.5A,  $R_{DS(ON)}=3.0\Omega@V_{GS}=10V$
- 60V/0.2A,  $R_{DS(ON)}=4.0\Omega@V_{GS}=4.5V$
- Super high density cell design for extremely low R<sub>DS (ON)</sub>
- Exceptional on-resistance and maximum DC current capability
- SOT-563 package design

#### **Product Description**

The LMNN7002KY is the Dual N-Channel enhancement mode field effect transistors are produced using high cell density DMOS technology.

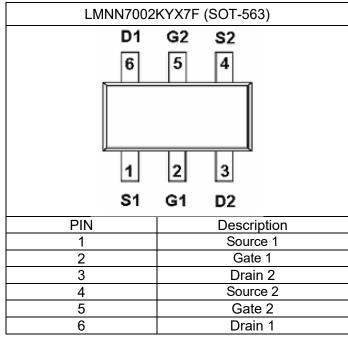
These products have been designed to minimize on-state resistance while provide rugged, reliable, and fast switching performance.

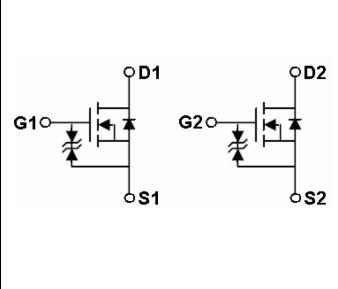
They can be used in most applications requiring up to 640mA DC and can deliver pulsed currents up to 950mA. These products are particularly suited for low voltage, low current applications such as small servo motor control, power MOSFET gate drivers, and other switching applications.

### **Applications**

- Drivers: Relays, Solenoids, Lamps, Hammers, Display, Memories, Transistors, etc.
- High saturation current capability. Direct Logic-Level Interface: TTL/CMOS
- Battery Operated Systems
- Solid-State Relays

# **Pin Configuration**







# **Ordering Information**

| Ordering Information |            |          |              |         |          |
|----------------------|------------|----------|--------------|---------|----------|
| Part Number          | P/N        | PKG code | Pb Free code | Package | Quantity |
| LMNN7002KYX7F        | LMNN7002KY | X7       | F            | SOT-563 | 4000pcs  |

# **Marking Information**

| Marking Information |          |  |  |  |  |
|---------------------|----------|--|--|--|--|
| Part Number         | LFC code |  |  |  |  |
| <u>e2</u>           | XW       |  |  |  |  |

# **Absolute Maximum Ratings**

(T<sub>C</sub>=25°C Unless otherwise noted)

| Symbol           | Parameter                              | Parameter                         |      | Unit                                   |
|------------------|--|-----------------------------------|------|--|
| $V_{DS}$         | Drain-Source Voltage                   | Drain-Source Voltage              |      | V                                      |
| $V_{GS}$         | Gate-Source Voltage                    | Gate-Source Voltage               |      | V                                      |
|                  | Continuous Drain Current               | T <sub>A</sub> =25℃               | 270  | Δ.                                     |
| I <sub>D</sub>   |  | T <sub>A</sub> =70℃               | 216  | —————————————————————————————————————— |
| I <sub>DM</sub>  | Pulsed Drain Current <sup>1</sup>      | Pulsed Drain Current <sup>1</sup> |      | Α                                      |
| $P_D$            | Power Dissipation                      | T <sub>A</sub> =25℃               | 0.35 | 10/                                    |
|                  |  | T <sub>A</sub> =70°C              | 0.22 | W                                      |
| TJ               | Operating Junction Temperate           | Operating Junction Temperature    |      | $^{\circ}$                             |
| T <sub>STG</sub> | Storage Temperature Range              | Storage Temperature Range         |      | $^{\circ}$                             |
| $R_{\theta JA}$  | Thermal Resistance-Junction to Ambient |                                   | 357  | °C/W                                   |

<sup>1)</sup> Pulse width limited by safe operating area.



### **Electrical Characteristics**

## (T<sub>C</sub>=25°C Unless otherwise noted)

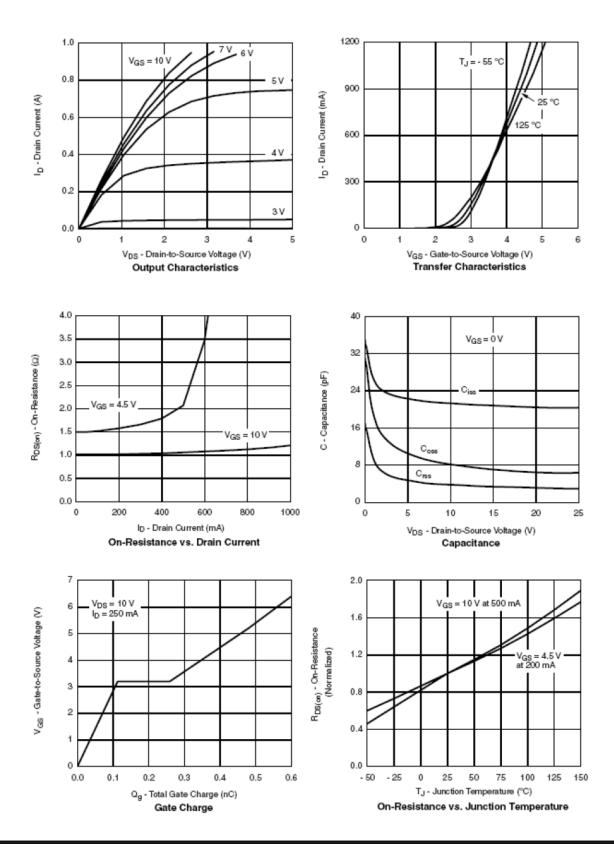
| Symbol           | Parameter                       | Conditions   | Min | Тур  | Max | Unit |  |
|------------------|---------------------------------|--|-----|------|-----|------|--|
|                  | S                               | Static characteristics   |     | •    | •   | •    |  |
| $V_{(BR)DSS}$    | Drain-Source Breakdown Voltage  | V <sub>GS</sub> =0V, I <sub>D</sub> =250uA   | 60  |      |     | V    |  |
| $V_{GS\ (th)}$   | Gate Threshold Voltage          | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA   | 1   |      | 2.5 | V    |  |
| I <sub>GSS</sub> | Gate Leakage Current            | V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V   |     |      | ±10 | uA   |  |
| . 7              | Zero Gate Voltage Drain Current | V <sub>DS</sub> =60V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C<br>V <sub>DS</sub> =60V, V <sub>GS</sub> =0V, T <sub>J</sub> =85°C |     |      | 1   | uA   |  |
| I <sub>DSS</sub> |                                 |  |     |      | 30  |      |  |
| D D.             | Drain-Source On-Resistance      | V <sub>GS</sub> =10V, I <sub>D</sub> =500mA  |     |      | 3   | Ω    |  |
| $R_{DS(on)}$     |                                 | V <sub>GS</sub> =4.5V, I <sub>D</sub> =200mA   |     |      | 4   |      |  |
|                  | Dy                              | namic characteristics  |     |      |     |      |  |
| $Q_g$            | Total Gate Charge               | \/=10\/_\/=4.5\/   |     | 1000 |     | pC   |  |
| $Q_gs$           | Gate-Source Charge              | V <sub>DD</sub> =10V, V <sub>GS</sub> =4.5V,<br>I <sub>D</sub> =0.25A  |     | 100  |     |      |  |
| $Q_{gd}$         | Gate-Drain Charge               | 10-0.23A   |     | 150  |     |      |  |
| C <sub>iss</sub> | Input Capacitance               | \/ -25\/ \/ -0\/   |     | 32   |     |      |  |
| Coss             | Output Capacitance              | V <sub>DS</sub> =25V, V <sub>GS</sub> =0V,<br>f=1.0MHz   |     | 8    |     | pF   |  |
| $C_{rss}$        | Reverse Transfer Capacitance    | 1 - 1.0MH2   |     | 6    |     |      |  |
| $t_{d(on)}$      | Turn-On Time                    |  |     | 10   | 20  | ns   |  |
| t <sub>r</sub>   |                                 | $V_{DD}$ =30V, $V_{GEN}$ =4.5V,  |     | 35   | 50  |      |  |
| $t_{d(off)}$     | Turn-Off Time                   | $R_G=10\Omega$ , $I_D=0.2A$ , $R_L=150\Omega$  |     | 20   | 30  |      |  |
| t <sub>f</sub>   |                                 |  |     | 40   | 60  |      |  |
| $V_{SD}$         | Diode Forward Voltage           | I <sub>S</sub> =0.2A, V <sub>GS</sub> =0V  |     | 0.7  | 1.3 | V    |  |
| g <sub>fs</sub>  | Forward Transconductance        | V <sub>DS</sub> =10V, I <sub>D</sub> =0.2A   |     | 0.2  |     | S    |  |
| Is               | Continuous Source Current       | $V_G = V_D = 0V$ , Force Current   |     |      | 450 | mA   |  |

#### Note

- 1. The data tested by pulsed, pulse width  $\leq$  300us, duty cycle  $\leq$  2%.
- 2. Essentially independent of operating temperature.

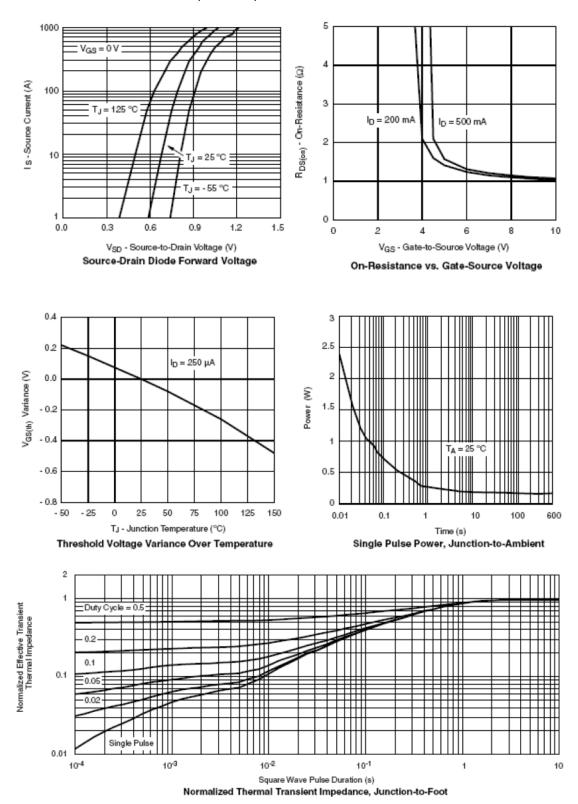


## **Typical Performance Characteristics**





## **Typical Performance Characteristics**(continue)

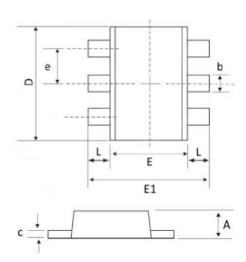


LMNN7002KY



#### Package Dimension:

#### SOT-563



| -       |             | Dimensions |        |       |  |
|---------|-------------|------------|--------|-------|--|
| Cymphol | Millimeters |            | Inches |       |  |
| Symbol  | Min         | Max        | Min    | Max   |  |
| Α       | 0.500       | 0.600      | 0.020  | 0.024 |  |
| b       | 0.150       | 0.300      | 0.006  | 0.012 |  |
| С       | 0.100       | 0.180      | 0.004  | 0.007 |  |
| D       | 1.500       | 1.700      | 0.059  | 0.067 |  |
| E       | 1.100       | 1.250      | 0.043  | 0.049 |  |
| E1      | 1.550       | 1.700      | 0.061  | 0.067 |  |
| е       | 0.5 BSC     |            | 0.02   | BSC   |  |
| L       | 0.100       | 0.300      | 0.004  | 0.012 |  |

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