

Rev. 1.0

LMPP3925EX6F 30V P-Channel Enhancement Mode MOSFET

Features

- -30V/-0.3A, $R_{DS(ON)}$ =2500m Ω @V_{GS}=-4.5V $R_{DS(ON)}$ =2900m Ω @V_{GS}=-2.5V $R_{DS(ON)}$ =5000m Ω @V_{GS}=-1.8V
- Low-Voltage Operation
- High-Speed Circuits
- ESD Protection
- SOT-363 package design

Product Description

LMPP3925EX6F, P-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent $R_{DS(ON)}$, low gate charge.

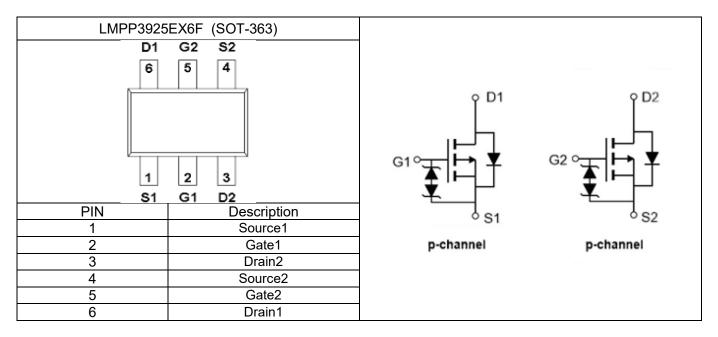
These devices are particularly suited for low

Pin Configuration

voltage power management, such as smart phone and notebook computer, and low in-line power loss are needed in commercial industrial surface mount applications.

Applications

- Drivers: Relays, Solenoids, Lamps, Hammers
- Battery Operated Systems
- Power Supply Converter Circuits
- Load/Power Switching Smart Phones, Pagers





Ordering Information

Ordering Information						
Part Number	P/N	PKG code	Pb Free code	Package	Quantity	
LMPP3925EX6F	LMPP3925E	X6	F	SOT-363	3000pcs	

Marking Information

Marking Information					
Part Number	LFC code				
<u>5</u>	WM				

Absolute Maximum Ratings

(T_C=25°C Unless otherwise noted)

Symbol	Parameter		Typical	Unit
V _{DSS}	Drain-Source Voltage		-30	V
V _{GSS}	Gate-Source Voltage		±10	V
1	Continuous Drain Current (T _J =150°C)	T _A =25℃	-0.3	^
D		T _A =70℃	-0.24	Α
l _{DM}	Pulsed Drain Current		-1.2	A
PD	Devuer Dissinction	T ,=25℃	0.31	10/
	Power Dissipation	T , =70 ℃	0.2	W
TJ	Operating Junction Temperature		-55 to +150	°C
Т _{sтg}	Storage Temperature Range		-55 to +150	°C
R _{θJA}	Thermal Resistance Junction to ambient		400	°C/W

Note1. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



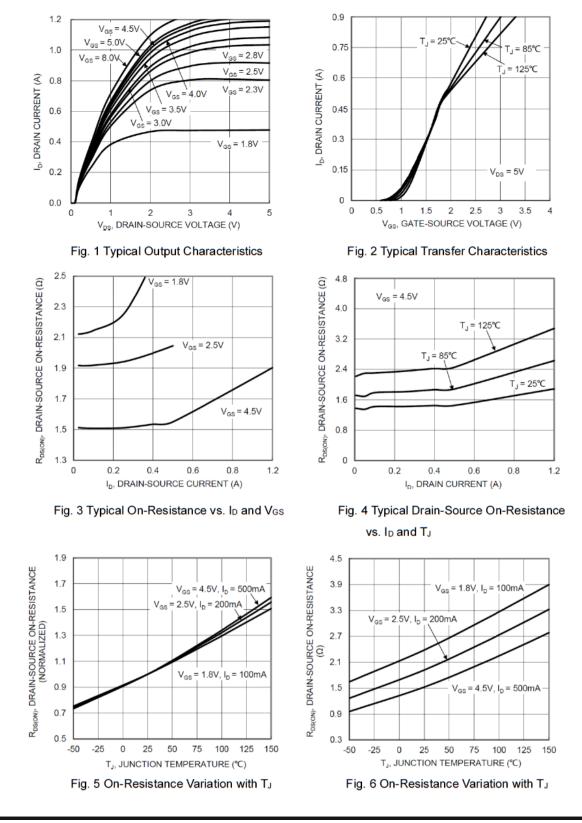
Electrical Characteristics

(T_C=25°C Unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
	St	atic characteristics					
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-30			V	
V _{GS (th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250uA	-0.4		-1.0	V	
l _{GSS}	Gate Leakage Current	V _{DS} =0V, V _{GS} =±8V			±10	nA	
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-24V, V _{GS} =0V			-1	uA	
V _{SD}	Diode Forward Voltage	I _S =-0.5A, V _{GS} =0V			-1.3	V	
$R_{DS(on)}$	Drain-Source On-Resistance	V _{GS} =-4.5V, I _D =-0.5A		1.5	2.5	mΩ	
		V _{GS} =-2.5V, I _D =-0.2A		1.9	2.9		
		V _{GS} =-1.8V, I _D =-0.1A		2.4	50		
g fs	Forward Transconductance	V _{DS} =-10V, I _D =-0.25A		550		mS	
		Dynamic			I		
Q _g T	Total Gate Charge	V _{DD} =-15V, V _{GS} =-10V,		1.0	1.0		
		I _D =-1A		1.0		nC	
Q_{gs}	Gate-Source Charge	V _{DD} =-15V, V _{GS} =-8V,		0.2			
Q_{gd}	Gate-Drain Charge	I _D =-1A		0.1		7	
C _{iss}	Input Capacitance			54			
C _{oss}	Output Capacitance	V _{DS} =-15V, V _{GS} =0V,		10.9		pF	
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz		5.8		1	
t _{d(on)}	Turne Ore Time -	V_{DD} =-10V, R _L =47Ω, - V_{GEN} =-4.5V,		3.8			
tr	-Turn-On Time			11		ns	
t _{d(off)}	Turne Off Time a			45			
t _f	-Turn-Off Time	I _D =-0.2A, R _G =10Ω		20			



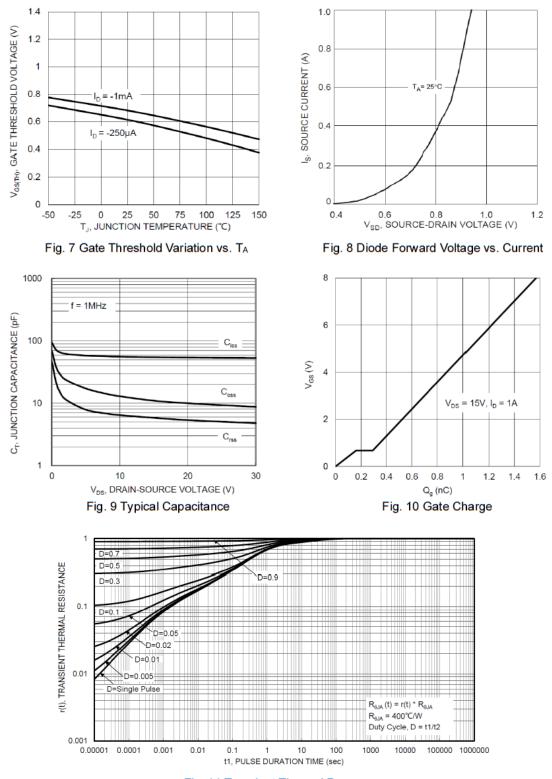
Typical Performance Characteristics



LMPP3925EX6F



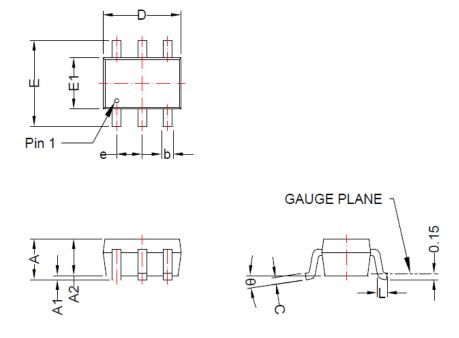
Typical Performance Characteristics(continue)







SOT-363



THE D DIMENSION DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS, MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.2mm END. THE E1 DIMENSION DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION, INTERLEAD FLASH OR PROTRUSION SHALL NOT 0.20mm PER SIDE.

Dimensions					
Symbol	Millimeters		Inches		
Symbol	Min	Max	Min	Max	
Α	0.80	1.10	0.031	0.043	
A1	0.00	0.10	0.000	0.004	
A2	0.70	1.00	0.028	0.039	
b	0.15	0.30	0.006	0.012	
С	0.08	0.25	0.003	0.010	
D	1.80	2.20	0.071	0.087	
E	1.80	2.40	0.071	0.094	
E1	1.15	1.35	0.045	0.053	
e	0.65 BSC		0.026 BSC		
L	0.26	0.45	0.010	0.018	
θ	0°	8°	0°	8°	



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