

OBSOLETE

August 30, 2011

Enhanced Plastic 5A and 3A Low Dropout Fast Response Regulators

General Description

The LMS1585AEP and LMS1587EP are low dropout positive regulators with output load current of 5A and 3A respectively. Their low dropout voltage (1.2V) and fast transient response make them an excellent solution for low voltage microprocessor applications.

The LMS1585AEP/87EP are available in adjustable versions, which can set the output voltage with only two external resistors. In addition, they are also available in 1.5V and 3.3V fixed voltage versions (Note 12).

The LMS1585AEP/87EP circuits include a zener trimmed bandgap reference, current limiting and thermal shutdown.

The LMS1585AEP/87EP series are available in TO-220 and TO-263 packages.

ENHANCED PLASTIC

- Extended Temperature Performance of -40°C to 125°C
- Baseline Control Single Fab & Assembly Site
- Process Change Notification (PCN)
- Qualification & Reliability Data
- Solder (PbSn) Lead Finish is standard
- Enhanced Diminishing Manufacturing Sources (DMS) Support

Features

- Fast transient response
- Available in Adjustable, 1.5V, and 3.3V versions
- Current limiting and thermal protection
- Line regulation

0.005% (typical)

Load regulation

0.05% (typical)

Applications

- Low voltage logic supplies
- Selected Military Applications
- Selected Avionics Applications

Ordering Information

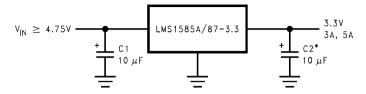
Part Number	VID Part Number	NS Package Number (Note 3)
LMS1585AIS33EP	V62/04639-01	TS3B
LMS1585AISADJEP	V62/04639-02	TS3B
LMS1587ISXADJEP	V62/04639-03	TS3B
(Note 1, Note 2)	TBD	TBD

Note 1: For the following (Enhanced Plastic) version, check for availability: LMS1585ACT3.3EP, LMS1585ACTADJEP, LMS1585AIT1.5EP, LMS1585AIT3.3EP, LMS1585AITADJEP, LMS1585ACS1.5EP, LMS1585ACS3.3EP, LMS1585ACSADJEP, LMS1585AIS1.5EP, LMS1585ACSX33EP, LMS1585CSXADJEP, LMS1585AISX15EP, LMS1585AISX33EP, LMS1585ASXADJEP, LMS1587CT1.5EP, LMS1587CT3.3EP, LMS1587CTADJEP, LMS1587IT1.5EP, LMS1587IT3.3EP, LMS1587ITADJEP, LMS1587CS1.5EP, LMS1587CS3.3EP, LMS1587CSADJEP, LMS1587IS1.5EP. LMS1587IS3.3EP, LMS1587ISADJEP, LMS1587CSX3.3EP, LMS1587CSXADJEP, LMS1587ISX1.5EP, LMS1587ISX3.3EP. Parts listed with an "X" are provided in Tape & Reel and parts without an "X" are in Rails.

Note 2: FOR ADDITIONAL ORDERING AND PRODUCT INFORMATION, PLEASE VISIT THE ENHANCED PLASTIC WEB SITE AT: www.national.com/ mil

Note 3: Refer to package details under Physical Dimensions

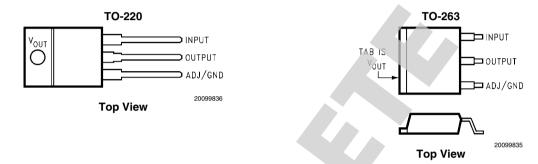
Typical Application



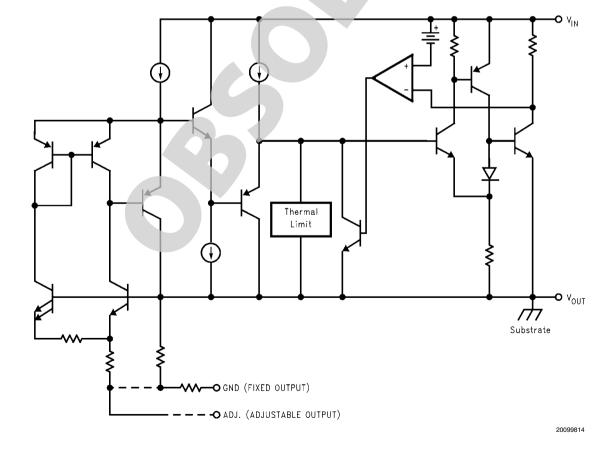
* REQUIRED FOR STABILITY

20099801

Connection Diagrams



Simplified Schematic



Absolute Maximum Ratings (Note 4)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Maximum Input to Output Voltage

(V_{IN} to GND) 13V Power Dissipation (*Note 5*) Internally Limited Junction Temperature (T_J) (*Note 5*) 150°C Storage Temperature Range -65°C to 150°C Lead Temperature 260°C, 10 sec ESD Tolerance (*Note 6*) 2000V

Electrical Characteristics

Typicals and limits appearing in normal type apply for $T_J = 25^{\circ}\text{C}$. Limits appearing in **Boldface** type apply over the entire junction temperature range for operation, 0°C to 125°C for commercial grade and -40°C to 125°C for Enhanced Plastic.

Symbol	Parameter	Conditions	Min	Тур	Max	Units
		Conditions	(Note 8)	(Note 7)	(Note 8)	UIIIIS
V _{REF} [Reference Voltage	LMS1585A-ADJEP				
		$V_{IN}-V_{OUT} = 3V$, $I_{OUT} = 10$ mA	1.238	1.25	1.262	V
		$10\text{mA} \le I_{\text{OUT}} \le 5\text{A}, 1.5\text{V} \le V_{\text{IN}} - V_{\text{OUT}} \le 5.75\text{V}$	1.225	1.250	1.275	V
		LMS1587-ADJEP				
		$10\text{mA} \le I_{\text{OUT}} \le 3\text{A}, 1.5\text{V} \le V_{\text{IN}} - V_{\text{OUT}} \le 5.75\text{V}$	1.225	1.250	1.275	V
V _{OUT}	Output Voltage	LMS1585A-1.5EP				
		$I_{OUT} = 0mA$, $V_{IN} = 5V$	1,485	1.500	1.515	V
		$0 \le I_{OUT} \le 5A$, $3V \le V_{IN} \le 7V$	1.470		1.530	V
		LMS1585A-3.3EP				
		$I_{OUT} = 0mA$, $V_{IN} = 5V$	3.267	3.300	3.333	V
		$0 \le I_{OUT} \le 5A, 4.75V \le V_{IN} \le 7V$	3.235	3.300	3.365	V
		LMS1587-1.5EP				
		$V_{IN} = 5V, I_{OUT} = 0mA, T_{J} = 25^{\circ}C$	1.485	1.500	1.515	V
		$0 \le I_{OUT} \le 3A$, $3V \le V_{IN} \le 7V$	1.470	1.500	1.530	V
		LMS1587-3.3EP				
		$0 \le I_{OUT} \le 3A, 4.75 \lor \le \lor_{1N} \le 7 \lor$	3.235	3.300	3.365	V
ΔV _{OUT}	Line Regulation	LMS1585AEP/87-ADJEP				
001	(Note 9)	$I_{OUT} = 10 \text{mA}, 2.75 \text{V} \le \text{V}_{\text{IN}} \le 7 \text{V}$		0.005	0.2	%
		LMS1585AEP/87-3.3EP				
		$I_{OUT} = 0$ mA, 4.75 V $\leq V_{IN} \leq 7$ V		0.005	0.2	%
		LMS1585AEP/87-1.5EP		0.000		,-
		$I_{OUT} = 0$ mA, $3V \le V_{IN} \le 7V$		0.005	0.2	%
ΔV _{OUT}	Load Regulation	LMS1585A-ADJEP		0.000		%
OUT	(Note 9)	$ V_{\text{IN}}-V_{\text{OUT}} = 3V$, $10\text{mA} \le I_{\text{OUT}} \le 5\text{A}$		0.05	0.3 0.5	76
		LMS1585A-1.5EP/LMS1585A-3.3EP		0.05	0.3	%
		$V_{\rm IN} = 5V, 0 \le I_{\rm OUT} \le 5A$		0.05	0.5	70
		LMS1587-ADJEP				
				0.05	0.3 0.5	%
		$V_{\text{IN}} - V_{\text{OUT}} = 3V$, 10mA $\leq I_{\text{OUT}} \leq 3A$		0.05		
		LMS1587-1.5EP/LMS1587-3.3EP		0.05	0.3	%
	D 11/1	$V_{IN} = 5V, 0 \le I_{OUT} \le 3A$		0.05	0.5	%
$V_{IN} - V_{OUT}$	Dropout Voltage	LMS1585A-ADJEP/LMS1587-ADJEP				
		$\Delta V_{REF} = 1\%, I_{OUT} = 3A$		1.15	1.3	V
		LMS1585A-3.3EP/LMS1587-3.3EP/				
		LMS1585A-1.5EP/LMS1587-1.5EP		4.45	4.0	.,
		$\Delta V_{OUT} = 1\%, I_{OUT} = 3A$		1.15	1.3	V
		LMS1585A-ADJEP		, ,		.,
		ΔV _{REF} = 1%, I _{OUT} = 5A		1.2	1.4	V
		LMS1585A-1.5EP/LMS1585A-3.3EP				
		$\Delta V_{OUT} = 1\%$, $I_{OUT} = 5A$		1.2	1.4	V

	T		Min	Тур	Max	
Symbol	Parameter	Conditions	(<i>Note 8</i>)	(<i>Note 7</i>)	(Note 8)	Units
I _{LIMIT}	Current Limit	LMS1585A-ADJEP/LMS1585A-3.3EP/	(333 3)	,	(
'LIMIT	Garrotte Elittie	LMS1585A-1.5EP				
		$V_{IN}-V_{OUT} = 5.5V$	5.0	6.6		Α
		LMS1587-ADJEP/LMS1587-3.3EP/				
		LMS1587-1.5EP				
		$V_{IN}-V_{OUT} = 5.5V$	3.1	4.3		Α
	Minimum Load Current	LMS1585AEP/87-ADJEP				
	(Note 10)	$1.5V \le V_{IN} - V_{OLIT} \le 5.75V$		2.0	10.0	mA
	Quiescent Current	LMS1585A-3.3EP/LMS1587-3.3EP/				
		LMS1585A-1.5EP/LMS1587-1.5EP				
		$V_{IN} = 5V$		7.0	13.0	mA
	Thermal Regulation	T _A = 25°C, 30ms Pulse		0.003		%/W
	Ripple Rejection	LMS1585A-ADJEP	7			
	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	$f_{\text{RIPPLE}} = 120 \text{Hz}, V_{\text{IN}} - V_{\text{OUT}} = 3 \text{V},$	K / 7 /			
		$I_{OUT} = 5A$, $C_{OUT} = 25\mu F$ Tantalum		72		dB
		LMS1585A-1.5EP				
		f _{BIPPLE} = 120Hz, C _{OUT} = 25µF Tantalum,				
		$I_{OUT} = 5A$, $V_{IN} = 4.5V$	60	72		dB
		LMS1585A-3.3EP				
		f _{RIPPLE} = 120Hz, C _{OUT} = 25μF Tantalum,				
		$I_{OUT} = 5A, V_{IN} = 6.3V$		72		dB
		LMS1587-ADJEP				
		$f_{RIPPLE} = 120Hz, V_{IN} - V_{OUT} = 3V, I_{OUT} = 3A$				
		C _{OUT} = 25µF Tantalum		72		dB
		LMS1587-1.5EP				
		$f_{RIPPLE} = 120Hz, C_{OUT} = 25\mu F Tantalum,$				
		$I_{OUT} = 3A$, $V_{IN} = 4.5$	60	72		dB
		LMS1587-3.3EP				
		$f_{RIPPLE} = 120Hz, C_{OUT} = 25\mu F Tantalum,$				
Adjust Pin Current		$I_{OUT} = 3A, V_{IN} = 6.3V$		72		dB
	Adjust Pin Current			55	120	μΑ
	Adjust Pin Current	10mA ≤ I _{OUT} ≤ I _{FULLLOAD} ,				
		$1.5V \le V_{\text{IN}} - V_{\text{OUT}} \le 5.75V \text{ (Note 11)}$		0.2		μΑ
	Temperature Stability	110 1001 0110 (1000 11)		0.5		%
	Long Term Stability	T _A = 125°C, 1000Hrs		0.03		%
	RMS Output Noise			0.003		%
	(% of V _{OUT})	10Hz ≤ f ≤ 10kHz		0.000		/0
	Thermal Resistance	3-Lead TO-263: Control/Output Section			0.65/2.7	°C/W
	Junction-to-Case	3-Lead TO-220: Control/Output Section			0.65/2.7	°C/W

Note 4: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not guaranteed. For guaranteed specifications and the test conditions, see the Electrical Characteristics.

Note 5: The maximum power dissipation is a function of $T_{J(max)}$, θ_{JA} , and T_A . The maximum allowable power dissipation at any ambient temperature is $P_D = (T_{J(max)} - T_A)/\theta_{JA}$. All numbers apply for packages soldered directly into a PC board.

Note 6: For testing purposes, ESD was applied using human body model, $1.5 k\Omega$ in series with 100pF.

Note 7: Typical Values represent the most likely parametric norm.

Note 8: All limits are guaranteed by testing or statistical analysis.

Note 9: Load and line regulation are measured at constant junction temperature, and are guaranteed up to the maximum power dissipation of 30W. Power dissipation is determined by the input/output differential and the output current. Guaranteed maximum power dissipation will not be available over the full input/output range.

Note 10: The minimum output current required to maintain regulation.

Note 11: I_{FULLLOAD} is 5A for LMS1585AEP and 3A for LMS1587EP.

Note 12: Consult factory for other fixed voltage options.

Application Note

Output Voltage

The adjustable version develops at 1.25V reference voltage, (V_{REF}), between the output and the adjust terminal. As shown in *Figure 1*, this voltage is applied across resistor R1 to generate a constant current I1. This constant current then flows through R2. The resulting voltage drop across R2 adds to the reference voltage to sets the desired output voltage.

The current I_{ADJ} from the adjustment terminal introduces an output error. But since it is small (120 μ A max), it becomes negligible when R1 is in the 100 Ω range.

For fixed voltage devices, R1 and R2 are integrated inside the devices.

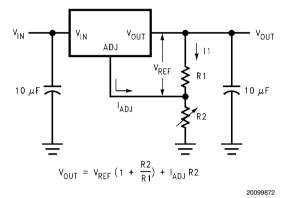
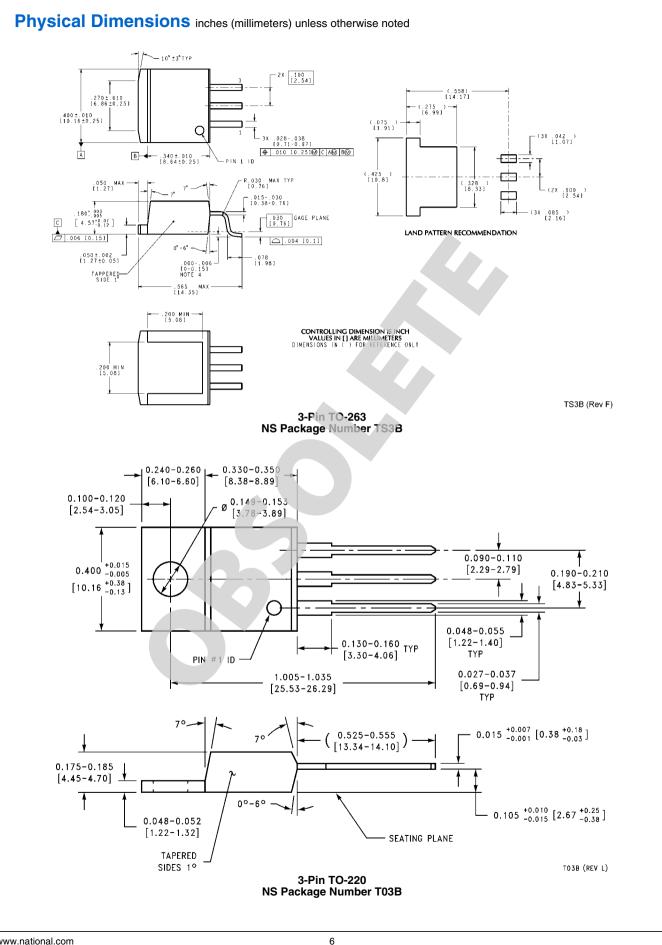
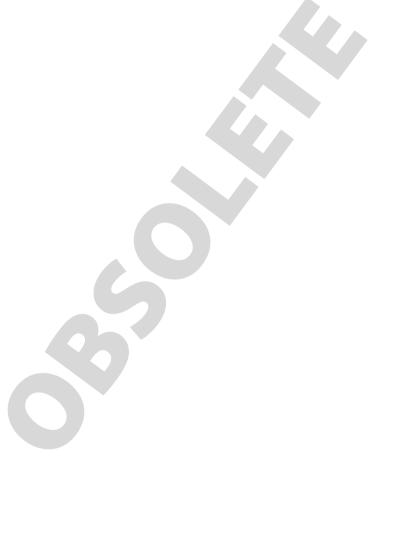


FIGURE 1. Basic Adjustable Regulator







Notes

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