MODEL NO.: _	LMT057DNAFWD-NNA-1
ISSUED DATE:_	2016-04-05
VERSION :	Ver 0.2

■ Preliminary Specification□ Final Product Specification

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Approved by	Notes						

Confirmed:

Prepared by	Checked by	Approved by
HT LIU		

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LMT057DNAFWD-NNA-1

Record of Revision

Rev	Issued Date	Description	Editor
0.1	2016-01-30	Preliminary release.	Zou Guo Rui
0.2	2016-04-05	Typing Correction	HT LIU

1. General Specifications

	Feature	Spec	
	Size	5.7 inch	
	Resolution	320(RGB) x 240	
	Interface	RGB 18 bits	
	Color Depth	262K	
Display Spec.	Technology Type	a-Si	
Display speed	Pixel Pitch (mm)	0.360x0.360	
	Pixel Configuration	R.G.B. Vertical Stripe	
	Display Mode	TM with Normally White	
	Surface Treatment(Up Polarizer)	Anti-Glare(3H)	
	Viewing Direction	6 o'clock	
	Gray Scale Inversion Direction	12 o'clock	
	LCM (W x H x D) (mm)	144.00x104.60x13.50	
	Active Area(mm)	115.20x86.40	
Mechanical	With /Without TSP	With TSP	
Characteristics	Weight (g)	TBD	
	LED Numbers	15 LEDs	

Note 1: Viewing direction for best image quality is different from TFT definition, there is a 180 degree shift.

Note 2: Requirements on Environmental Protection: Q/S0002

Note 3: LCM weight tolerance: ± 5%

2. Input/Output Terminals

2.1 CN1 pin assignment (Signal interface)

Connector type: 089H33-000100-G2-R (STARCONN)

1	No	Symbol	I/O	Description	Comment
Hsync	1	GND	Р	Ground	
Vertical sync signal in SYNC mode. Pull low or floating in DE mode.	2	DOTCLK	I	Dot clock. Latch data at falling edge of DOTCLK.	
Pull low or floating in DE mode.	3	Hsync	I	Pull low or floating in DE mode.	
6 R0 I Red data (LSB) 7 R1 I Red data 8 R2 I Red data 9 R3 I Red data 10 R4 I Red data 10 R4 I Red data 11 R5 I Red data (MSB) 12 GND P Ground 13 G0 I Green data(LSB) 14 G1 I Green data 15 G2 I Green data 16 G3 I Green data 17 G4 I Green data(MSB) 19 GND P Ground 20 B0 I Blue data(LSB) 21 B1 I Blue data 22 B2 I Blue data 23 B3 I Blue data 24 B4 I Blue data(MSB) 26 <td>4</td> <td>Vsync</td> <td>Į</td> <td></td> <td></td>	4	Vsync	Į		
7 R1 I Red data 8 R2 I Red data 9 R3 I Red data 10 R4 I Red data 10 R4 I Red data 11 R5 I Red data (MSB) 11 R5 I Red data (MSB) 12 GND P Ground 13 G0 I Green data 15 G2 I Green data 16 G3 I Green data 17 G4 I Green data 18 G5 I Green data(MSB) 19 GND P Ground 20 B0 I Blue data(LSB) 21 B1 I Blue data 22 B2 I Blue data 23 B3 I Blue data 24 B4 I Blue data(MSB) 26	5	GND	Р	Ground	
8 R2 I Red data 9 R3 I Red data 10 R4 I Red data 11 R5 I Red data (MSB) 11 R5 I Red data (MSB) 12 GND P Ground 13 G0 I Green data 15 G2 I Green data 16 G3 I Green data 17 G4 I Green data 18 G5 I Green data 19 GND P Ground 20 B0 I Blue data(LSB) 21 B1 I Blue data 22 B2 I Blue data 23 B3 I Blue data 24 B4 I Blue data(MSB) 26 GND P Ground 27 ENABLE I Data enable signal in DE mode. This pin must pull high i	6	R0	ı	Red data (LSB)	
9 R3 I Red data 10 R4 I Red data 11 R5 I Red data (MSB) 12 GND P Ground 13 G0 I Green data(LSB) 14 G1 I Green data 15 G2 I Green data 16 G3 I Green data 17 G4 I Green data 18 G5 I Green data(MSB) 19 GND P Ground 20 B0 I Blue data(LSB) 21 B1 I Blue data 22 B2 I Blue data 23 B3 I Blue data 24 B4 I Blue data(MSB) 26 GND P Ground 27 ENABLE I Data enable signal in DE mode. This pin must pull high in SYNC mode. 28 VCC P	7	R1	I	Red data	
10	8	R2	- 1	Red data	
11	9	R3	- 1	Red data	
12	10	R4	I	Red data	
13 G0	11	R5	I	Red data (MSB)	
14 G1 I Green data 15 G2 I Green data 16 G3 I Green data 17 G4 I Green data 18 G5 I Green data(MSB) 19 GND P Ground 20 B0 I Blue data(LSB) 21 B1 I Blue data 22 B2 I Blue data 23 B3 I Blue data 24 B4 I Blue data(MSB) 25 B5 I Blue data(MSB) 26 GND P Ground 27 ENABLE I Data enable signal in DE mode. This pin must pull high in SYNC mode. 28 VCC P Power supply 29 VCC P Power supply 30 R/L I Set horizontal scan direction: Low/NC: left to right; High: right to left 31 U/D I Set vertical scan dir	12	GND	Р	Ground	
15 G2	13	G0	I	Green data(LSB)	
16	14	G1	I	Green data	
17 G4 I Green data 18 G5 I Green data(MSB) 19 GND P Ground 20 B0 I Blue data(LSB) 21 B1 I Blue data 22 B2 I Blue data 23 B3 I Blue data 24 B4 I Blue data 25 B5 I Blue data(MSB) 26 GND P Ground 27 ENABLE I Data enable signal in DE mode. This pin must pull high in SYNC mode. 28 VCC P Power supply 29 VCC P Power supply 30 R/L I Set horizontal scan direction: Low/NC: left to right; High: right to left 31 U/D I Set vertical scan direction: High/NC: up to down; Low: down to up 32 NC - No connection	15	G2	I	Green data	
18	16	G3	I	Green data	
19 GND P Ground 20 B0 I Blue data(LSB) 21 B1 I Blue data 22 B2 I Blue data 23 B3 I Blue data 24 B4 I Blue data 25 B5 I Blue data(MSB) 26 GND P Ground 27 ENABLE I Data enable signal in DE mode. This pin must pull high in SYNC mode. 28 VCC P Power supply 29 VCC P Power supply 30 R/L I Set horizontal scan direction: Low/NC: left to right; High: right to left 31 U/D I Set vertical scan direction: High/NC: up to down; Low: down to up 32 NC - No connection	17	G4	I	Green data	
20 B0 I Blue data(LSB)	18	G5	I	Green data(MSB)	
21 B1 I Blue data 22 B2 I Blue data 23 B3 I Blue data 24 B4 I Blue data 25 B5 I Blue data(MSB) 26 GND P Ground 27 ENABLE I Data enable signal in DE mode. This pin must pull high in SYNC mode. 28 VCC P Power supply 29 VCC P Power supply 30 R/L I Set horizontal scan direction: Low/NC: left to right; High: right to left 31 U/D I Set vertical scan direction: High/NC: up to down; Low: down to up 32 NC - No connection	19	GND	Р	Ground	
22 B2 I Blue data 23 B3 I Blue data 24 B4 I Blue data 25 B5 I Blue data(MSB) 26 GND P Ground 27 ENABLE I Data enable signal in DE mode. This pin must pull high in SYNC mode. 28 VCC P Power supply 29 VCC P Power supply 30 R/L I Set horizontal scan direction: Low/NC: left to right; High: right to left 31 U/D I Set vertical scan direction: High/NC: up to down; Low: down to up 32 NC - No connection	20	В0	I	Blue data(LSB)	
23 B3 I Blue data 24 B4 I Blue data 25 B5 I Blue data(MSB) 26 GND P Ground 27 ENABLE I Data enable signal in DE mode. This pin must pull high in SYNC mode. 28 VCC P Power supply 29 VCC P Power supply 30 R/L I Set horizontal scan direction: Low/NC: left to right; High: right to left 31 U/D I Set vertical scan direction: High/NC: up to down; Low: down to up 32 NC - No connection	21	B1	I	Blue data	
24 B4 I Blue data 25 B5 I Blue data(MSB) 26 GND P Ground 27 ENABLE I Data enable signal in DE mode. This pin must pull high in SYNC mode. 28 VCC P Power supply 29 VCC P Power supply 30 R/L I Set horizontal scan direction: Low/NC: left to right; High: right to left 31 U/D I Set vertical scan direction: High/NC: up to down; Low: down to up 32 NC - No connection	22	B2	I	Blue data	
25 B5 I Blue data(MSB) 26 GND P Ground 27 ENABLE I Data enable signal in DE mode. This pin must pull high in SYNC mode. 28 VCC P Power supply 29 VCC P Power supply 30 R/L I Set horizontal scan direction: Low/NC: left to right; High: right to left 31 U/D I Set vertical scan direction: High/NC: up to down; Low: down to up 32 NC - No connection	23	В3	I	Blue data	
26 GND P Ground 27 ENABLE I Data enable signal in DE mode. This pin must pull high in SYNC mode. 28 VCC P Power supply 29 VCC P Power supply 30 R/L I Set horizontal scan direction: Low/NC: left to right; High: right to left 31 U/D I Set vertical scan direction: High/NC: up to down; Low: down to up 32 NC - No connection	24	B4	I	Blue data	
27 ENABLE I Data enable signal in DE mode. This pin must pull high in SYNC mode. 28 VCC P Power supply 29 VCC P Power supply 30 R/L I Set horizontal scan direction: Low/NC: left to right; High: right to left 31 U/D I Set vertical scan direction: High/NC: up to down; Low: down to up 32 NC - No connection	25	B5	I	Blue data(MSB)	
This pin must pull high in SYNC mode. 28	26	GND	Р	Ground	
29 VCC P Power supply 30 R/L I Set horizontal scan direction: Low/NC: left to right; High: right to left 31 U/D I Set vertical scan direction: High/NC: up to down; Low: down to up 32 NC - No connection	27	ENABLE	I		
30 R/L I Set horizontal scan direction: Low/NC: left to right; High: right to left 31 U/D I Set vertical scan direction: High/NC: up to down; Low: down to up 32 NC - No connection	28	VCC	Р	Power supply	
Low/NC: left to right; High: right to left Set vertical scan direction: High/NC: up to down; Low: down to up No connection	29	VCC	Р	Power supply	
High/NC: up to down; Low: down to up NC - No connection	30	R/L	I		
	31	U/D	I		
33 GND P Ground	32	NC	-	No connection	
	33	GND	Р	Ground	

Note1: I/O definition: I----Input O----Output P----Power/Ground Note2: CN1 Matching FPC type: 33 pin, pitch: 0.5mm, height: 0.3mm.

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2.2 CN2 pin assignment (Backlight interface)

Connector type: SHLP-06V-S-B (JST)

No	Symbol	I/O	Description	Comment
1	AN1	Р	LED driving anode 1 (high voltage)	
2	AN2	Р	LED driving anode 2 (high voltage)	
3	AN3	Р	LED driving anode 3 (high voltage)	
4	CA1	Р	LED driving cathode 1 (low voltage)	
5	CA2	Р	LED driving cathode 2 (low voltage)	
6	CA3	Р	LED driving cathode 3 (low voltage)	

Note1: CN2 Matching Connector type: SM06B-SHLS-TF (JST)

3 Absolute Maximum Ratings

3.1 Driving TFT LCD Panel

GND=0V,Ta=25℃

Item	Symbol	Min	Max	Unit	Remark
Power Voltage	VCC	-0.5	5.0	V	
Input voltage	Vin	-0.5	5.0	V	Note2
Operating Temperature	Тор	-20	70	${\mathbb C}$	Note1
Storage Temperature	Tst	-30	85	${\mathbb C}$	Note1

Note1: The parameter is for driver IC (gate driver, source driver) only.

Note2: Signals include R0~R5, G0~G5, B0~B5, DOTCLK, Hsync, Vsync, Enable, R/L, U/D.

Table 3.1 absolute maximum rating

4 Electrical Characteristics

4.1 Driving TFT LCD Panel

GND=0V, Ta=25°C

	Item	Symbol	Min	Тур	Max	Unit	Remark
Supply Voltag	е	VCC	3.0	3.3	3.6	V	
Permissive inp voltage	out ripple	VRF	-	ı	100	mVp-p	VCC=3.3V
Input Signal	Low Level	VIL	0	-	0.3xVCC	V	R0~R5;G0~G5;B0~B5 DOTCLK; Hsync; Vsync
Voltage	High Level	VIH	0.7xVCC	1	VCC	V	ENABLE;R/L;U/D
Common Electric Driving Signal		VCOM	-	4.56	-	V	Note1
Current of VC supply	C Power	Ivcc	-	145	225	mA	Note2

Note1: For different LCM, the value may have a bit of difference.

Note2: To test the current dissipation, use "all Black Pattern".

Table 4.1 LCD module electrical characteristics

4.2 Driving Backlight

Item	Symbol	Min	Тур	Max	Unit	Remark
Channel1	Ichannel 1	-	25.0	-	mA	
Channel2	Ichannel 2	-	25.0	-	mA	Note 1
Channel3	Ichannel 3	-	25.0	-	mA	
Forward Voltage	VBL	14.85	-	18.15	V	
Backlight Power Consumption	WBL	-	1,238	-	mW	
Life Time	-	25,000	(50,000)		Hrs	Note 3

Note 1: IF is defined for one channel LED. There are total three LED channels in back light unit

Note 2: Optical performance should be evaluated at Ta=25 °C only.

Note 3: If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.

Table 4.2 LED backlight characteristics

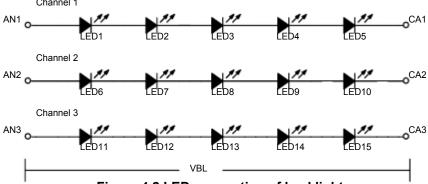
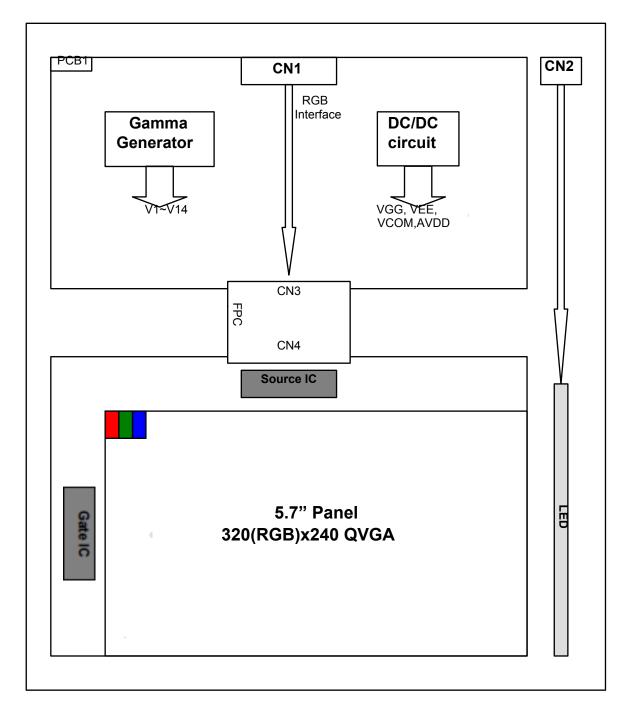


Figure 4.2 LED connection of backlight

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4.3 Block Diagram



5. Data input timing

5.1 SYNC mode

Parameter	Symbol	Symbol	Min	Тур	Max	Unit
DOTCLK	DOTCLK frequency	Fclk	6.2	6.4	12.1	MHz
DOTOLIK	DOTCLK cycle	Tclk	82.64	156.25	161.29	ns
	Horizontal display area	Thd	320	320	320	Tclk
Llovena	1 horizontal line	Th	406	408	560	Tclk
Hsync	Hsync pulse width	Thpw	1	-	-	Tclk
	Horizontal blanking	Thb	70	70	70	Tclk
	Horizontal front porch	Thfp	16	18	170	Tclk
	Frame rate	-	-	60	65	Hz
	Vertical display area	Tvd	240	240	240	Th
Vsync	Vsync period time	Tv	254	263	360	Th
	Vsync pulse width	Tvpw	1	-	-	Th
	Vsync blanking	Tvb	13	13	13	Th
	Vsync front porch	Tvfp	1	10	107	Th

Table 5.1 SYNC mode

5.2 DE mode

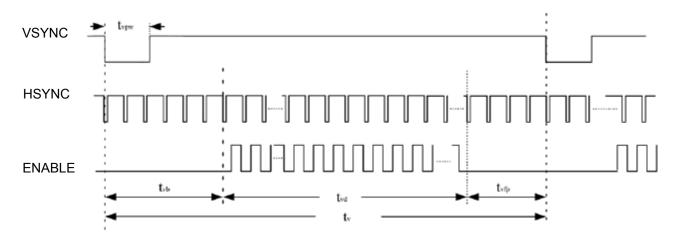
Des	Symbol	Min.	Тур.	Max.	Unit	
DOTCLK frequency		Fclk	6.2	6.4	12.1	MHz
Horizontal	Horizontal total	Th	406	408	560	Tclk
section	H Total blank	Thb+Thfp	86	88	240	Tclk
3660011	Valid Data Width	Thd	320	320	320	Tclk
	Frame rate	-	-	60	65	Hz
Vertical	Vertical total	Tv	254	263	360	Th
section	V total blank	Tvb+Tvfp	14	23	120	Th
	Valid Data Width	Tvd	240	240	240	Th

Note: The LCM could auto-detect which mode is working.

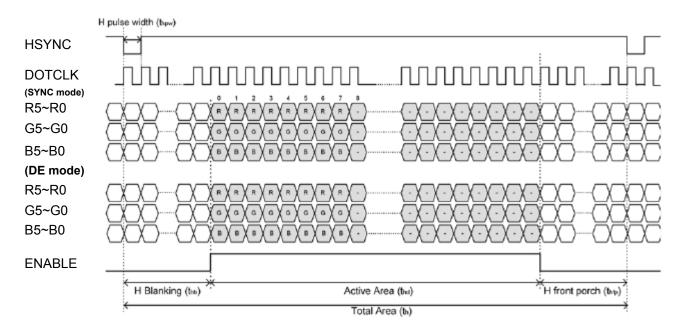
Table 5.2 DE mode

5.3 Timing Diagram

5.3.1 Vertical Input Timing



5.3.2 Horizontal Input Timing



5.4 AC input characteristics

(VCC=3.3V, GND=0V, Ta=25°C)

Parameter	Symbol	Min	Тур	Max	Unit	Conditions
DOTCLK pulse duty	Tcwh	40%	50%	60%	Tclk	Tcph is DCLK cycle
VSYNC setup time	Tvst	10	-	-	ns	
VSYNC hold time	Tvhd	10	-	-	ns	
HSYNC setup time	Thst	10	-	-	ns	
HSYNC hold time	Thhd	10	-	-	ns	
Data setup time	Tdsu	10	-	-	ns	Rn, Gn, Bn to DCLK
Data hold time	Tdhd	10	-	-	ns	Rn, Gn, Bn to DCLK
Enable setup time	Tesu	10			ns	

Table 5.4 AC input characteristics

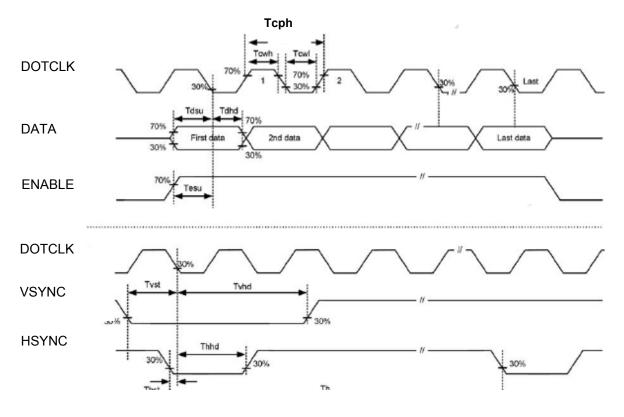


Figure 5.4 AC input characteristics

5.5 Power ON/OFF Sequence

Item	Symbol	Min	Тур	Max	Unit	Remark
VCC 3.0V to signal starting	Tp1	5	-	50	ms	
Signal starting to backlight on	Tp2	50	-	-	ms	
Signal off to VCC 3.0V	Tp3	5	-	50	ms	
Backlight off to signal off	Tp4	50	-	-	ms	

Table 5.5 Power on/off sequence

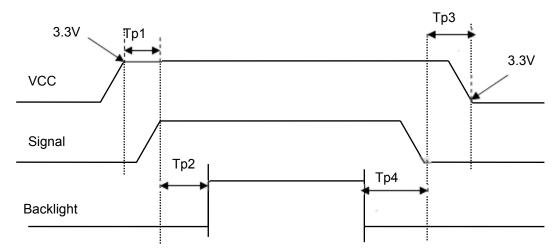


Figure 5.5 Power on/off sequence

6. Optical Characteristics

Item		Symbol	Condition	Min	Тур	Max	Unit	Remark
				60	70			
View Angles		θВ	CR≧10	50	60		Degree	Note 2
				60	70			
		θR		60	70			
Contrast R	atio	CR	θ=0°	400	500			Note1、Note3
Response 7	Time	Ton	25 ℃		20	30	ms	Note1
·		Toff						Note4
	White	х	Backlight is	0.274	0.324	0.374		
		у		0.279	0.362	0.379		
	Red	Х		0.566	0.616	0.666		Note5
Chromaticity		у		0.303	0.353	0.403		
	Green	Х	on	0.285	0.335	0.385		Note1
		у		0.526	0.576	0.626		
	Blue	х		0.086	0.136	0.186		
		у		0.076	0.126	0.176		
Uniformi	Uniformity			75	80		%	Note1、Note6
NTSC					50		%	Note 5
Luminance		L			320		cd/m ²	Note1、Note7

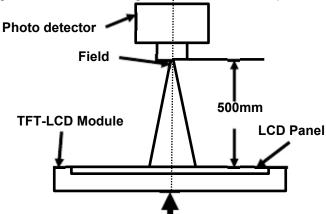
Test Conditions:

^{1.} IF= 25mA(one channel), VF=16.5V, the ambient temperature is 25° C.

^{2.} The test systems refer to Note 1 and Note 2.

Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 Minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.

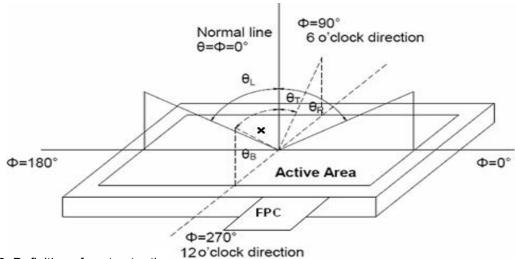


The center of the screen

Item	Photo detector	Field
Contrast Ratio		
Luminance	SR-3A	1°
Chromaticity		
Lum Uniformity		
Response Time	BM-7A	2°

Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).



Note 3: Definition of contrast ratio

Contrast ratio (CR) = $\frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$

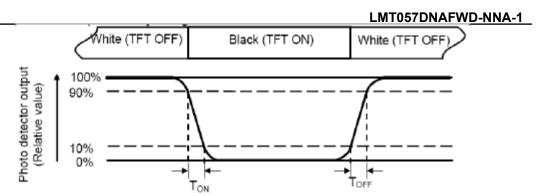
"White state ": The state is that the LCD should drive by Vwhite.

"Black state": The state is that the LCD should drive by Vblack.

Vwhite: To be determined Vblack: To be determined.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (Ton) is the time between photo detector output intensity changed from 90% to 10%. And fall time (Toff) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

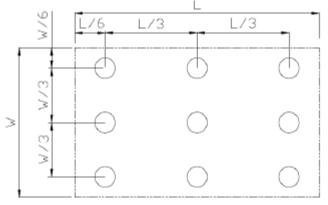
Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U) = Lmin/Lmax

L-----Active area length W----- Active area width



Lmax: The measured Maximum luminance of all measurement position.

Lmin: The measured Minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.

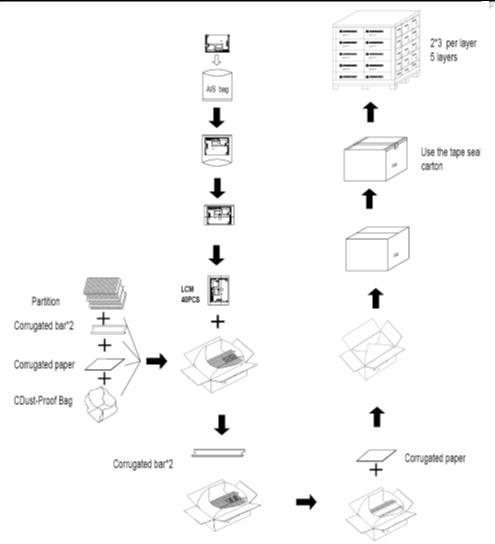
7. Environmental / Reliability Test

No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts = +70℃, 240 hours	Note1 IEC60068-2-1,GB2423.2
2	Low Temperature Operation	Ta = -20℃, 240 hours	IEC60068-2-1 GB2423.1
3	High Temperature Storage	Ta = +80°C, 240 hours	IEC60068-2-1 GB2423.2
4	Low Temperature Storage	Ta = -30℃, 240 hours	IEC60068-2-1 GB2423.1
5	Storage at High Temperature and Humidity	Ta = +60℃, 90% RH max,240hours	Note2 IEC60068-2-78 GB/T2423.3
6	Thermal Shock (non-operation)	-20°C 30 min~+60°C 30 min, Change time:5min, 100 Cycle	Start with cold temperature, End with high temperature, IEC60068-2-14,GB2423.22
7	ESD	C=150pF,R=330Ω,5point/panel Air:±15Kv,5times; Contact:±8Kv,5times (Environment:15°C~35°C 30%~60%.86Kpa~106Kpa)	IEC61000-4-2 GB/T17626.2
8	Vibration Test	Frequency range:10~200Hz Stroke:1.5mm Sweep:10Hz~200Hz~10Hz 30 minutes for each direction of X.Y.Z. (1.5 hours for total)	IEC60068-2-6 GB/T2423.10
9	Mechanical Shock (Non Op)	Half Sine Wave 50G 20ms, ±X,±Y,±Z 3times for each direction	IEC60068-2-27 GB/T2423.5
10	Package Drop Test	Height:80cm, 1corner,3edges,6surfaces	IEC60068-2-32 GB/T2423.8
11	Package Vibration Test	Random Vibration: 0.015G*G/Hz for 5-200Hz, -6dB/Octave from 200-500Hz 2 hours for each direction of X,Y,Z (6 hours for total)	IEC60068-2-34 GB/T2423.11

Note1: Ts is the temperature of panel's surface. Note2: Ta is the ambient temperature of samples.

8. Packing Drawing

No	Item	Model(Material)	Dimensions (mm)	Unit Weigt (Kg)	Quantity	Remark		
1	LCM module	TM057KBH01-00	144X104.6X12.3	TBD	40			
2	Partition_1	Corrugated paper	513X333X215	1.388	1			
3	Anti-static Bag	PE	180X165X0.05	0.001	40	Anti-static		
4	Dust-Proof Bag	PE	700X530	0.06	1			
5	Partition_2	Corrugated Paper	505X332X4.0	0.098	2			
6	Corrugated Bar	Corrugated paper	513X110×31	0.048	4			
7	Carton	Corrugated paper	530X350X250	1.12	1			
8	Total weight	TBD						



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9. Precautions for Use of LCD Modules

9.1 Handling Precautions

- 9.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 9.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 9.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 9.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 9.1.5 If the display surface is contaMinated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 9.1.6 Do not attempt to disassemble the LCD Module.
- 9.1.7 If the logic circuit power is off, do not apply the input signals.
- 9.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - 9.1.8.1 Be sure to ground the body when handling the LCD Modules.
 - 9.1.8.2 Tools required for assembly, such as soldering irons, must be properly ground.
- 9.1.8.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
- 9.1.8.4 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

9.2 Storage precautions

- 9.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 9.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0°C ~ 40°C Relatively humidity: ≤80%

9.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

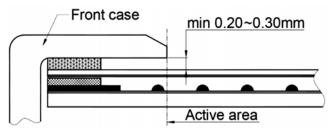
9.3 Transportation Precautions

9.3.1 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

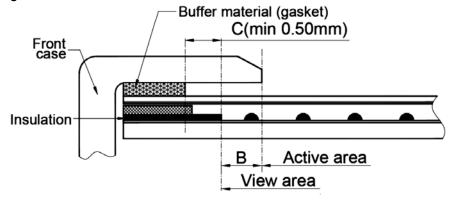
附录: Touch panel Design Precautions

1. It should prevent front case touching the touch panel Active Area (A.A.) to prevent abnormal touch.

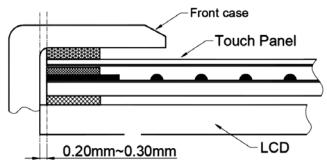
It should left gab (e.g. 0.2~0.3mm) in between.



Outer case design should take care about the area outside the A.A.
 Those areas contain circuit wires which is having different thickness. Touching those areas could deform the ITO film. As a result case the ITO cold be damaged and shorten its lifetime.
 It is suggested to protect those areas with gasket (between the front case and the touch panel).
 The suggested figures are B≥0.50mm; C≥0.50mm.



3. The front case side wall should keep space (e.g. $0.2 \sim 0.3$ mm) from the touch panel.



 In general design, touch panel V.A. should be bigger than the LCD V.A. and touch panel A.A. should be bigger than the LCD A.A.

