

Low Current Consumption, High Sensitivity CMOS Hall IC

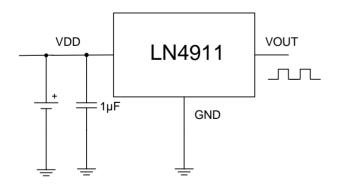
■ General Description

LN4911 is a Hall IC (a magnetic sensor) which has 2 times or more sensitivity and a low current consumption of about one fifties compared with our conventional one. Precise magnetic switching points and high temperature stability are achieved through the unique design of the internal circuit.

An onboard clock scheme is used to reduce the average operating current of the IC. During the operate phase the IC compares the actual magnetic field detected with the internally compensated switching points. The output Q is switched at the end of each operating phase. If the magnetic field perpendicular to enter the chip and the N-level magnetic field strength reaches a certain value of the CMOS output changes from high to low, and have been known to maintain a low state of the magnetic field perpendicular to enter the S-class chips, then the output again from low to high, the formation of cross-sensitive magnetic field detection effect.

LN4911 use of ultra-small package, can be widely applied N, S-level change detection in interactive environments, such as mobile phones trackball detection occasions.

■ Typical Application Circuit



Applications

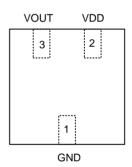
- Function key operation
- Low-speed rotary detector
- Mouse

■ Features

- Micro power design
- 2.4 V to 5.5 V battery operation
- High sensitivity and high stability of the magnetic switching points
- High resistance to mechanical stress
- Digital output signal
- interactive model of the magnetic field induction

Package

DFN2×2-3L



■ Pin Assignment

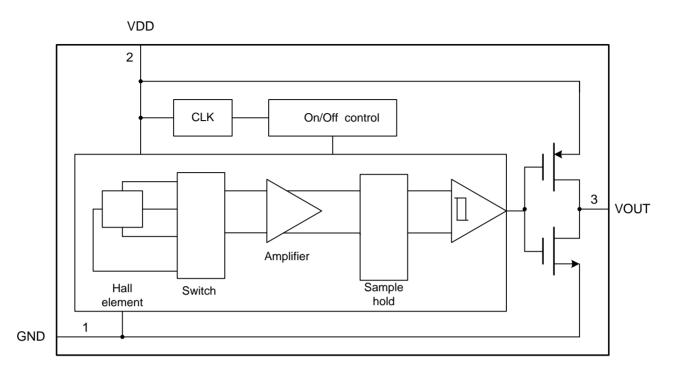
Pin Number	Symbol	Function Description
1	GND	Ground Pin
2	VDD	Supply Voltage Pin
3	VOUT	Output Pin

Ordering Information

Part Number	Package	Lot Number
LN4911LD	DFN2×2-3L	4911



■ Function Block Diagram



■ Absolute Maximum Ratings

Symbol	Characteristics	Values	Unit
Vdd	Supply voltage	-0.3—6.0	V
Is	Operating current	-1-5.0	mA
V _{OUT}	Output voltage	-0.3-6.0	V
I _{OUT}	Output current	-1-2.0	mA
Ts	Storage temperature range	-40—150	°C
TJ	Maximum junction temperature	150	°C
-	ESD Protection	4000	V



■ Electrical Characteristics

AC/DC Characteristics (T_A=+25 °C, V_{DD}=3.0V, Unless otherwise specified)

Symbol	Characteristic	Conditions	Min	Тур	Max	Unit
VDD	Supply voltage	_	2.4	_	6.0	V
I _{SAVG}	Averaged supply current		30	40	60	uA
I _{SOPAVG}	Averaged current during operating time		0.5	2.0	3.5	mA
I _{SOPT}	Peak current during operating time				4.5	mA
I _{SSTB}	Supply current during standby time		1	2.5	8	uA
V _{OUT1}	Output voltage 1	IOUT=2mA, B=6mT		0.1	0.3	V
V _{OUT2}	Output voltage 2	IOUT=-2mA, B=-6mT	2.7	2.9		V
t _{op}	Operating time		18	26	36	us
t _{stb}	Standby time		490	670	850	us
t _{stu}	Start-up time of IC			12	20	us

■ Mangentic Characteristics

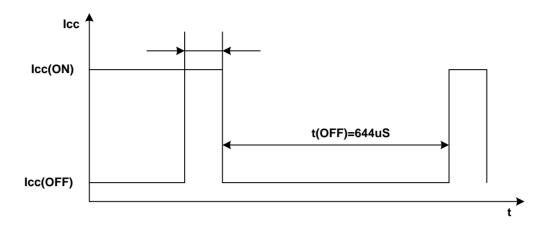
(T_A=+25°C,V_{DD}=3V, Unless otherwise specified)

Symbol	Parameter	Min	Тур	Max	Unit
BHL	Forward flip the magnetic field density	0.5		6	mT
BLH	Reverse flip the magnetic field density	-0.5		-6	mT
BW	Magnetic hysteresis bandwidth		7		mT

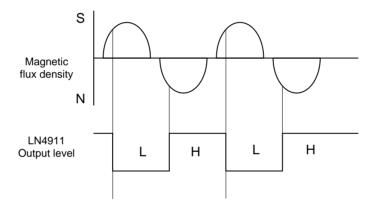


Operating Characteristics

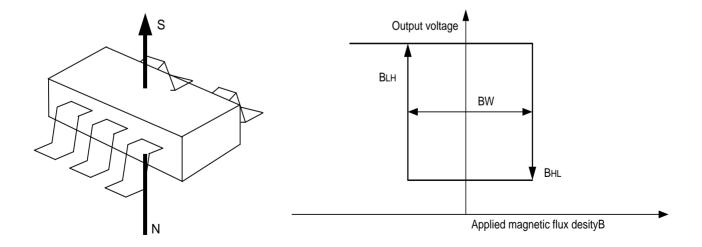
• lcc vdd t timing diagram



• Magnetic flux density vdd output level



Magneto-electro conversion characteristics

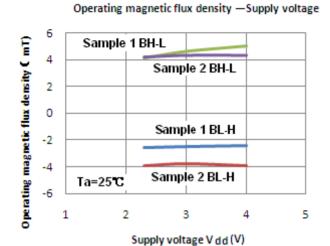


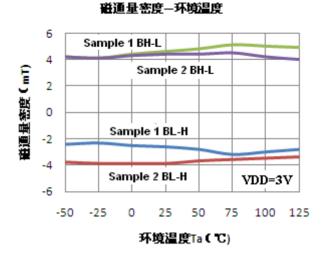
Direction of applied magnetic field

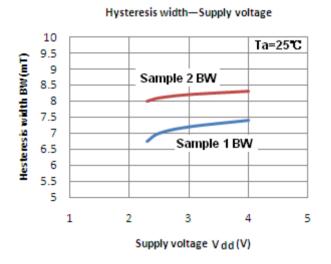
Operating magnetic flux density

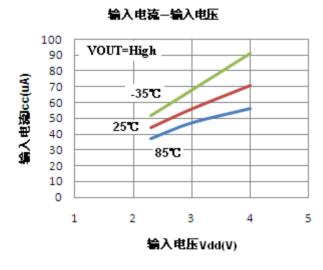


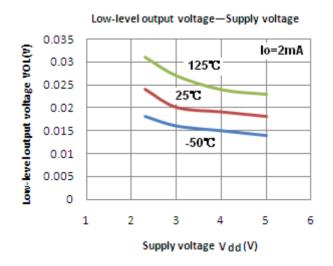
Typical Operating Characteristics

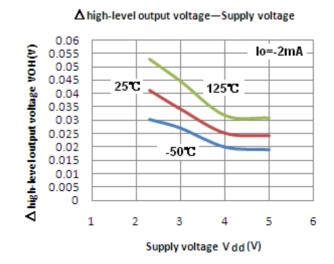






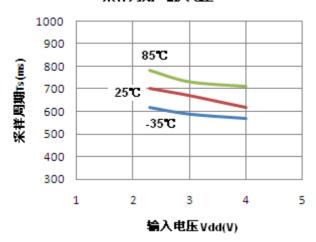






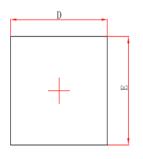


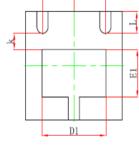
采样周期-输入电压

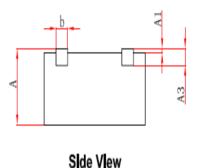


■ Package

● DFN2×2-3L







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Bottom View

Dimensions In Millimeters Dimensions In Inches Symbol Min. Min. Max. Max. 0.028/0.031 0.700/0.800 0.800/0.900 0.031/0.035 Α 0.000 0.050 0.000 **A1** 0.002 0.008REF. 0.203REF. А3 D 1.900 2.100 0.075 0.083 2.100 0.075 0.083 Ε 1.900 D₁ 1.220 1.420 0.048 0.056 E1 0.780 0.980 0.031 0.039 0.200MIN. 0.008MIN. k 0.300 0.180 0.007 0.012 b 0.650TYP. 0.026TYP. е 0.300 0.500 0.012 0.020