

## Integrated Dual LDO And Single Step Down DC-DC

### General Description

The LN5066 is a complex power management, which can provide two roads of low-noise high-speed LDOs and high efficiency reached 95% of the DC-DC buck. As the use of CMOS process realization of the work of the chip consumes very little current, the internal use of low-resistance, makes the LDO's output current of up to 300mA, DC-DC load current of up to 800mA.

A built-in low on-resistance transistor provides a low dropout voltage and large output current, a built-in overcurrent protector prevents the load current from exceeding the current capacitance of the output transistor, and a built-in thermal shutdown circuit prevents damage caused by the heat. Small DFN3×3-10 package realize high-density mounting.

### Applications

- mobile phones and other handheld electronic products
- DVD
- Battery powered equipment

### Ordering Information

LN5066B①②③④⑤⑥⑦

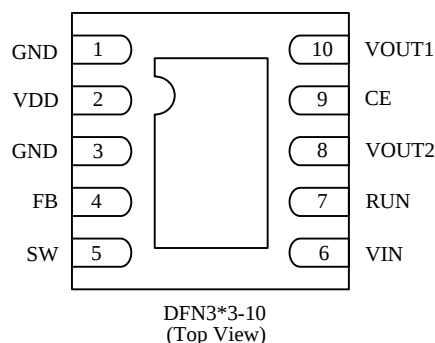
| Designator | Symbol | Description                  |
|------------|--------|------------------------------|
| ①②         | 18-33  | Output voltage of LDO1       |
| ③④         | 18-33  | Output voltage of LDO2       |
| ⑤          | A      | Adjustable Output            |
|            | others | Fixed Voltage Output         |
| ⑥          | D      | Package type : DFN3×3-10L    |
| ⑦          | R      | Embossed Tape: Standard Feed |
|            | L      | Embossed Tape: Reverse Feed  |

### Features

- Highly accurate : LDO :  $\pm 2\%$ ; DC-DC :  $\pm 2.5\%$
- Output current range : LDO:300mA; DC-DC:800mA
- High ripple rejection : 70dB@(1KHz,50mA)
- Low power consumption : 150 $\mu$ A (TYP.)
- Shutdown current : less than 0.1 $\mu$ A
- Internal protector : current limiter ,short protector and thermal shutdown protector
- Small package : DFN3×3-10

### Package

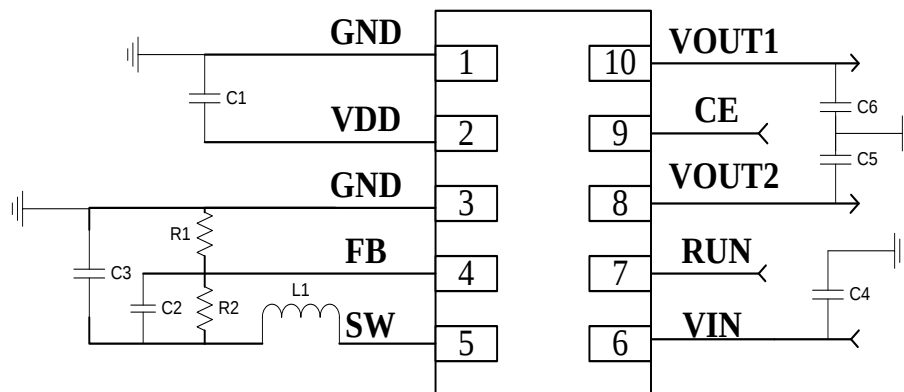
- DFN3×3-10



## ■ Pin Assignment

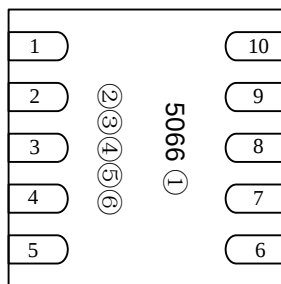
| Pin Number | Pin Name | Function Description                          |
|------------|----------|-----------------------------------------------|
| 1          | LGND     | Ground of LDO                                 |
| 9          | CE       | Enable of LDO, high active                    |
| 2          | VDD      | Supply Power of LDO                           |
| 4          | FB       | Feedback of DC-DC                             |
| 7          | RUN      | Enable of DC-DC, high active                  |
| 6          | VIN      | Input of DC-DC                                |
| 5          | SW       | Output of DC-DC                               |
| 3          | DGND     | Ground of DC-DC                               |
| 10         | VOUT1    | Output of LDO1( Low output voltage terminal)  |
| 8          | VOUT2    | Output of LDO2( high output voltage terminal) |

## ■ Typical Application Circuit



## ■ Marking Rule

### ● DFN3×3-10



DFN3\*3-10  
(Top View)

#### ① Represents the product lot

Symbol a-z,A-Z(except G, I, J, O, Q, W)

#### ② ③ Represents the output voltage1 of LDO

33 represents 3.3V, 18 represents 1.8V

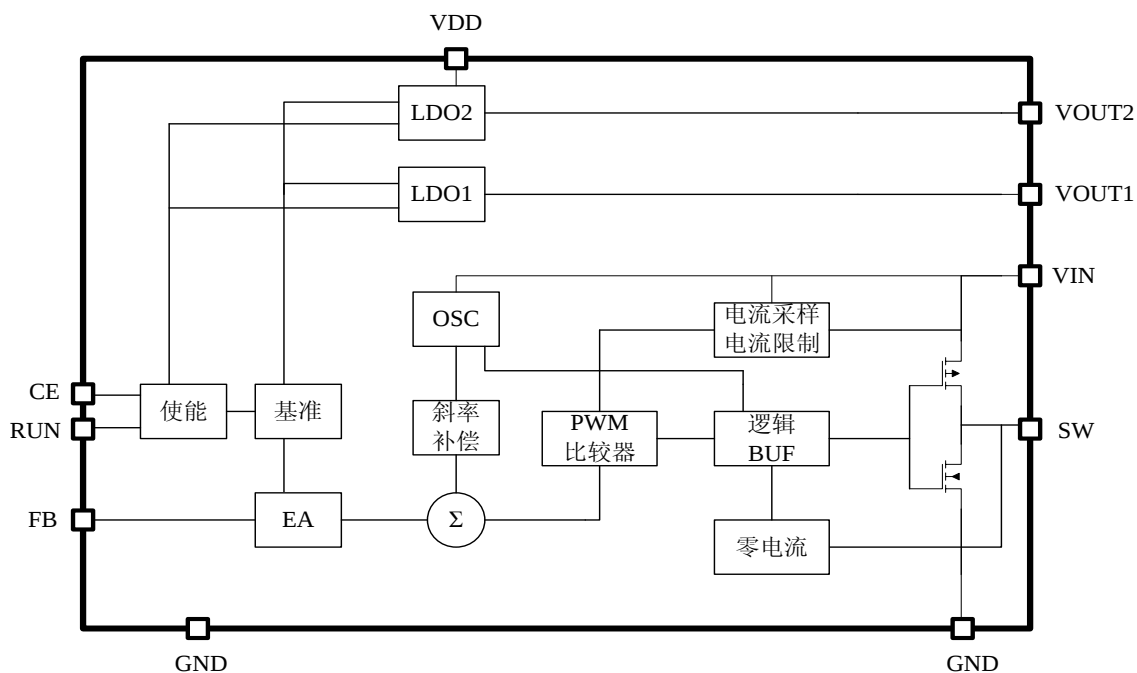
#### ④⑤ Represents the output voltage2 of LDO

33 represents 3.3V, 18 represents 1.8V

#### ⑥ Represents the output voltage of DC-DC

| Symbol | Output Voltage(V) | Symbol | Output Voltage(V) | Symbol | Output Voltage(V) | Symbol | Output Voltage(V) |
|--------|-------------------|--------|-------------------|--------|-------------------|--------|-------------------|
| A      | Adjustable        | N      | 1.8               | a      | 2.8               | n      | 3.8               |
| B      | 0.9               | P      | 1.9               | b      | 2.9               | p      | 3.9               |
| C      | 1.0               | R      | 2.0               | c      | 3.0               | r      | 4.0               |
| D      | 1.1               | S      | 2.1               | d      | 3.1               | s      | 4.1               |
| E      | 1.2               | T      | 2.2               | e      | 3.2               | t      | 4.2               |
| F      | 1.3               | U      | 2.3               | f      | 3.3               | u      | 4.3               |
| H      | 1.4               | V      | 2.4               | h      | 3.4               | v      | 4.4               |
| K      | 1.5               | X      | 2.5               | k      | 3.5               | x      | 4.5               |
| L      | 1.6               | Y      | 2.6               | l      | 3.6               | y      | 4.6               |
| M      | 1.7               | Z      | 2.7               | m      | 3.7               | z      | 4.7               |

## Function Block Diagram



## Absolute Maximum Ratings

| Parameter                     | Symbol              | Maximum Rating                            |     | Unit |
|-------------------------------|---------------------|-------------------------------------------|-----|------|
| Input Voltage                 | V <sub>IN</sub>     | V <sub>SS</sub> -0.3~V <sub>SS</sub> +6   |     | V    |
|                               | V <sub>ON/OFF</sub> | V <sub>SS</sub> -0.3~V <sub>IN</sub> +0.3 |     |      |
| Output voltage                | V <sub>OUT</sub>    | V <sub>SS</sub> -0.3~V <sub>IN</sub> +0.3 |     |      |
| Power Dissipation             | P <sub>D</sub>      | DFN3×3-10                                 | 1.2 | W    |
| Operating Ambient Temperature | Topr                | -40~+85                                   |     | °C   |
| Storage Temperature           | Tstg                | -40~+125                                  |     |      |

**Caution:** The absolute maximum ratings are rated values exceeding which the product could suffer physical damage. These values must therefore not be exceeded under any conditions.

## ■ Electrical Characteristics

### ● LDO electrical characteristics

| Parameter                                  | Symbol                                                  | Condition                                                                                                                  | Min                      | Typ          | Max                      | Unit                  | Test Circuit |
|--------------------------------------------|---------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------|--------------------------|--------------|--------------------------|-----------------------|--------------|
| Output Voltage                             | $V_{OUT(E)}$                                            | $V_{IN} = V_{OUT(S)} + 1.0 \text{ V}$ , $I_{OUT} = 30 \text{ mA}$                                                          | $V_{OUT(S)} \times 0.98$ | $V_{OUT(S)}$ | $V_{OUT(S)} \times 1.02$ | V                     | 1            |
| Output Current                             | $I_{OUT}$                                               | $V_{IN} \geq V_{OUT(S)} + 1.0 \text{ V}$                                                                                   | 300                      | —            | —                        | mA                    | 1            |
| Dropout Voltage                            | $V_{drop}$                                              | $I_{OUT} = 50 \text{ mA}$                                                                                                  | —                        | 0.06         | 0.10                     | V                     | 1            |
|                                            |                                                         | $I_{OUT} = 100 \text{ mA}$                                                                                                 | —                        | 0.15         | 0.20                     |                       |              |
| Line Regulations                           | $\frac{\Delta V_{OUT1}}{\Delta V_{IN} \bullet V_{OUT}}$ | $V_{OUT(S)} + 0.5 \text{ V} \leq V_{IN} \leq 8 \text{ V}$ , $I_{OUT} = 10 \text{ mA}$                                      | —                        | 0.01         | 0.2                      | %/V                   |              |
| Load Regulation                            | $\Delta V_{OUT2}$                                       | $V_{IN} = V_{OUT(S)} + 1.0 \text{ V}$<br>$1.0 \text{ mA} \leq I_{OUT} \leq 100 \text{ mA}$                                 | —                        | 15           | 50                       | mV                    |              |
| Output Voltage Temperature Characteristics | $\frac{\Delta V_{OUT}}{\Delta T_a \bullet V_{OUT}}$     | $V_{IN} = V_{OUT(S)} + 1.0 \text{ V}$ , $I_{OUT} = 10 \text{ mA}$<br>$-40^\circ\text{C} \leq T_a \leq 85^\circ\text{C}$    | —                        | $\pm 100$    | —                        | ppm/ $^\circ\text{C}$ |              |
| Supply Current                             | $I_{SS1}$                                               | $V_{IN} = V_{OUT(S)} + 1.0 \text{ V}$                                                                                      | —                        | 70           | 110                      | $\mu\text{A}$         |              |
| Shutdown current                           | $I_{STB}$                                               | $V_{IN} = V_{EN} = V_{OUT(T)} + 1 \text{ V}$ , $V_{EN} = V_{SS}$                                                           | —                        | 0.01         | 1                        | $\mu\text{A}$         |              |
| Input Voltage                              | $V_{IN}$                                                | —                                                                                                                          | 2.0                      | —            | 10                       | V                     | -            |
| Ripple-Rejection                           | PSRR                                                    | $V_{IN} = V_{OUT(S)} + 1.0 \text{ V}$ , $f = 1 \text{ kHz}$<br>$V_{rip} = 0.5 \text{ V}_{rms}$ , $I_{OUT} = 30 \text{ mA}$ | —                        | 70           | —                        | dB                    | 2            |
| Short-circuit Current                      | $I_{short}$                                             | $V_{IN} = V_{OUT(S)} + 1.0 \text{ V}$ , $V_{CE}$ on $V_{OUT} = \text{gnd}$                                                 | —                        | 30           | —                        | mA                    | 1            |
| Current limit                              | $I_{lim}$                                               | $V_{IN} = V_{EN} = V_{OUT(T)} + 1 \text{ V}$                                                                               | -                        | 400          | -                        | mA                    | 1            |
| CE “High” Voltage                          | $V_{CEH}$                                               |                                                                                                                            | 1.3                      |              | $V_{IN}$                 | V                     |              |
| CE “Low” Voltage                           | $V_{CEL}$                                               |                                                                                                                            |                          |              | 0.25                     | V                     |              |
| CE “High” Current                          | $I_{CEH}$                                               | $V_{IN} = V_{CE} = V_{OUT(T)} + 1.0 \text{ V}$                                                                             | -0.1                     |              | 0.1                      | $\mu\text{A}$         |              |
| CE “Low” Current                           | $I_{CEL}$                                               | $V_{IN} = V_{OUT(T)} + 1.0 \text{ V}$ , $V_{CE} = V_{SS}$                                                                  | -0.1                     |              | 0.1                      | $\mu\text{A}$         |              |

● DC-DC electrical characteristics

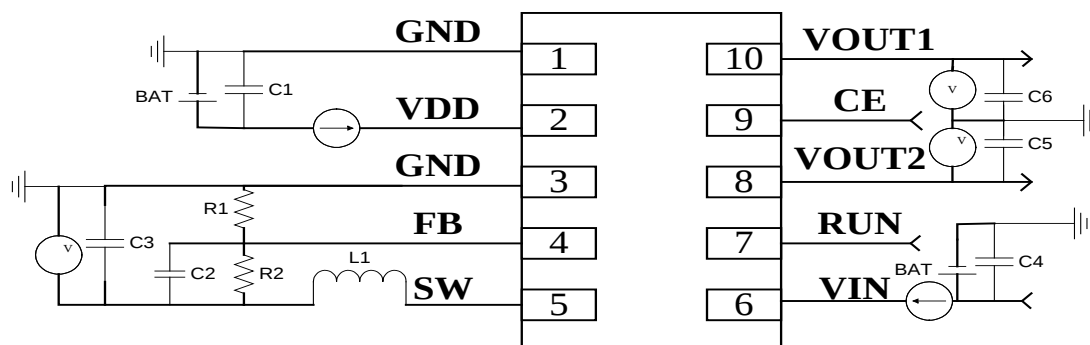
VIN=3.6V ,CIN=4.7uF ,CL=10uF ,L=3.3uH

(T<sub>A</sub> = 25°C, unless otherwise specified)

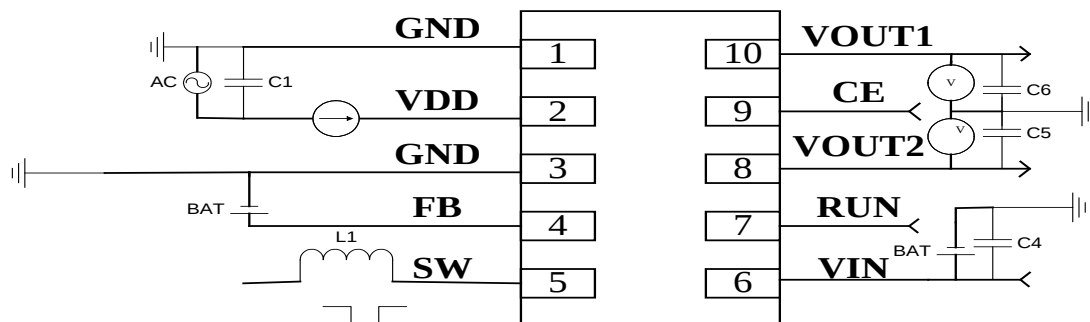
| Parameter             | Symbol             | Conditions                                 | Min  | Typ  | Max  | Units | Test Circuit |
|-----------------------|--------------------|--------------------------------------------|------|------|------|-------|--------------|
| Feedback voltage      | V <sub>FB</sub>    | -                                          | 0.59 | 0.6  | 0.61 | V     | 1            |
| Input voltage range   | V <sub>IN</sub>    |                                            | 2    | -    | 6    |       |              |
| Output voltage ripple | △V <sub>OUT</sub>  | I <sub>L</sub> MAX=600mA                   |      | 5    |      | mV    |              |
| Efficiency            | EFFI               | V <sub>IN</sub> =2.7V;I <sub>L</sub> =60mA | —    | 92   | —    | %     |              |
| Minimum CE voltage    | V <sub>CEH</sub>   | -                                          | 0.8  | 1    | -    | V     |              |
| Shutdown current      | I <sub>STB</sub>   | V <sub>CE</sub> =0V、V <sub>IN</sub> =3.6V  | 0    | -    | 1    | μA    | 2            |
| Supply current        | I <sub>DD1</sub>   | V <sub>FB</sub> =0.6V*0.9                  | -    | 150  | -    | μA    |              |
| Quiescent current     | I <sub>DD2</sub>   | V <sub>FB</sub> =0.6V*1.1                  | —    | 40   | -    |       |              |
| Output current Limit  | I <sub>LIM</sub>   | -                                          | -    | 1200 | -    | mA    |              |
| PFM switching point   | I <sub>L</sub>     |                                            |      | 40   |      | mA    |              |
| Oscillation frequency | F <sub>OSC</sub>   |                                            | -    | 1.2  | -    | MHz   | 2            |
| Maximum duty circle   | M <sub>AXDTY</sub> | -                                          | 100  | -    | -    | %     |              |

■ Test Circuit

Circuit 1



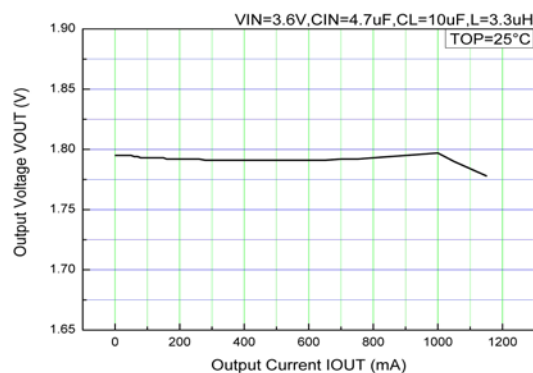
Circuit 2



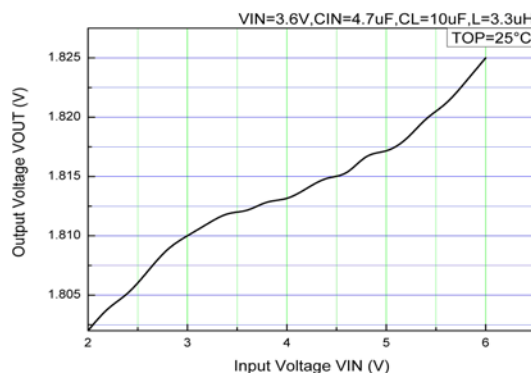
## Typical Performance Characteristics

### DC-DC electrical characteristics

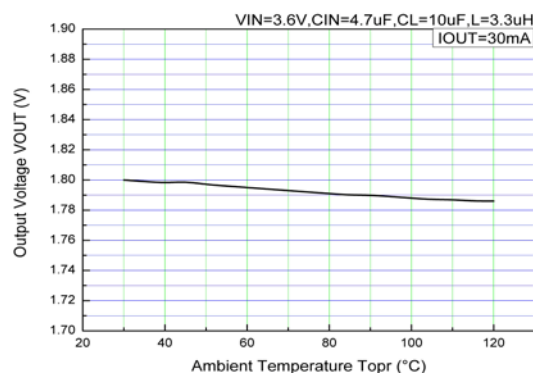
Output voltage-output current



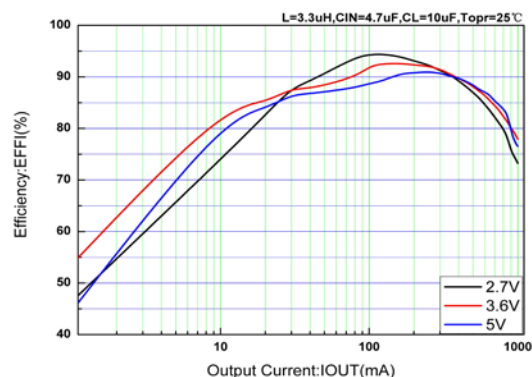
Input voltage-output voltage



Temperature characteristics

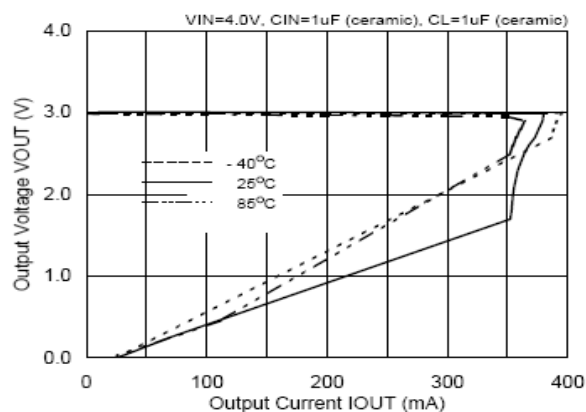


Efficiency

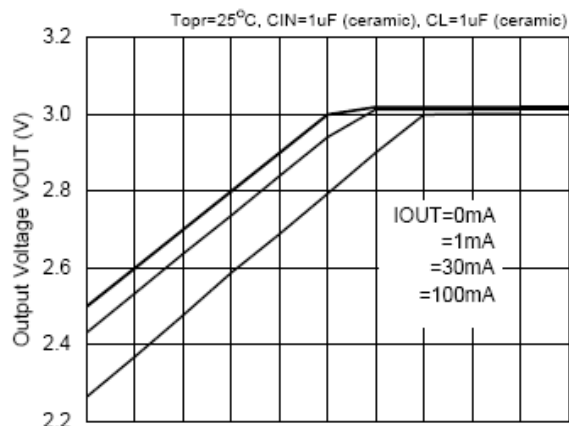


### LDO electrical characteristics

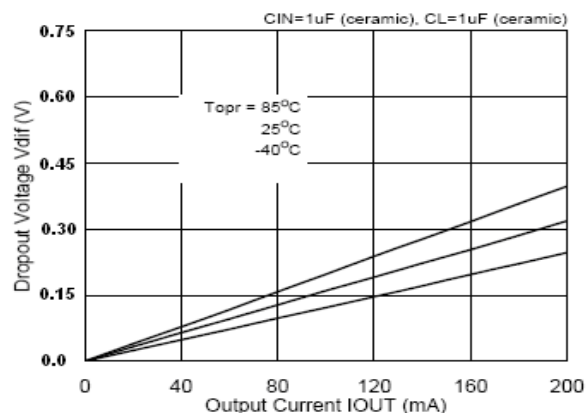
Output voltage-output current



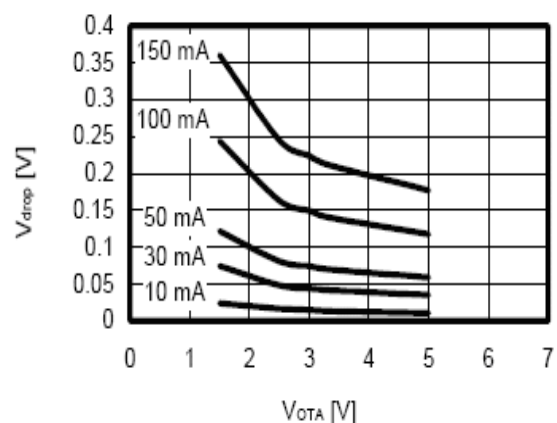
Input voltage-output voltage



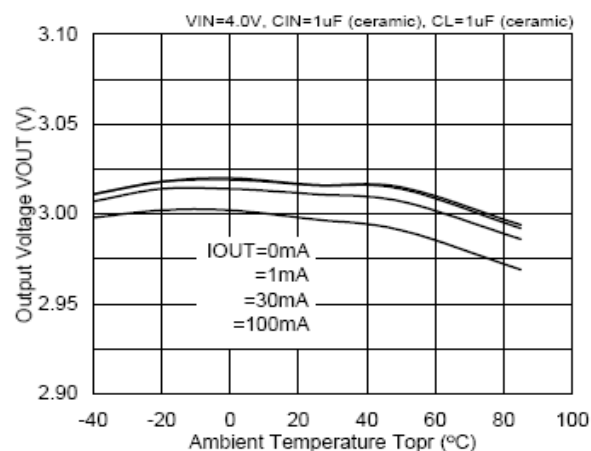
### Dropout voltage and output current



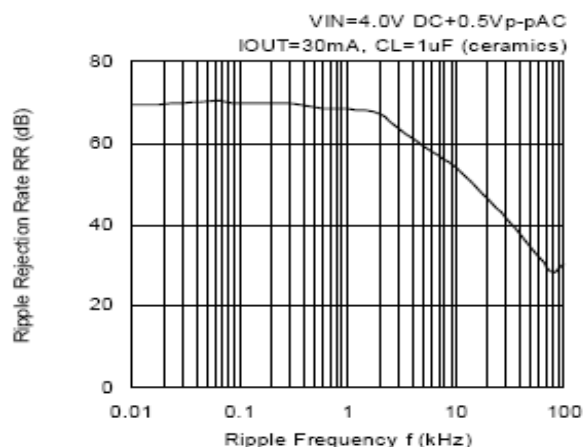
### Dropout voltage and output voltage



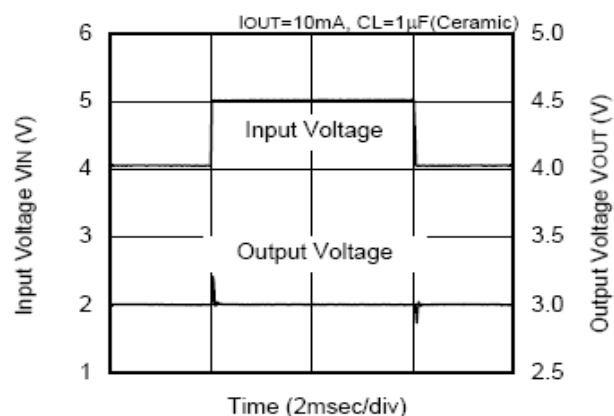
### Output voltage and temperature



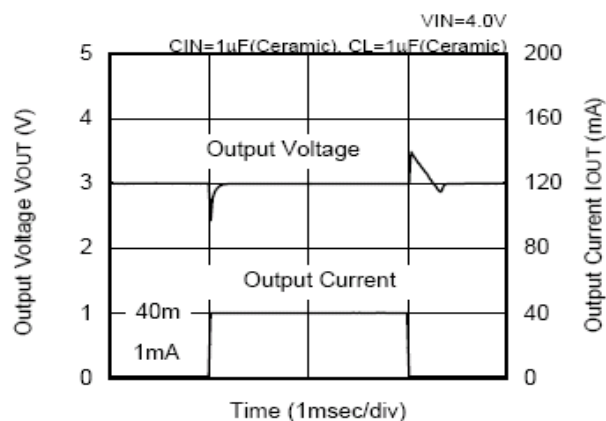
### PSRR



### Input transient response characteristics

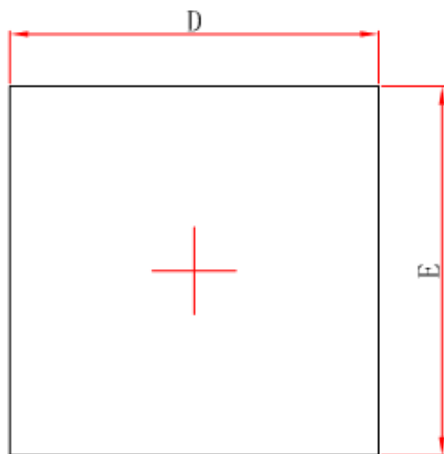


### Load-response characteristics of the transitional type

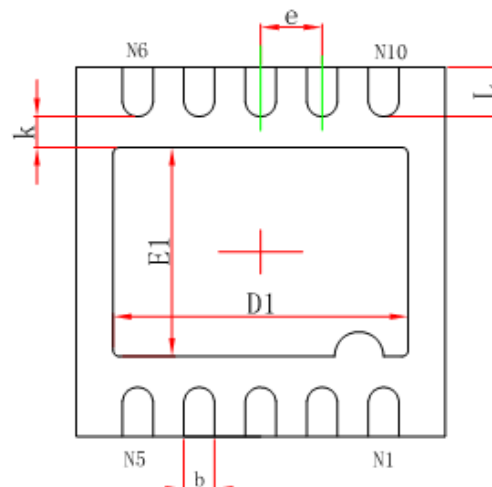


## Package Information

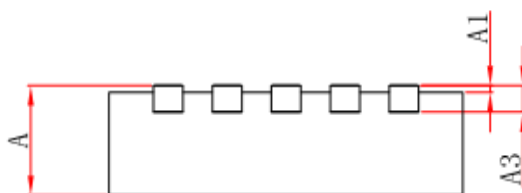
### DFN3×3-10



Top View



Bottom View



Side View

| Symbol | Dimensions In Millimeters |             | Dimensions In Inches |             |
|--------|---------------------------|-------------|----------------------|-------------|
|        | Min.                      | Max.        | Min.                 | Max.        |
| A      | 0.700/0.800               | 0.800/0.900 | 0.028/0.031          | 0.031/0.035 |
| A1     | 0.000                     | 0.050       | 0.000                | 0.002       |
| A3     | 0.203REF.                 |             | 0.008REF.            |             |
| D      | 2.900                     | 3.100       | 0.114                | 0.122       |
| E      | 2.900                     | 3.100       | 0.114                | 0.122       |
| D1     | 2.300                     | 2.500       | 0.091                | 0.098       |
| E1     | 1.600                     | 1.800       | 0.063                | 0.071       |
| k      | 0.200MIN.                 |             | 0.008MIN.            |             |
| b      | 0.180                     | 0.300       | 0.007                | 0.012       |
| e      | 0.500TYP.                 |             | 0.020TYP.            |             |
| L      | 0.300                     | 0.500       | 0.012                | 0.020       |