

Dual N-Channel Enhancement Mode Power MOSFET

■ Features

- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

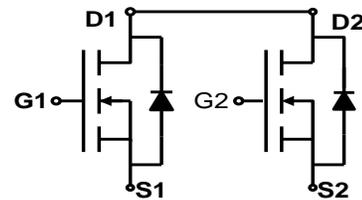
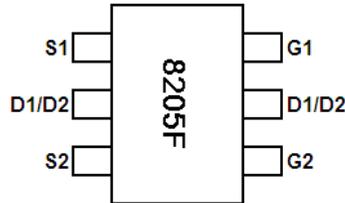
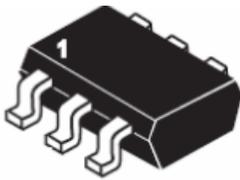
■ Package

- SOT-23-6

■ Ordering Information

Part Number	Storage Temperature	Package
LN8205F	-55°C to 150°C	SOT-23-6

■ Pin Configuration



■ Absolute Maximum Ratings

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 10	V
Drain Current-Continuous	I_D	6.5	A
Drain Current-Pulsed ^(Note 1)	I_{DM}	25	A
Maximum Power Dissipation	P_D	1.25	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C
Thermal Resistance, Junction-to-Ambient ^(Note 2)	$R_{\theta JA}$	100	°C/W

Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	20	21		V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=20V, V_{GS}=0V$			1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 12V, V_{DS}=0V$			± 100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.5	0.7	1.2	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=4.5V, I_D=6A$		14	18	m Ω
		$V_{GS}=2.5V, I_D=5.5A$		18	24	m Ω
Forward Transconductance	g_{FS}	$V_{DS}=5V, I_D=6A$		10		S
Dynamic Characteristics (Note 4)						
Input Capacitance	C_{iss}	$V_{DS}=8V, V_{GS}=0V,$ $F=1.0MHz$		800		pF
Output capacitance	C_{oss}			220		pF
Reverse transfer capacitance	C_{rss}			100		pF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=10V, I_D=6A$ $V_{GS}=4.5V, R_{GEN}=6\Omega$		10	20	nS
Turn-on Rise Time	t_r			11	25	nS
Turn-Off Delay Time	$t_{d(off)}$			35	70	nS
Turn-Off Fall Time	t_f			30	60	nS
Total Gate Charge	Q_g	$V_{DS}=10V, I_D=6A,$ $V_{GS}=4.5V$		12	15	nC
Gate-Source Charge	Q_{gs}			2.3		nC
Gate-Drain Charge	Q_{gd}			1		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	$V_{GS}=0V, I_S=6A$			1.2	V
Diode Forward Current (Note 2)	I_S				6.5	A

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production

Typical Performance Characteristics

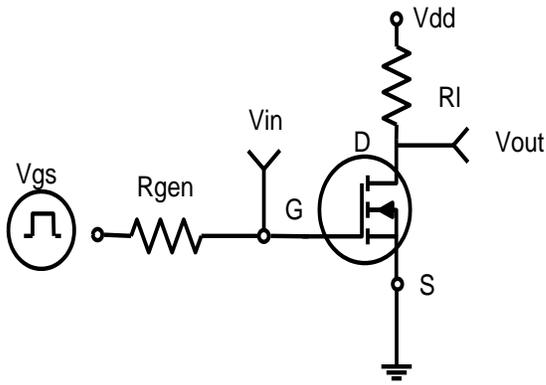


Figure 1: Switching Test Circuit

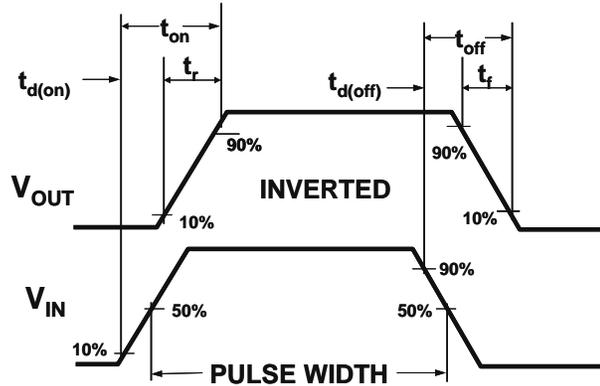


Figure 2: Switching Waveforms

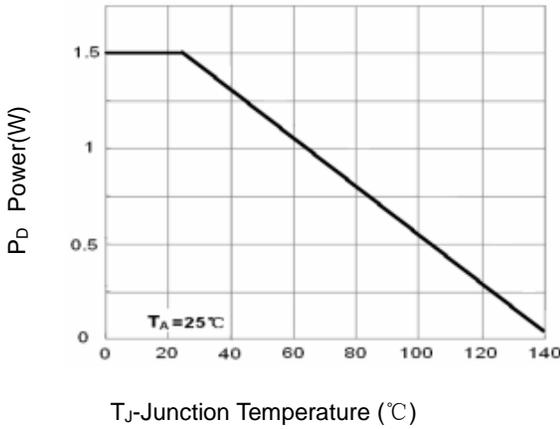


Figure 3 Power Dissipation

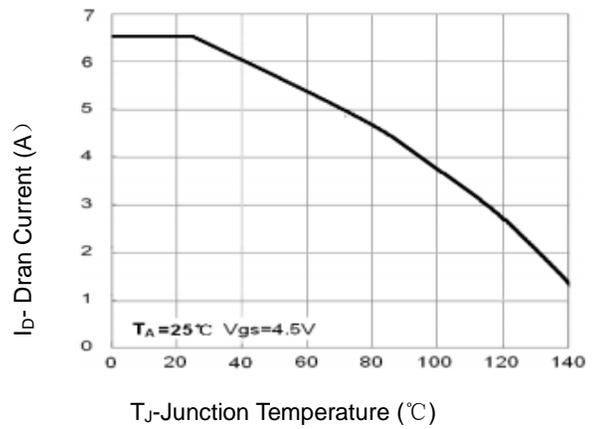


Figure 4 Drain Current

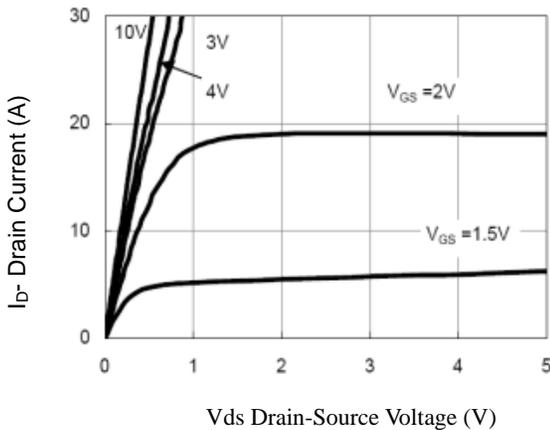


Figure 5 Output Characteristics

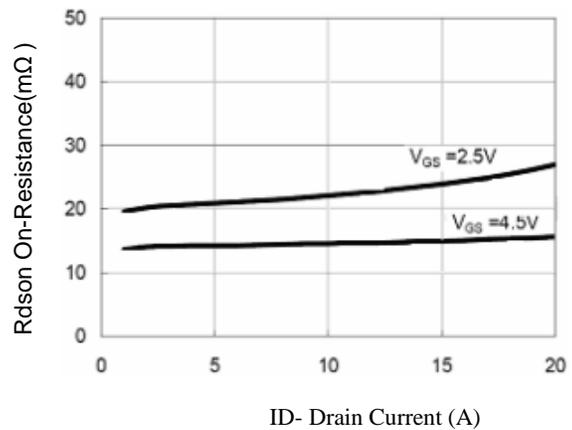


Figure 6 Drain-Source On-Resistances

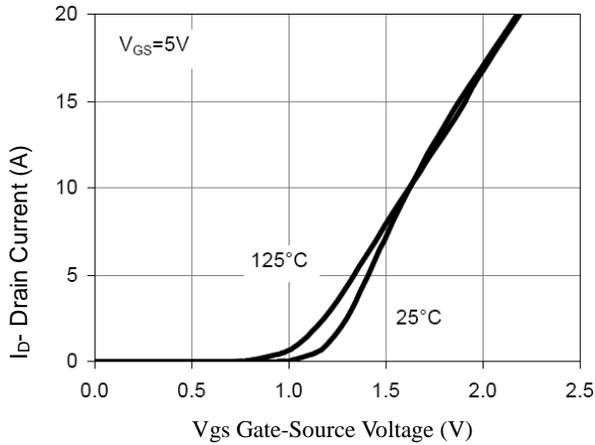


Figure 7 Transfer Characteristics

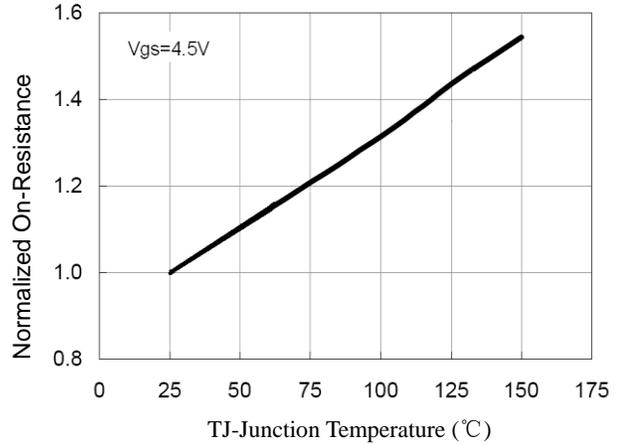


Figure 8 Drain-Source On-Resistances

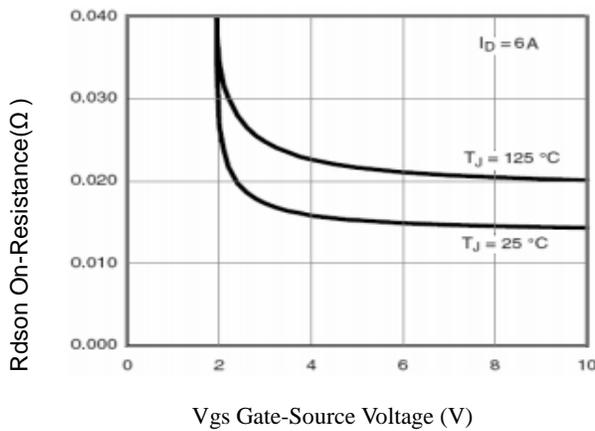


Figure 9 Rdson vs Vgs

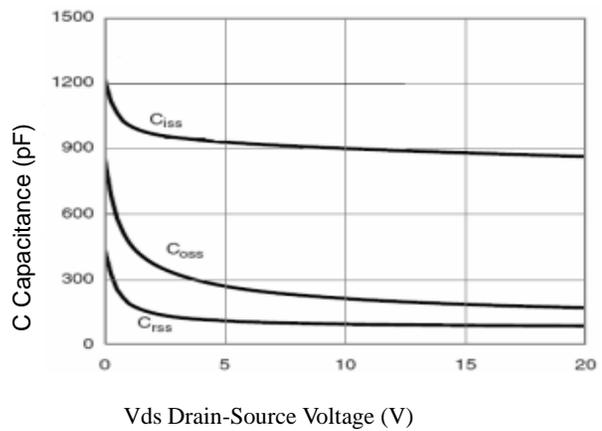


Figure 10 Capacitance vs Vds

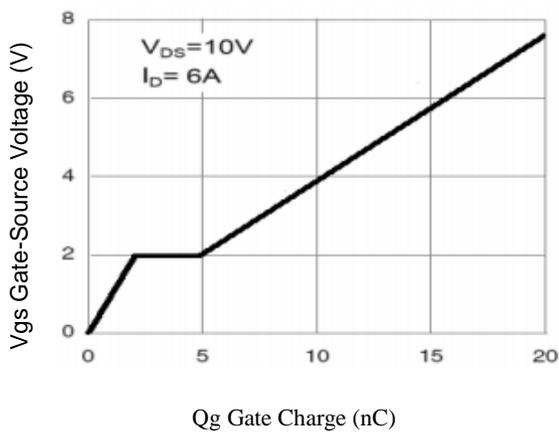


Figure 11 Gate Charge

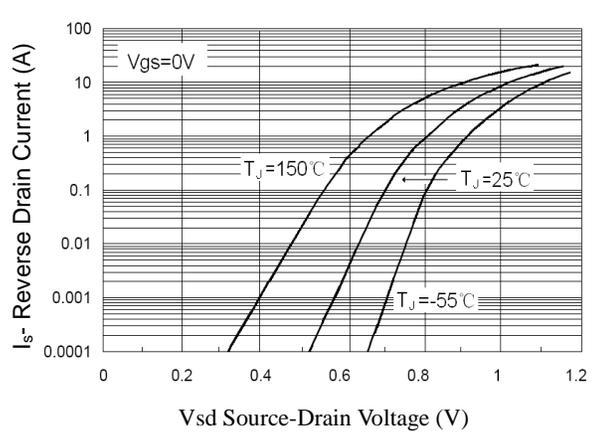


Figure 12 Source- Drain Diode Forward

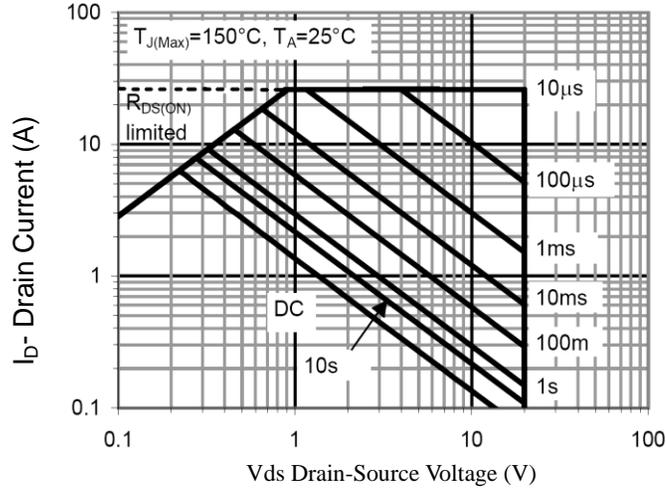


Figure 13 Safe Operation Area

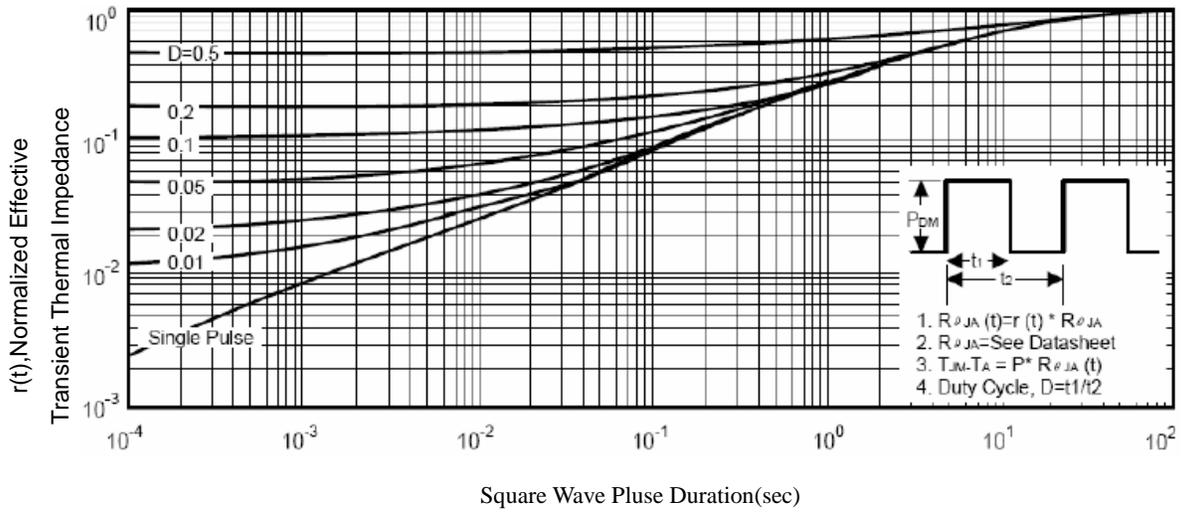
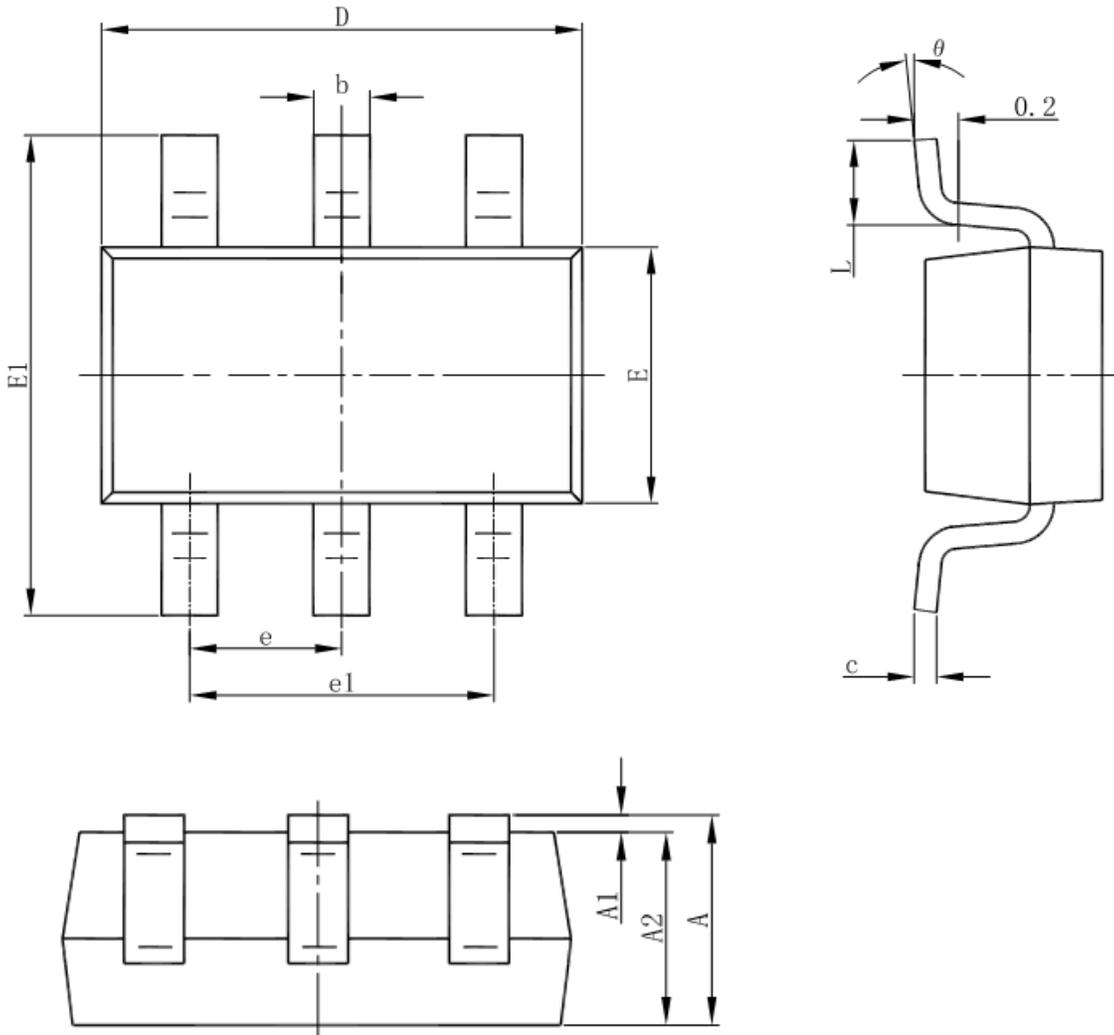


Figure 14 Normalized Maximum Transient Thermal Impedance

Package Information

- SOT-23-6



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
theta	0°	8°	0°	8°