

One Cell Lithium-ion/Polymer Battery Protection IC

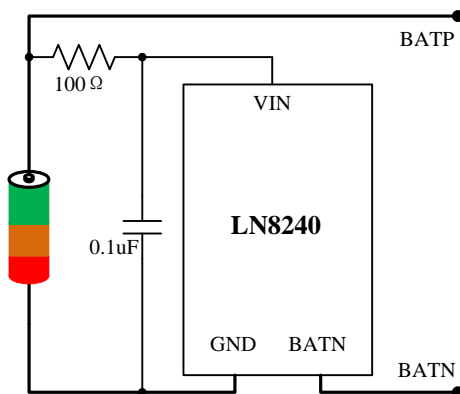
■ General Description

The LN8240 provides a high integration solution for lithium-ion/polymer battery protection. The LN8240 contains advanced power MOSFET, high-accuracy voltage detection circuits and delay circuits.

The LN8240 has all the protection functions required in the battery application including over-charging, over-discharging, over-current and load short circuiting protection etc. The low standby current drains little current from the cell while in storage.

The device is targeted for any Li-Ion and Li-Poly battery-powered information appliances requiring long-term battery life.

■ Application Information Package



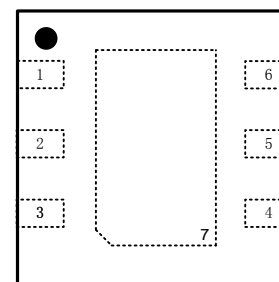
■ Features

- Protection of Charger Reverse Connection
- Protection of Battery Cell Reverse Connection
- High-accuracy Voltage Detection
- Charger Detection Function
- 0V Battery Charging Enable/Disable Function
- Integrate Advanced Power MOSFET(20mΩ)
- Low Current Consumption
 Operation Mode: 2.2μA typ.
 Power-down Mode: 0.1μA max.

■ Application

- One-Cell Lithium-ion Battery Pack
- Lithium-Polymer Battery Pack

■ Package



DFN2x2-6L
(TOP VIEW)

■ Functional Pin Description

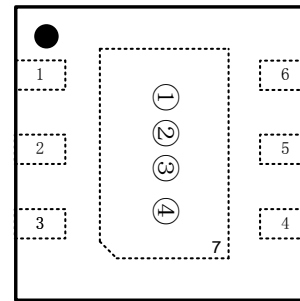
Pin	DFN2x2-6L	Note
1	VIN	Power Supply
2	GND	Ground
3	GND	Ground
4	BATN	The Negative Terminal of Battery Pack
5	BATN	The Negative Terminal of Battery Pack
6	BATN	The Negative Terminal of Battery Pack
7		Heatsink must be connected to BATN

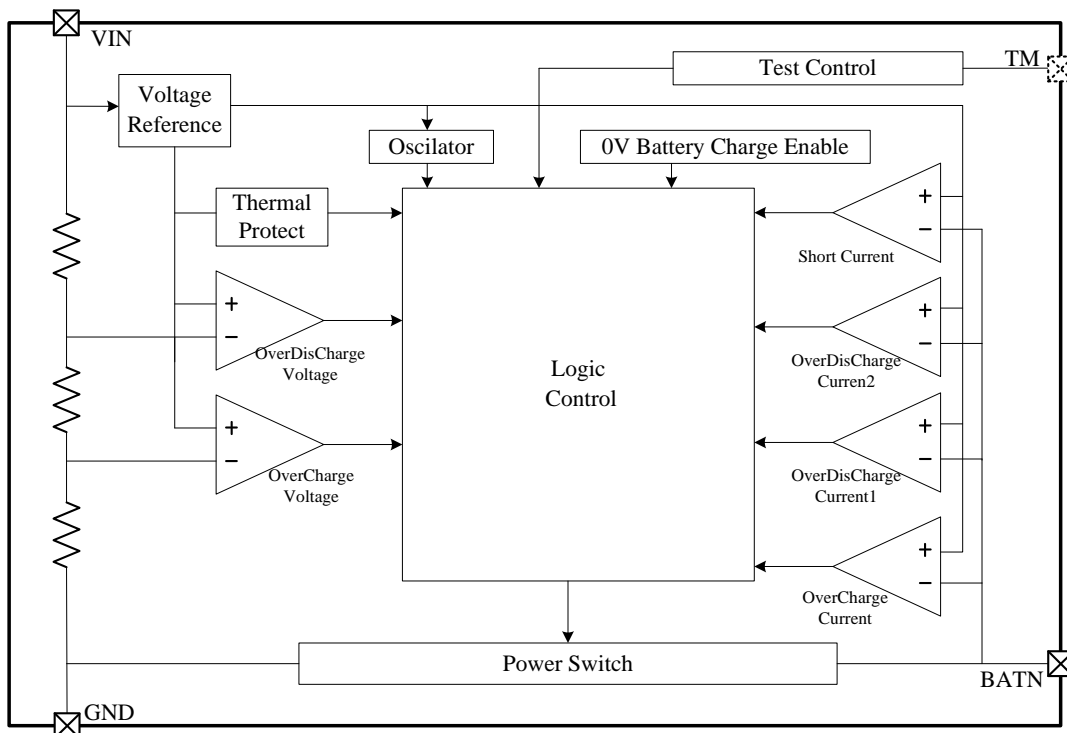
Ordering Information
LN8240 ①②③④ - ⑤

Designator	Symbol	Description
①	A	4.20V Battery Over Voltage Protect
	B	4.35V Battery Over Voltage Protect
②	A	Power NMOS Ron=30mΩ
	B	Power NMOS Ron=50mΩ
③	D	DFN2x2-6L
④	R	Reel
	T	Tape
⑤	G	绿料

Marking Information

Designator	Symbol	Description
①	9	LN8240
②	A	4.20V Battery Over Voltage Protect
	B	4.35V Battery Over Voltage Protect
③	A	Power NMOS Ron=30mΩ
	B	Power NMOS Ron=50mΩ
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 DFN2x2-6L
 (TOP VIEW)

Function Block Diagram


■ Absolute Maximum Ratings

Parameter	Symbol	Max	Unit
Supply Voltage	V _{IN}	GND-0.3~GND+6.0	V
Power Dissipation	PD@T _A =25°C	0.4	W
Package Thermal Resistance(Junction to Ambient)		130	°C/W
Lead Temperature		260	°C
Ambient Temperature	Topa	-40~+85	
Storage Temperature	T _{str}	-55~+125	
ESD Susceptibility	HBM	4000	V

■ Electrical Characteristics

 Note: V_{VIN} = 3.5V, T_A = 25°C, unless otherwise specification.

Parameters	Test Condition	Min	Typ	Max	Unit
Voltage					
Over-Charge Detection Voltage		4.27	4.30	4.33	V
Over-Charge Release Voltage		4.07	4.10	4.13	V
Over-Charge Detection Voltage (*)			4.45		V
Over-Charge Release Voltage(*)			4.25		V
Over-Discharge Detection Voltage		2.76	2.80	2.84	V
Over-Discharge Release Voltage		2.96	3.00	3.04	V
Charger Detection Voltage			-0.50		V
Current					
Over-Discharge Current Detection 1	V _{DD} =3.5V		2		A
Load Short Detection	V _{DD} =3.5V		5		A
Normal Operator Current	V _{DD} =3.5V, V _{BATN} =0V		2.2	6	uA
Power Down Current	V _{DD} =2.0V, BATN floating			0.1	uA
Delay Time					
Over-Charge Voltage Detection Delay Time			200		mS
Over-Discharge Voltage Detection Delay Time			50		mS
Over-Discharge Current Detection 1 Delay Time			12		mS
Over-Discharge Current Detection 2 Delay Time			2.5		mS
Load Short Detection Delay Time			40		uS
BATN Internal Resistance					
Internal Resistance between BATN and V _{DD}	V _{DD} =3.5V BATN=1.0V		320		kΩ
Internal Resistance between BATN and GND	V _{DD} =2.0V BATN=1.0V		100		kΩ
Power FET on Resistance	V _{DD} =4.0V, I _{BATN} =1.0A		20		mΩ
Over Temperature Protection			100		°C
Over Temperature Hybrid			20		°C
Note: Parameter with * is used for 4.35V Battery Protect, Other is for 4.2V Battery Protect					

■ Typical Operating Characteristics

The LN8240 monitors the voltage and current of a battery and protects it from being damaged due to overcharge voltage, over-discharge voltage, over-discharge current, and short circuit conditions by disconnecting the battery from the load or charger. These functions are required in order to operate the battery cell within specified limits.

Normal operating mode

If no exception condition is detected, charging and discharging can be carried out freely. This condition is called the normal operating mode.

Over-Charge Condition

When the battery voltage becomes higher than the over-charge detection voltage (V_{CU}) during charging under normal condition and the state continues for the overcharge detection delay time (t_{CU}) or longer, the LN8240 turns off the power switch FET.

Over-Discharge Condition

When the battery voltage drops below the over-discharge detection voltage (V_{DL}) during discharging under normal condition and it continues for the over-discharge detection delay time (t_{DL}) or longer, the LN8240 turns off the power switch FET and stops discharging. After over-discharging condition, the BATN pin is pulled up to high state by the internal control of LN8240, the current of the chip is reduced to the power-down current (I_{PDN}).

At the over-discharge condition, when a charger is connected, the power switch FET is still off. At this time, charging is still permitted through the parasitic diode of the power switch FET, when the battery voltage becomes higher than the over-discharge detection voltage (V_{DL}), the LN8240 turns on the power switch FET and changes to the normal condition from the over-discharge condition.

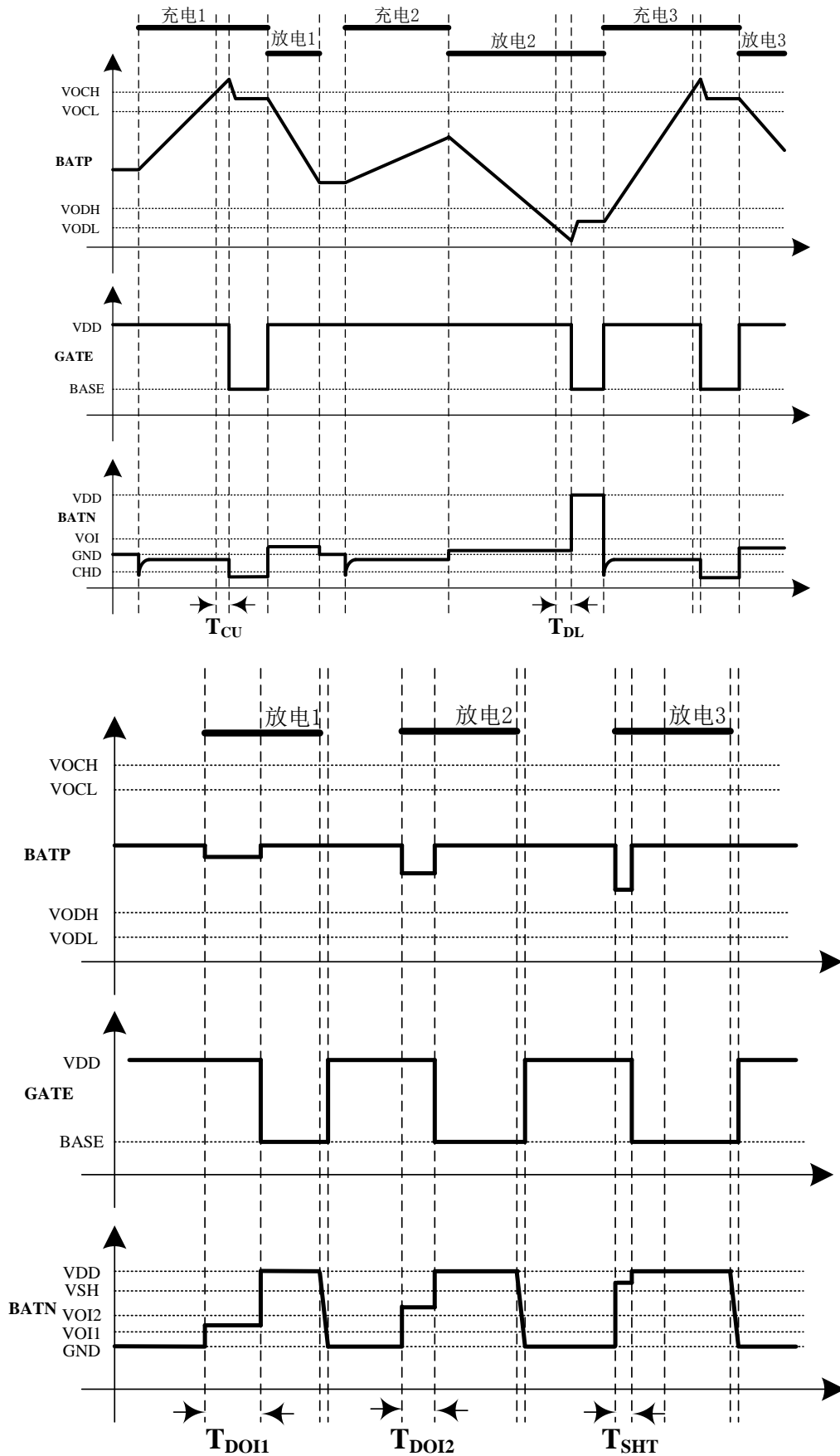
Over-Current Condition

When the discharging current becomes equal to or higher than a specified value during discharging under normal condition and the state continues for the over-current detection delay time or longer, the LN8240 turns off power switch FET to stop discharging, and the BATN pin is pulled up to high state by the internal control of LN8240. This condition includes over-current1, over-current2 or load short-circuiting.

The over-current condition returns to the normal mode when the load is released or the impedance between BATH and BATN is larger than 500k Ω .

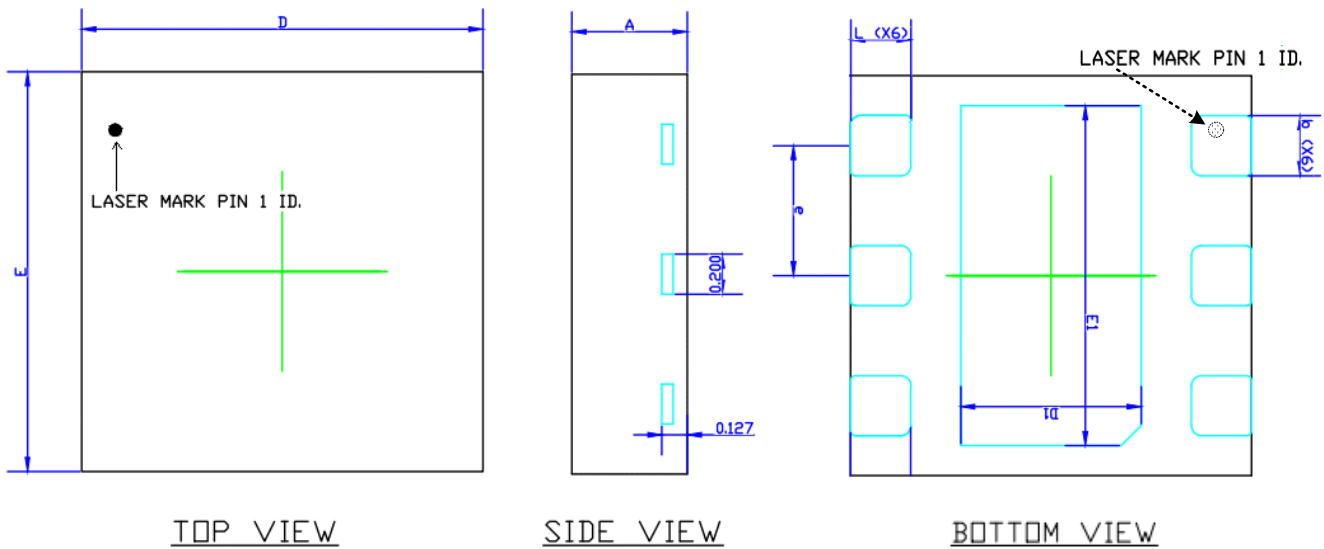
The LN8240 provides two over-current detection levels (2A and 5A) with two over-current delay time (T_{OI1} and T_{OI2}) corresponding to each over-current detection level.

Delay Circuits



Package Information

- DFN2x2-6L



COMMON DIMENSION (MM)			
PKG	DFN2020-6L		
SYMBOL	MIN.	NOM.	MAX
A	0.527	0.552	0.577
b	0.20	0.25	0.30
D	1.90	2.00	2.10
E	1.90	2.00	2.10
D1	0.80	0.90	1.00
E1	1.60	1.70	1.80
e	0.65 REF.		
L	0.25	0.30	0.35