

100mΩ Power Distribution Switches

■ General Description

The LN9701 is an integrated 100 mΩ power switch for self-powered and bus-powered Universal Series Bus (USB) applications. A built-in charge pump is used to drive the N-channel NMOSFET that is free of parasitic body diode to eliminate any reversed current flow across the switch when it is powered off. Its low quiescent supply current (23µA) and small package (SOT-23-5) is particularly suitable in battery-powered portable equipment.

Several protection functions include soft start to limit inrush current during plug-in, current limiting at 1.1A to meet USB power requirement, and thermal shutdown to protect damage under over current conditions.

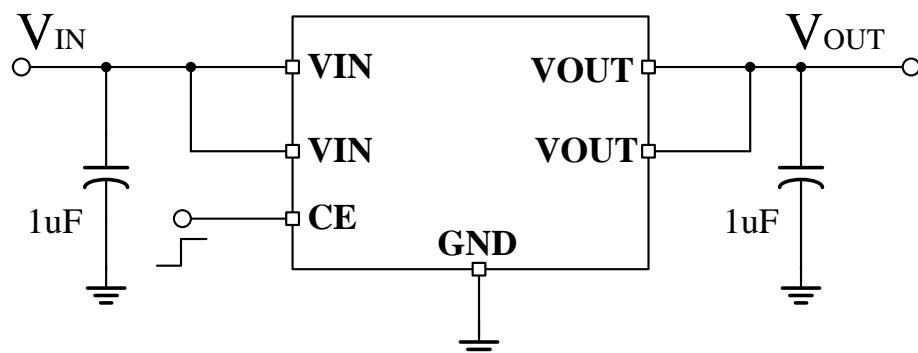
■ Features

- 100mΩ (Typ.) High-Side NMOSFET (SOT- 23-5)
- 1.1A Current Limit
- Small SOT- 23-5 Package Minimizes Board Space
- Soft Start
- Thermal Protection
- Low 23 µA Supply Current
- Wide Input Voltage Range: 2.2V ~ 6V

■ Applications

- Battery-Powered Equipment
- Motherboard USB Power Switch
- USB Device Power Switch
- Hot-Plug Power Supplies
- Battery-Charger Circuits

■ Typical Application Circuit

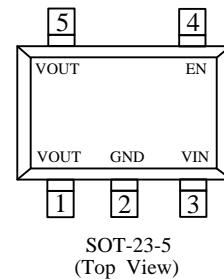


■ Package

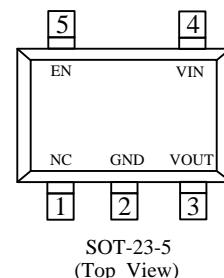
- SOT-23-5

■ Pin Configuration

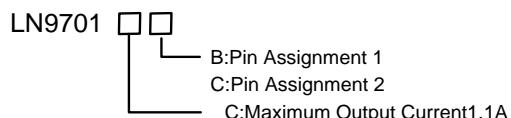
- Ordering information:LN9701CB



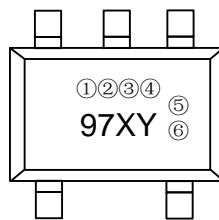
- Ordering information:LN9701CC



■ Product Name Description



■ Marking Rule



97----- LN9701

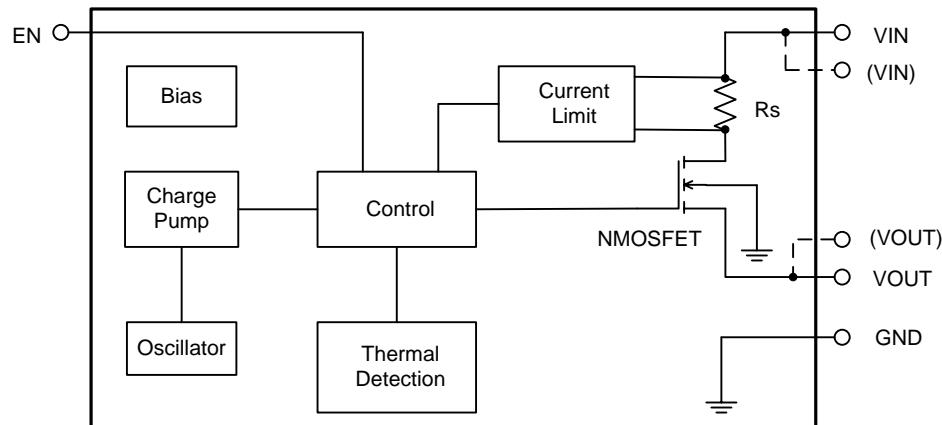
X----- C represents the product LN9701CB

E represents the product LN9701CC

Y----- Production identified

①②③④⑤⑥-- Identification of quality control

■ Block Diagram



■ Absolute Maximum Ratings

Parameter	Symbol	Maximum Rating	Unit
Input Voltage	V _{DD}	7.0	V
EN to GND Voltage	V _{EN}	-0.3—7.0	V
Power Dissipation, TA=25°C SOT-23-5	P _D	0.25	W
Thermal Resistance (SOT-23-5)	θ _{JA}	250	°C/W
Lead Temperature (Soldering, 10 sec.)	—	260	°C
Storage Temperature	T _{stg}	-65—150	°C
Operating Ambient Temperature	—	-20—100	°C
ESD Susceptibility	HBM	6000	V
	MM	600	V

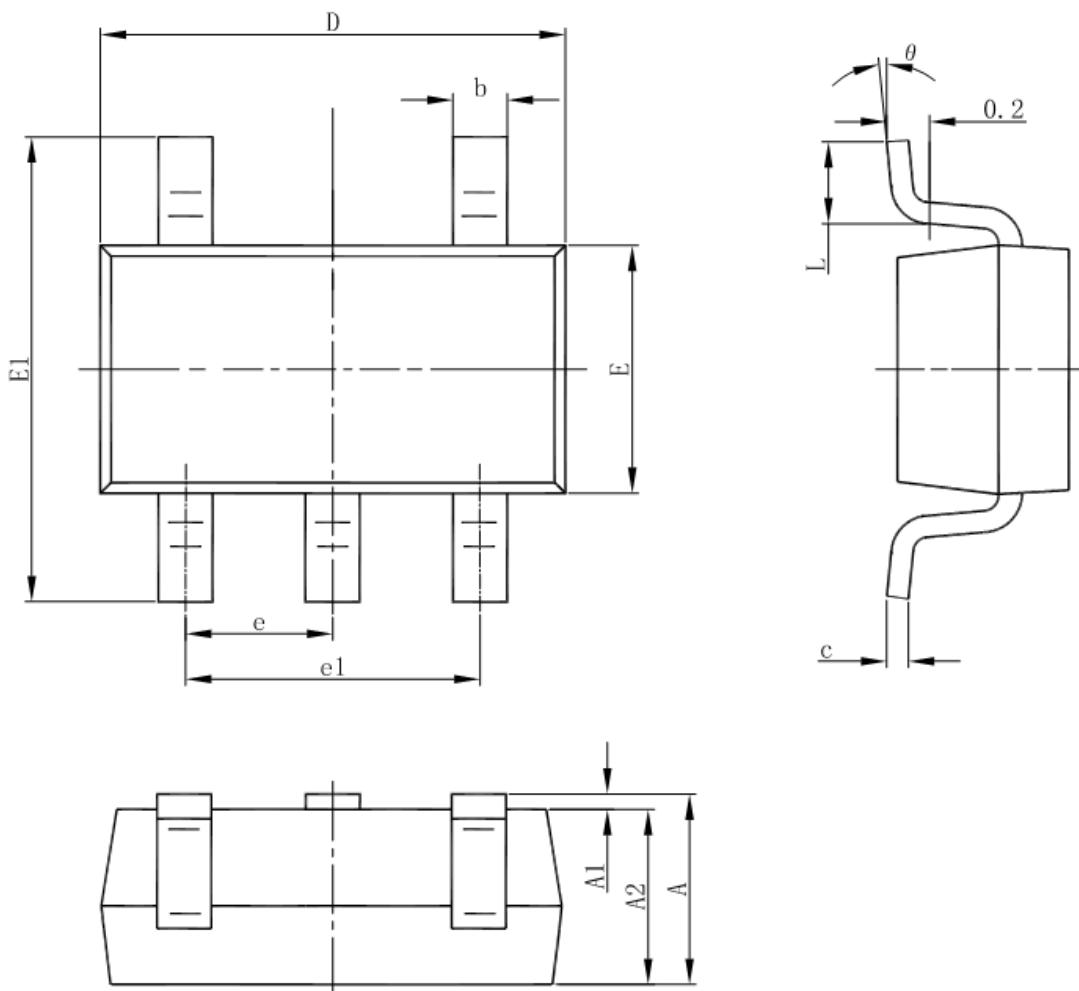
■ Electrical Characteristics

($V_{IN} = 5V$, $C_{IN} = C_{OUT} = 1\mu F$, unless otherwise noted. Typical values are at $TA = +25^{\circ}C$.)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Input Voltage RANGE	V_{IN}		2.2		6	V
NMOS OUTPUT On-Resistance	$R_{DS(ON)}$	$IL = 500mA$		100	110	$m\Omega$
Quiescent Current	I_Q	$VIN = 3V$		19	40	μA
		$VIN = 5V$		23	45	
Turn-On Time	T_R	$RL = 10\Omega$, 90% Settling		400		μs
Current Limit Setting	I_{LIMIT}	$RL = 2\Omega$	1.1	1.3	1.5	A
EN PIN Input High Voltage			1.5			V
EN PIN Input Low Voltage					0.8	V
Shutdown current	I_{OFF}	$EN = "0"$		0.1	1	μA
Output leakage current	$I_{LEAKAGE}$	$EN = "0"$ $VOUT = 0V$		0.5	10	μA
VIN Under voltage LOCKOUT	$UVLO$		1.3	1.8		V
VIN under voltage Hysteresis				100		mV
Thermal Limit	T_{SD}			130		$^{\circ}C$
Thermal Limit Hysterisis	ΔT_{SD}			20		$^{\circ}C$

■ Package Information

- SOT-23-5



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°