



LP121WX3
Liquid Crystal Display

Product Specification

SPECIFICATION FOR APPROVAL

(◆) Preliminary Specification

() Final Specification

| | |
|-------|--------------------|
| Title | 12.1" WXGA TFT LCD |
|-------|--------------------|

| | |
|----------|--------|
| Customer | LENOVO |
| MODEL | |

| | |
|----------|----------------------|
| SUPPLIER | LG Display Co., Ltd. |
| *MODEL | LP121WX3 |
| Suffix | TLC1 |

*When you obtain standard approval,
please use the above model name without suffix

| APPROVED BY | SIGNATURE |
|-------------|-----------|
| / | _____ |
| / | _____ |
| / | _____ |

Please return 1 copy for your confirmation with your signature and comments.

| APPROVED BY | SIGNATURE |
|------------------------|-----------|
| G. J. Kwon / G.Manager | _____ |
| REVIEWED BY | |
| J. S. Park / Manager | _____ |
| PREPARED BY | |
| S. Y. Kim / Engineer | _____ |
| T. S. Yun / Engineer | _____ |

**Products Engineering Dept.
LG Display Co., Ltd**

Product Specification

Contents

| No | ITEM | Page |
|-----|---|------|
| | COVER | 1 |
| | CONTENTS | 2 |
| | RECORD OF REVISIONS | 3 |
| 1 | GENERAL DESCRIPTION | 4 |
| 2 | ABSOLUTE MAXIMUM RATINGS | 5 |
| 3 | ELECTRICAL SPECIFICATIONS | |
| 3-1 | ELECTRICAL CHARACTERISTICS | 6 |
| 3-2 | INTERFACE CONNECTIONS | 8 |
| 3-3 | LVDS SIGNAL TIMING SPECIFICATIONS | 9 |
| 3-4 | SIGNAL TIMING SPECIFICATIONS | 11 |
| 3-5 | SIGNAL TIMING WAVEFORMS | 11 |
| 3-6 | COLOR INPUT DATA REFERNECE | 12 |
| 3-7 | POWER SEQUENCE | 13 |
| 4 | OPTICAL SPECIFICATIONS | 14 |
| 5 | MECHANICAL CHARACTERISTICS | 17 |
| 6 | RELIABILITY | 24 |
| 7 | INTERNATIONAL STANDARDS | |
| 7-1 | SAFETY | 25 |
| 7-2 | EMC | 25 |
| 8 | PACKING | |
| 8-1 | DESIGNATION OF LOT MARK | 26 |
| 8-2 | PACKING FORM | 26 |
| 9 | PRECAUTIONS | 27 |
| A | APPENDIX. Enhanced Extended Display Identification Data | 29 |



LP121WX3
Liquid Crystal Display

Product Specification

RECORD OF REVISIONS

| Revision No | Revision Date | Page | Description | EDID ver |
|-------------|---------------|-------|---|----------|
| 0.0 | Dec. 24. 2008 | - | First Draft (Preliminary Specification) | 0.0 |
| 0.1 | Jun. 04. 2009 | 4 | Update the General Description Block Diagram | 0.1 |
| | | 6 | Update the Electrical Characteristics.(Change the Spec.) | |
| | | 7 | Update the Pin Map Description. | |
| | | 10 | Add 50Hz Timing Table | |
| | | 13 | Update the Optical Characteristics. | |
| | | 16 | Update the Mechanical Characteristics. | |
| | | 17 | Update the Mechanical Drawing. | |
| 0.2 | Jun. 12. 2009 | 28~30 | Update the EDID Data (C0 → AC) | 0.1 |
| | | 6 | Update the LCD Driver Specification | |
| | | 7 | Update the LCD connector Description | |
| | | 12 | Update the Power Sequence | |
| 0.3 | Jun. 22. 2009 | 26 | Update the Label Description | 0.1 |
| | | 17~19 | Update the Mechanical Drawing. | |
| 0.4 | Jun. 25. 2009 | 18 | Update the Mechanical Drawing. | 0.1 |
| 0.5 | Jul. 01. 2009 | 12 | Update the Power Sequence. | 0.1 |
| 0.6 | Jul. 07. 2009 | 12 | Update the Power Sequence. | 0.1 |
| | | 16 | Update the Mechanical Characteristics. | |

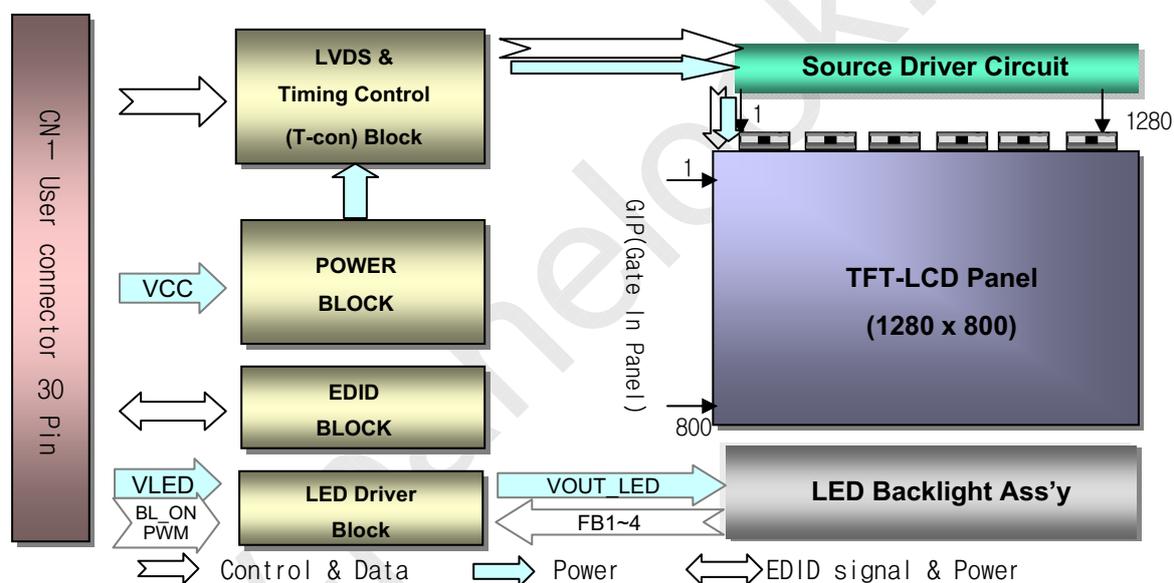
Product Specification

1. General Description

The LP121WX3 is a Color Active Matrix Liquid Crystal Display with an integral White LED backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 12.1 inches diagonally measured active display area with WXGA resolution (1280 horizontal by 800 vertical pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP121WX3 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP121WX3 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP121WX3 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

| | |
|------------------------|---|
| Active Screen Size | 12.1 inches diagonal |
| Outline Dimension | 275.8 (H) × 178.1 (V) × 5.3(D, max) mm |
| Pixel Pitch | 0.204 mm × 0.204 mm |
| Pixel Format | 1280 horiz. By 800 vert. Pixels RGB strip arrangement |
| Color Depth | 6-bit, 262,144 colors |
| Luminance, White | 220 cd/m ² (Typ.5 point) |
| Power Consumption | Total 3.8 W (Typ.) Logic: 0.8W (Typ.@Mosaic), B/L: 3.0W (Typ.@VLED:12V) |
| Weight | 285(Max.) |
| Display Operating Mode | Transmissive mode, normally white |
| Surface Treatment | Anti-glare treatment of the front polarizer (ARC150T) |
| RoHS Comply | Yes |

Product Specification

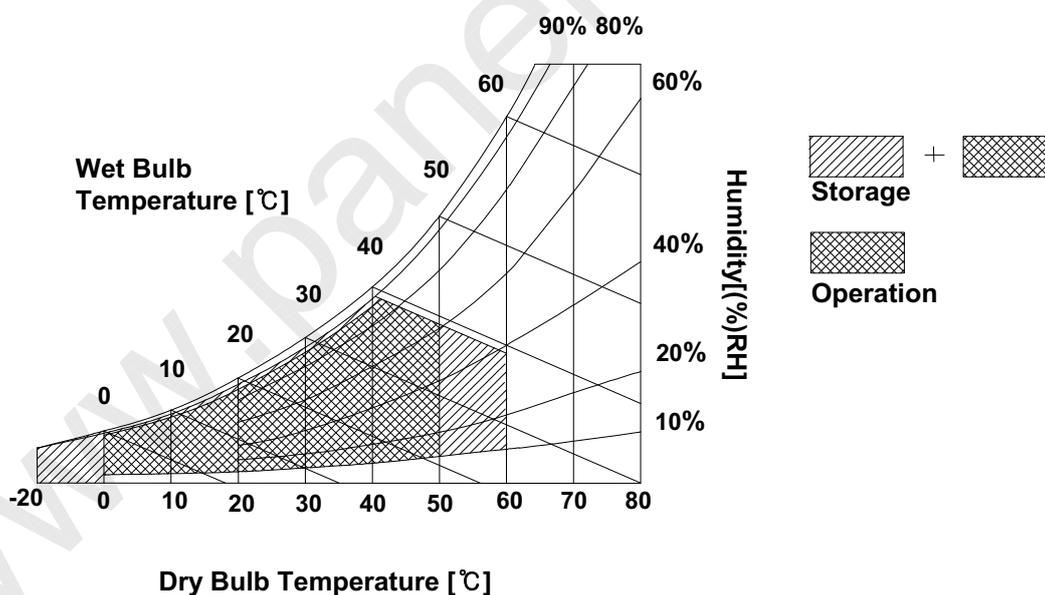
2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

| Parameter | Symbol | Values | | Units | Notes |
|----------------------------|--------|--------|-----|-------|-------------|
| | | Min | Max | | |
| Power Input Voltage | VCC | -0.3 | 4.0 | Vdc | at 25 ± 5°C |
| Operating Temperature | TOP | 0 | 50 | °C | 1 |
| Storage Temperature | HST | -20 | 60 | °C | 1 |
| Operating Ambient Humidity | HOP | 10 | 90 | %RH | 1 |
| Storage Humidity | HST | 10 | 90 | %RH | 1 |

Note : 1. Temperature and relative humidity range are shown in the figure below.
 Wet bulb temperature should be 39°C Max, and no condensation of water.



Product Specification

3. Electrical Specifications

3-1. Electrical Characteristics

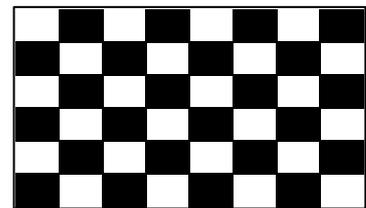
The LP121WX3 requires two power inputs. The first logic is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second backlight is the input about LED BL.with LED Driver.

Table 2. ELECTRICAL CHARACTERISTICS

| Parameter | Symbol | Values | | | Unit | Notes |
|------------------------------|--------------------------|--------|------|------|-----------------|-------|
| | | Min | Typ | Max | | |
| MODULE : | | | | | | |
| Power Supply Input Voltage | VCC | 3.0 | 3.3 | 3.6 | V _{DC} | |
| Power Supply Input Current | I _{CC} Mosaic | - | 250 | 285 | mA | 1 |
| Power Consumption | P _c Mosaic | - | 0.8 | 0.9 | W | |
| LVDS Impedance | Z _{LVDS} | 90 | 100 | 110 | Ω | 2 |
| LED Backlight: | | | | | | |
| Operating Current per string | I _{LED} | 5.0 | 20.0 | 21.0 | mA | 3 |
| Operating Voltage per string | V _{LED} | - | 32.0 | 34.0 | V | |
| Power Consumption | P _{BL} | | 3.0 | 3.4 | W | 3 |
| Life Time | | 12,000 | | | Hrs | 4 |
| LED Driver | | | | | | |
| Power Supply Input Voltage | V _{BL+} | 7.0 | 12.0 | 21.0 | V | |
| Frequency | F _{PWM} | 200 | | 1000 | Hz | 5 |
| PWM Dimming (Duty) Ratio | D _{on} | 1.5 | - | 100 | % | 6 |
| PWM High Voltage Level | V _{PWM_H} | 3.0 | - | 5.3 | V | |
| PWM Low Voltage Level | V _{PWM_L} | 0 | - | 0.5 | V | |
| LED_EN High Voltage | V _{LED_EN_H} | 3.0 | - | 5.3 | V | |
| LED_EN Low Voltage | V _{LED_EN_L} | 0 | - | 0.5 | V | |

Note)

- The specified current and power consumption are under the Vcc = 3.3V , 25°C , fv = 60Hz condition whereas Mosaic pattern is displayed and fv is the frame frequency.



- This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
- The specified LED current and power consumption are under the Vled = 12.0V , 25°C , Dimming of Max luminance whereas White pattern is displayed and fv is the frame frequency.
- The life time is determined as the time at which brightness of LCD is 50% compare to that of minimum value at Table. 7. These LED backlight has 4 strings on it and the typical current of LED's string is base on 20mA
- This Spec. is not effective at 100% dimming ratio as an exception because it has DC level equivalent to 0Hz. In spite of acceptable range as defined, the PWM Frequency should be fixed and stable for more consistent brightness control at any specific level desired.
- LGD LED Driver guarantee 6.0% at PWM minimum dimming ratio. Minimum dimming ratio 1.5% is based on Lenovo's.

Product Specification

3-2. Interface Connections

This LCD employs one interface connections, a 30 pin connector is used for the module electronics interface and the integral backlight system.

The electronics interface connector is a model FI-XB30SL-HF10 manufactured by JAE.

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

| Pin | Symbol | Description | Notes |
|-----|--------------------|--|-------|
| 1 | NC | No Connection | |
| 2 | VCC | Power Supply, 3.3V (typical) | |
| 3 | VCC | Power Supply, 3.3V (typical) | |
| 4 | V EEDID | DDC 3.3V power | |
| 5 | Reserved | Reserved for supplier test point | |
| 6 | Clk EEDID | DDC Clock | |
| 7 | DATA EEDID | DDC Data | |
| 8 | R _{IN} 0- | Negative LVDS differential data input | |
| 9 | R _{IN} 0+ | Positive LVDS differential data input | |
| 10 | GND | Ground | |
| 11 | R _{IN} 1- | Negative LVDS differential data input | |
| 12 | R _{IN} 1+ | Positive LVDS differential data input | |
| 13 | GND | Ground | |
| 14 | R _{IN} 2- | Negative LVDS differential data input | |
| 15 | R _{IN} 2+ | Positive LVDS differential data input | |
| 16 | GND | Ground | |
| 17 | CLKIN- | Negative LVDS differential clock input | |
| 18 | CLKIN+ | Positive LVDS differential clock input | |
| 19 | GND | Ground | |
| 20 | GND | Ground | |
| 21 | VBL- | LED Ground | |
| 22 | VBL- | LED Ground | |
| 23 | VBL- | LED Ground | |
| 24 | NC | No Connection | |
| 25 | VLED | LED Power Supply 7V-21V | |
| 26 | VLED | LED Power Supply 7V-21V | |
| 27 | VLED | LED Power Supply 7V-21V | |
| 28 | PWM | PWM for luminance control (200Hz ~ 1000Hz) | |
| 29 | LED_EN | Backlight On/Off Control | |
| 30 | NC | No Connection | |

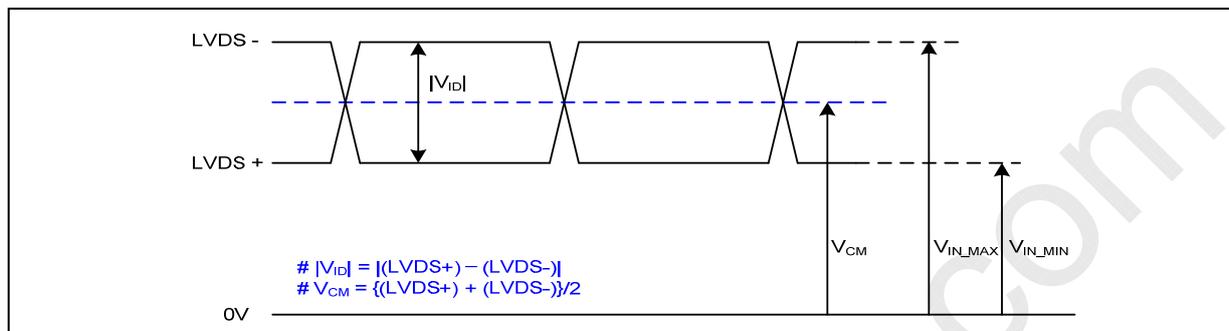


[LCD Module Rear View]

Product Specification

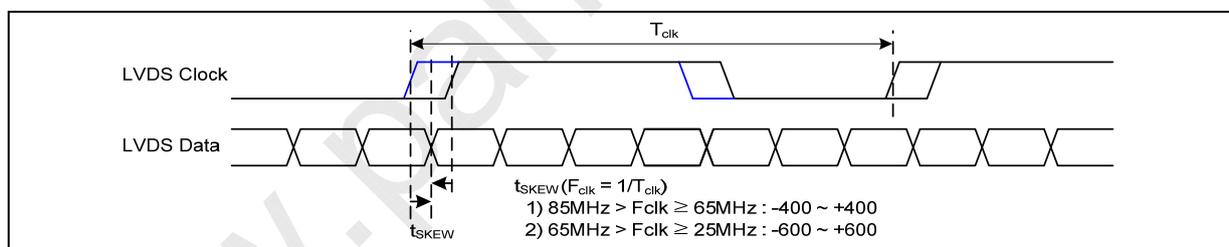
3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



| Description | Symbol | Min | Max | Unit | Notes |
|---------------------------|------------|-----|-----|------|-------|
| LVDS Differential Voltage | $ V_{ID} $ | 100 | 600 | mV | - |
| LVDS Common mode Voltage | V_{CM} | 0.6 | 1.8 | V | - |
| LVDS Input Voltage Range | V_{IN} | 0.3 | 2.1 | V | - |

3-3-2. AC Specification

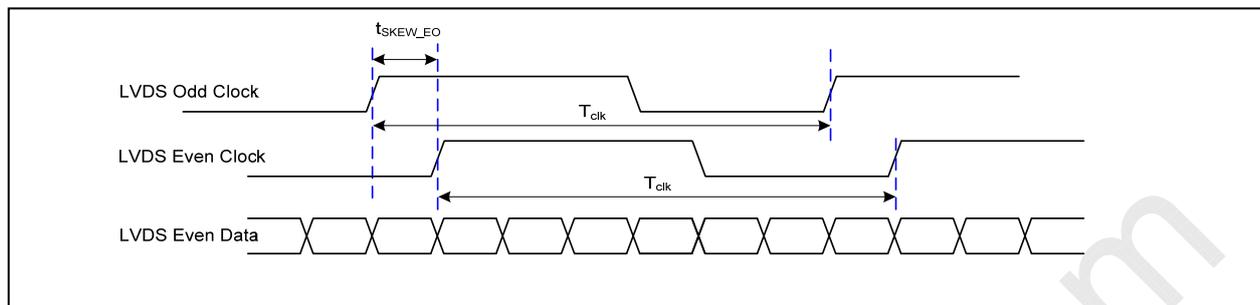


| Description | Symbol | Min | Max | Unit | Notes |
|--|----------------|-------|---------|-----------|------------------------------|
| LVDS Clock to Data Skew Margin | t_{SKEW} | - 400 | + 400 | ps | $85MHz > F_{clk} \geq 65MHz$ |
| | t_{SKEW} | - 600 | + 600 | ps | $65MHz > F_{clk} \geq 25MHz$ |
| LVDS Clock to Clock Skew Margin (Even to Odd) | t_{SKEW_EO} | - 1/7 | + 1/7 | T_{clk} | - |
| Maximum deviation of input clock frequency during SSC | F_{DEV} | - | ± 3 | % | - |
| Maximum modulation frequency of input clock during SSC | F_{MOD} | - | 200 | KHz | - |

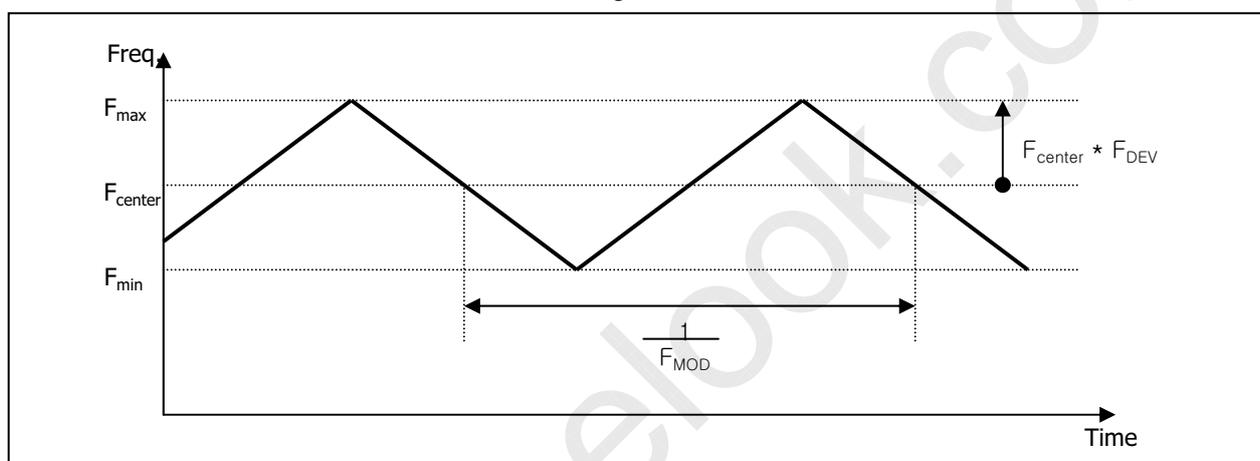


LP121WX3
Liquid Crystal Display

Product Specification



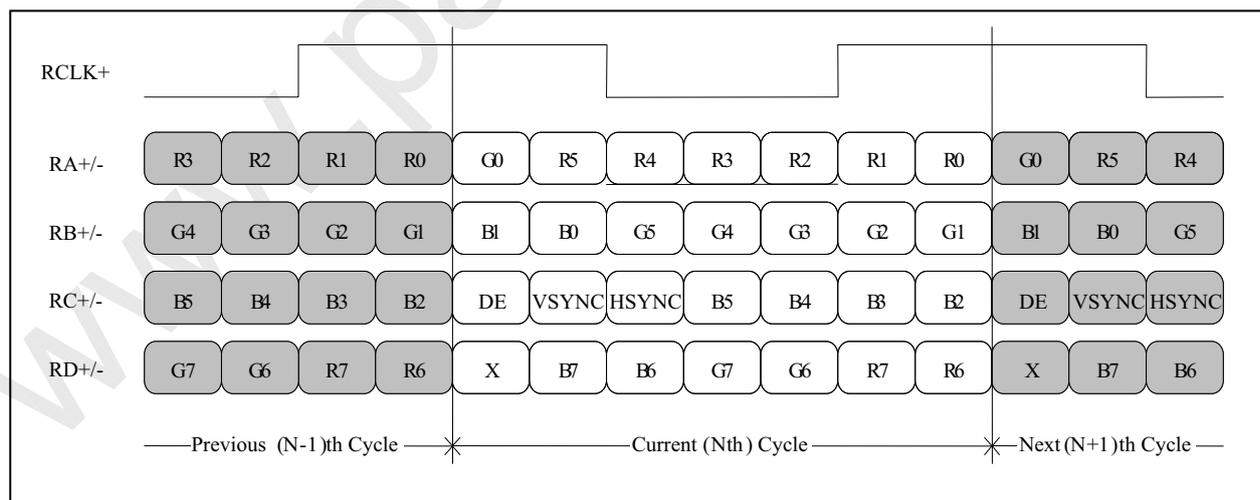
< Clock skew margin between channel >



< Spread Spectrum >

3-3-3. Data Format

1) LVDS 1 Port



< LVDS Data Format >

Product Specification

3-4. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 4. TIMING TABLE

| ITEM | Symbol | Min | Typ | Max | Unit | Note(50Hz) | |
|-------------|------------------------|-----------|------|------|------|------------|-------|
| DCLK | Frequency | f_{CLK} | 66.9 | 69.3 | 73.9 | MHz | 57.79 |
| Hsync | Period | T_{hp} | 1376 | 1408 | 1480 | tCLK | 1408 |
| | Width | t_{WH} | 24 | 32 | 40 | | 32 |
| | Width-Active | t_{WHA} | 1280 | 1280 | 1280 | | 1280 |
| Vsync | Period | t_{VP} | 810 | 820 | 832 | tHP | 820 |
| | Width | t_{WV} | 2 | 4 | 6 | | 4 |
| | Width-Active | t_{WVA} | 800 | 800 | 800 | | 800 |
| Data Enable | Horizontal back porch | t_{HBP} | 56 | 72 | 96 | tCLK | 72 |
| | Horizontal front porch | t_{HFP} | 16 | 24 | 64 | | 24 |
| | Vertical back porch | t_{VBP} | 6 | 12 | 18 | tHP | 12 |
| | Vertical front porch | t_{VFP} | 2 | 4 | 8 | | 4 |

Appendix) All reliabilities are specified for timing specification based on refresh rate of 60Hz.

However, LP121WX3 has a good actual performance even at lower refresh rate (e.g. 50Hz)

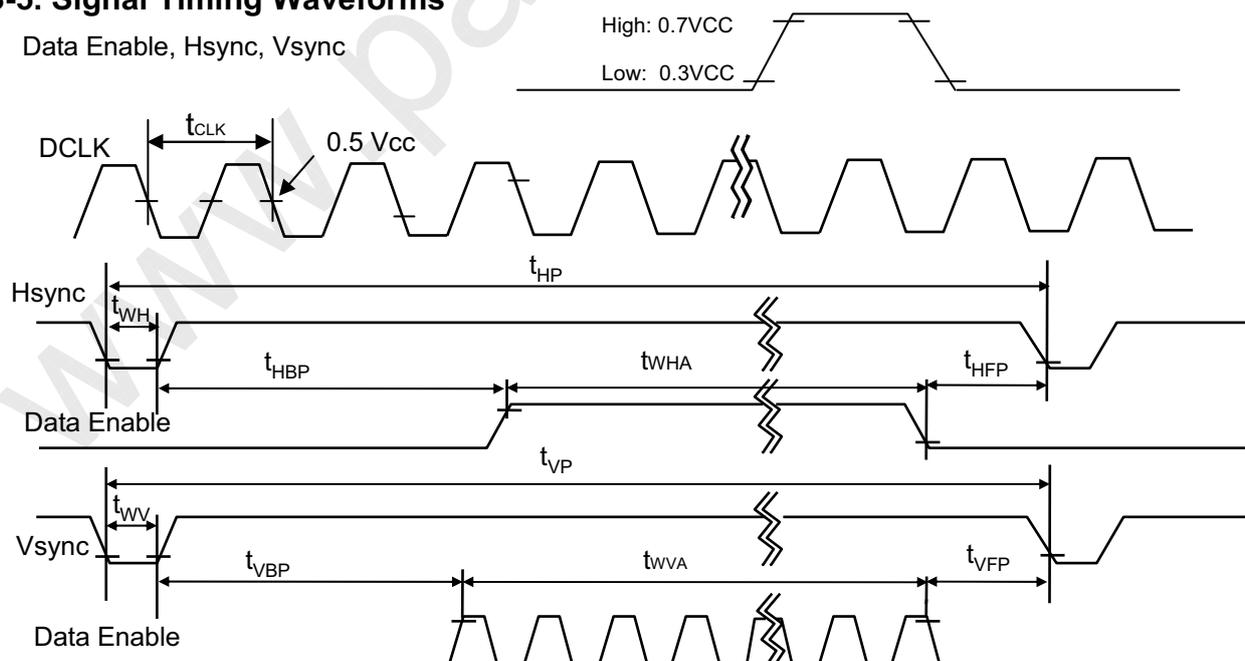
for power saving mode, whereas LP121WX3 is secured only for function under lower refresh rate.

60Hz at Normal mode, 50Hz at Power save mode. Don't care Flicker level (power save mode).

Condition : VCC = 3.3V

3-5. Signal Timing Waveforms

Data Enable, Hsync, Vsync





LP121WX3
Liquid Crystal Display

Product Specification

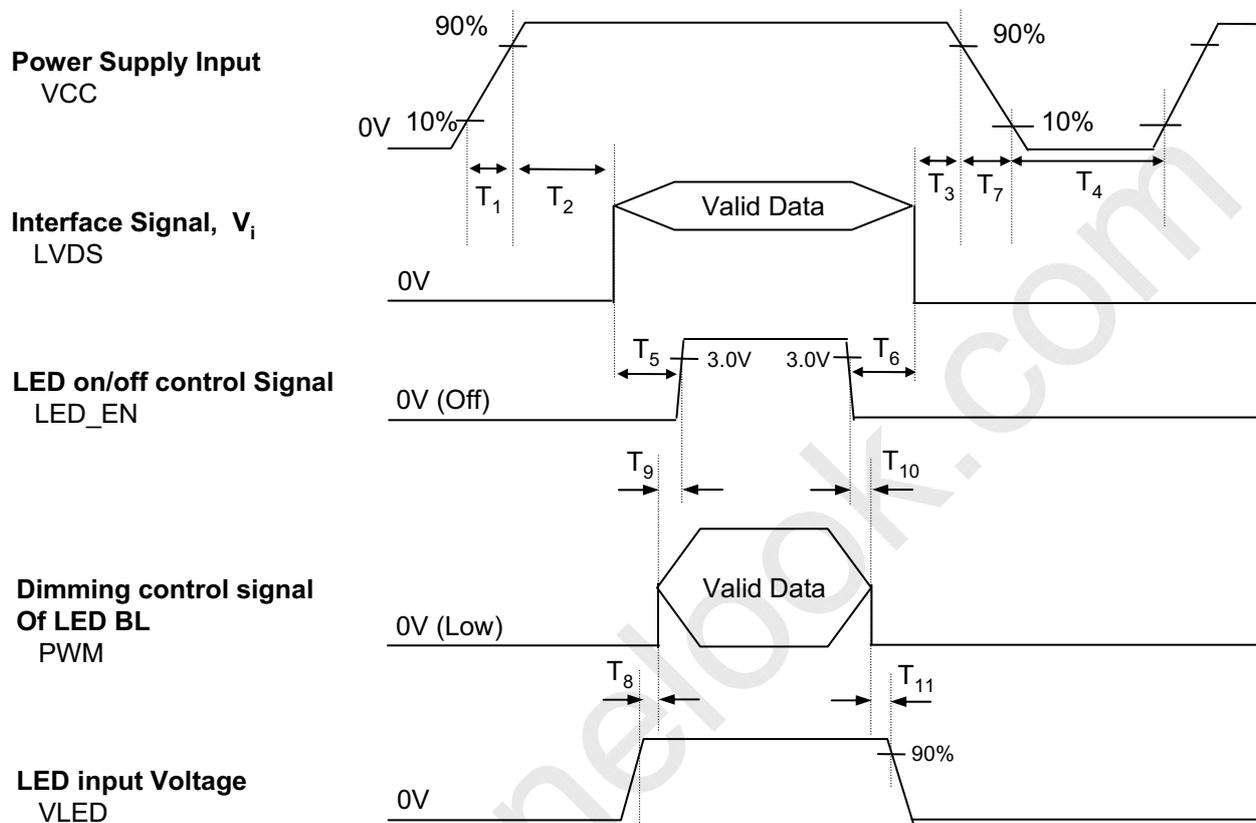
3-6. Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color ; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 5. COLOR DATA REFERENCE

| Color | | Input Color Data | | | | | | | | | | | | | | | | | | | | | |
|-------------|------------|------------------|----|----|----|----|----|-------|----|-----|----|----|----|------|----|-----|----|-----|----|--|--|--|--|
| | | RED | | | | | | GREEN | | | | | | BLUE | | | | | | | | | |
| | | MSB | | | | | | LSB | | MSB | | | | | | LSB | | MSB | | | | | |
| | | R5 | R4 | R3 | R2 | R1 | R0 | G5 | G4 | G3 | G2 | G1 | G0 | B5 | B4 | B3 | B2 | B1 | B0 | | | | |
| Basic Color | Black | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| | Red | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| | Green | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| | Blue | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | | | | |
| | Cyan | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | | | | |
| | Magenta | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | | | | |
| | Yellow | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| | White | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | | | | |
| RED | RED (00) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| | RED (01) | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| | ... | | | | | | | | | | | | | | | | | | | | | | |
| | RED (62) | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| | RED (63) | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| GREEN | GREEN (00) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| | GREEN (01) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| | ... | | | | | | | | | | | | | | | | | | | | | | |
| | GREEN (62) | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| | GREEN (63) | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| BLUE | BLUE (00) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| | BLUE (01) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | | | | |
| | ... | | | | | | | | | | | | | | | | | | | | | | |
| | BLUE (62) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 0 | | | | |
| | BLUE (63) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | | | | |

3-7. Power Sequence


Table 6. POWER SEQUENCE TABLE

| Logic Parameter | Value | | | Units | LED Parameter | Value | | | Units |
|-----------------|-------|------|------|-------|---------------|-------|------|------|-------|
| | Min. | Typ. | Max. | | | Min. | Typ. | Max. | |
| T_1 | 0 | - | 10 | ms | T_8 | 0 | - | - | ms |
| T_2 | 0 | - | 50 | ms | T_9 | 0 | - | - | ms |
| T_3 | 0 | - | - | ms | T_{10} | 0 | - | - | ms |
| T_4 | 150 | - | - | ms | T_{11} | 0 | - | - | ms |
| T_5 | 200 | - | - | ms | | | | | |
| T_6 | 0 | - | - | ms | | | | | |
| T_7 | 0 | - | 10 | ms | | | | | |

Note)

- Do not insert the mating cable when system turn on.
- Valid Data have to meet "3-3. LVDS Signal Timing Specifications"
- LVDS, LED_EN and PWM need to pull-down condition on invalid status.
- LGD recommend the rising sequence of VLED after the Vcc and valid status of LVDS turn on.
- LGD recommend the $T_1:0.5\text{ms}, T_8:10\text{ms}, T_{11}:10\text{ms}$ at power sequence.
 $T_1, T_8, T_{11}:0\text{ms}$ is based on Lenovo's.

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0°.

FIG. 1 presents additional information concerning the measurement equipment and method.

FIG. 1 Optical Characteristic Measurement Equipment and Method

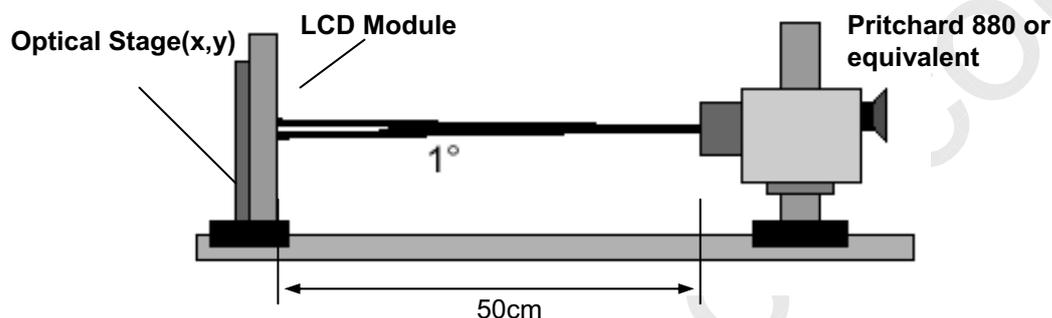


Table 7. OPTICAL CHARACTERISTICS

$T_a=25^\circ\text{C}$, $V_{CC}=3.3\text{V}$, $f_v=60\text{Hz}$, $f_{CLK}=69.3\text{MHz}$, $I_{LED}=20.0\text{mA}$

| Parameter | Symbol | Values | | | Units | Notes |
|-----------------------------------|------------------|--------|-------|-------|-------------------|-------|
| | | Min | Typ | Max | | |
| Contrast Ratio | CR | - | 300 | - | | 1 |
| Surface Luminance, white | L_{WH} | 185 | 220 | - | cd/m ² | 2 |
| Luminance Variation (5point) | δ_{WHITE} | 70 | - | - | % | 3 |
| Luminance Variation (13point) | δ_{WHITE} | 60 | - | - | % | |
| Response Time | $T_{R+} + T_{D}$ | | 16 | | ms | 4 |
| Color Coordinates | | | | | | |
| RED | RX | 0.562 | 0.592 | 0.622 | | |
| RY | RY | 0.321 | 0.351 | 0.381 | | |
| GREEN | GX | 0.304 | 0.334 | 0.364 | | |
| GY | GY | 0.519 | 0.549 | 0.579 | | |
| BLUE | BX | 0.124 | 0.154 | 0.184 | | |
| BY | BY | 0.100 | 0.130 | 0.160 | | |
| WHITE | WX | 0.283 | 0.313 | 0.343 | | |
| WY | WY | 0.299 | 0.329 | 0.359 | | |
| Viewing Angle | | | | | | 5 |
| x axis, right ($\Phi=0^\circ$) | Θ_r | 40 | 45 | - | degree | |
| x axis, left ($\Phi=180^\circ$) | Θ_l | 40 | 45 | - | degree | |
| y axis, up ($\Phi=90^\circ$) | Θ_u | 10 | 15 | - | degree | |
| y axis, down ($\Phi=270^\circ$) | Θ_d | 30 | 35 | - | degree | |
| Gray Scale | | | | | | 6 |

Product Specification

Note)

1. Contrast Ratio(CR) is defined mathematically as

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$L_{\text{WH}} = \text{Average}(L_1, L_2, \dots L_5)$$

3. The variation in surface luminance , The panel total variation (
- δ_{WHITE}
-) is determined by measuring
- L_N
- at each test position 1 through 13 and then defined as followed numerical formula.
-
- For more information see FIG 2.

$$\delta_{\text{WHITE}} = \frac{\text{Maximum}(L_1, L_2, \dots L_{13})}{\text{Minimum}(L_1, L_2, \dots L_{13})}$$

4. Response time is the time required for the display to transition from white to black (rise time,
- Tr_R
-) and from black to white(Decay Time,
- Tr_D
-). For additional information see FIG 3.

5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.

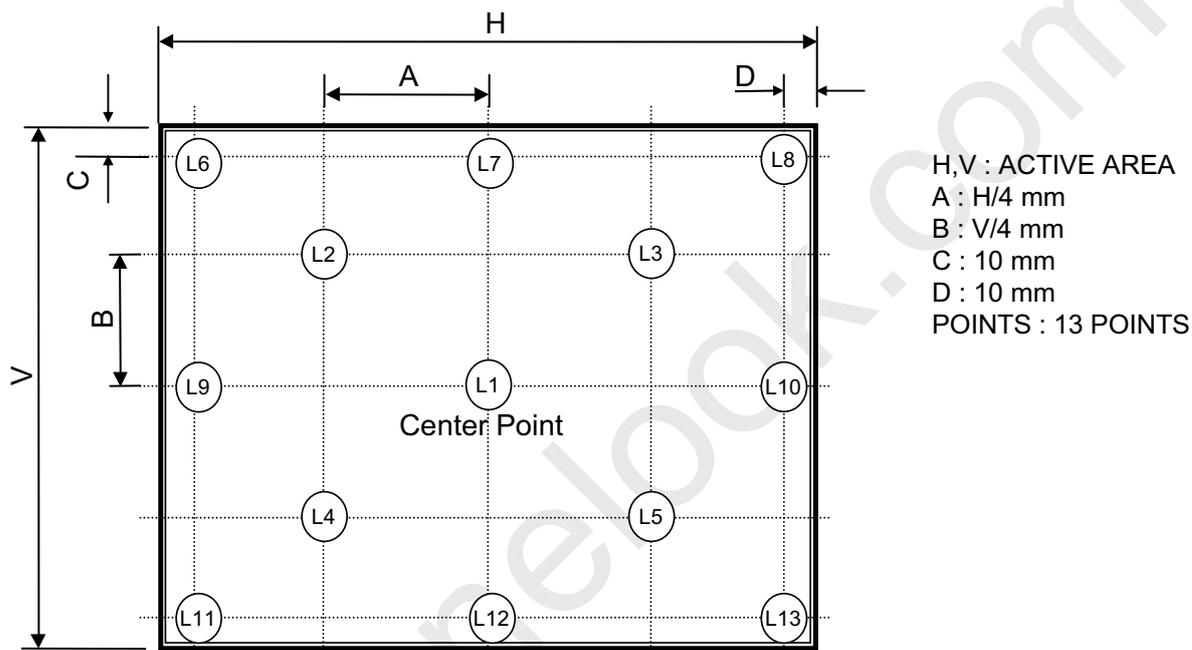
6. Gray scale specification

 * $f_v = 60\text{Hz}$

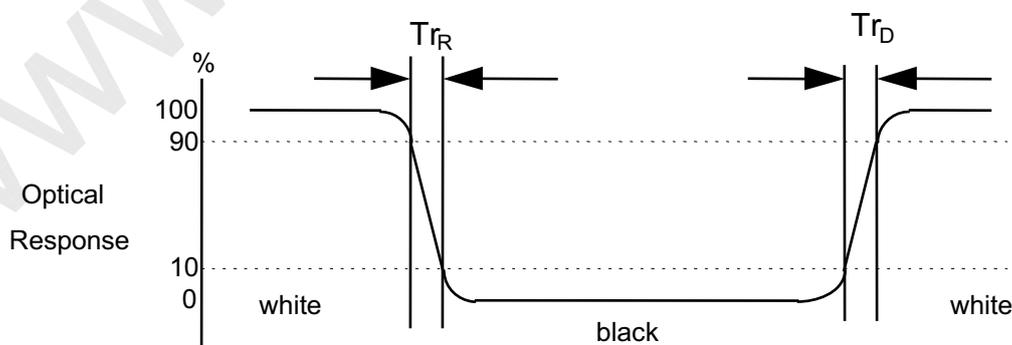
| Gray Level | Luminance [%] (Typ) |
|------------|---------------------|
| L0 | 0.22 |
| L7 | 2.77 |
| L15 | 8.65 |
| L23 | 16.4 |
| L31 | 25.4 |
| L39 | 39.3 |
| L47 | 57.2 |
| L55 | 77.9 |
| L63 | 100 |

FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

**FIG. 3 Response Time**

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



Product Specification

5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP121WX3. In addition the figures in the next page are detailed mechanical drawing of the LCD.

| | | |
|---------------------|---|---------------------------|
| Outline Dimension | Horizontal | $275.8 \pm 0.5\text{mm}$ |
| | Vertical | $178.1 \pm 0.5\text{mm}$ |
| | Thickness | 5.3 (Max) |
| Bezel Area | Horizontal | $266.50 \pm 0.5\text{mm}$ |
| | Vertical | $166.6 \pm 0.5\text{mm}$ |
| Active Display Area | Horizontal | $260.93 \pm 0.3\text{mm}$ |
| | Vertical | $163.08 \pm 0.3\text{mm}$ |
| Weight | 285g(Max) | |
| Surface Treatment | Anti-glare treatment of the front polarizer | |

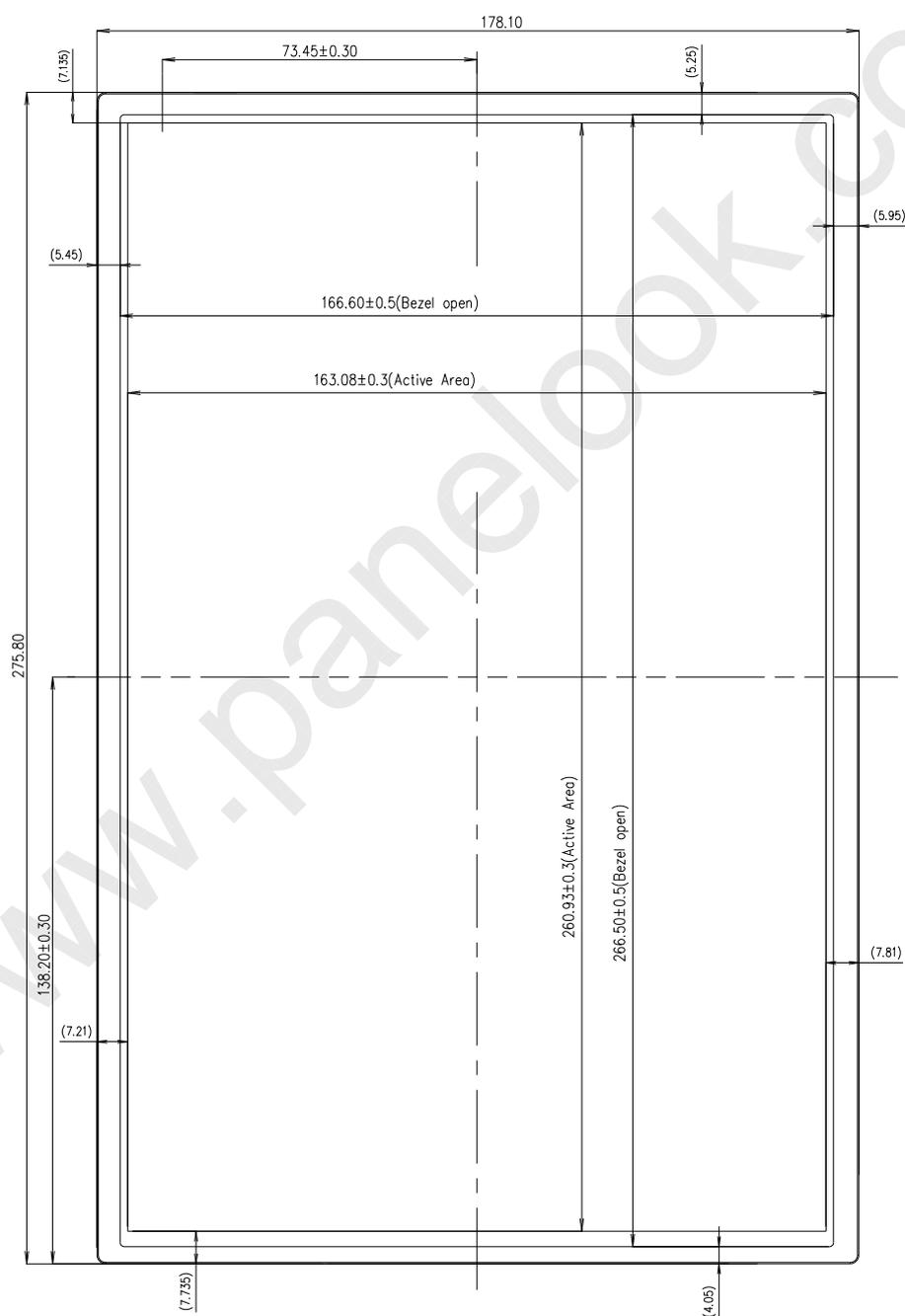
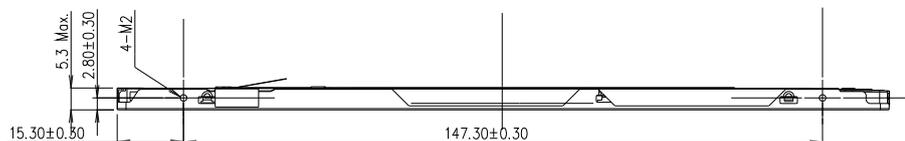


LP121WX3
Liquid Crystal Display

Product Specification

<FRONT VIEW>

Note) Unit:[mm], General tolerance: ± 0.5 mm



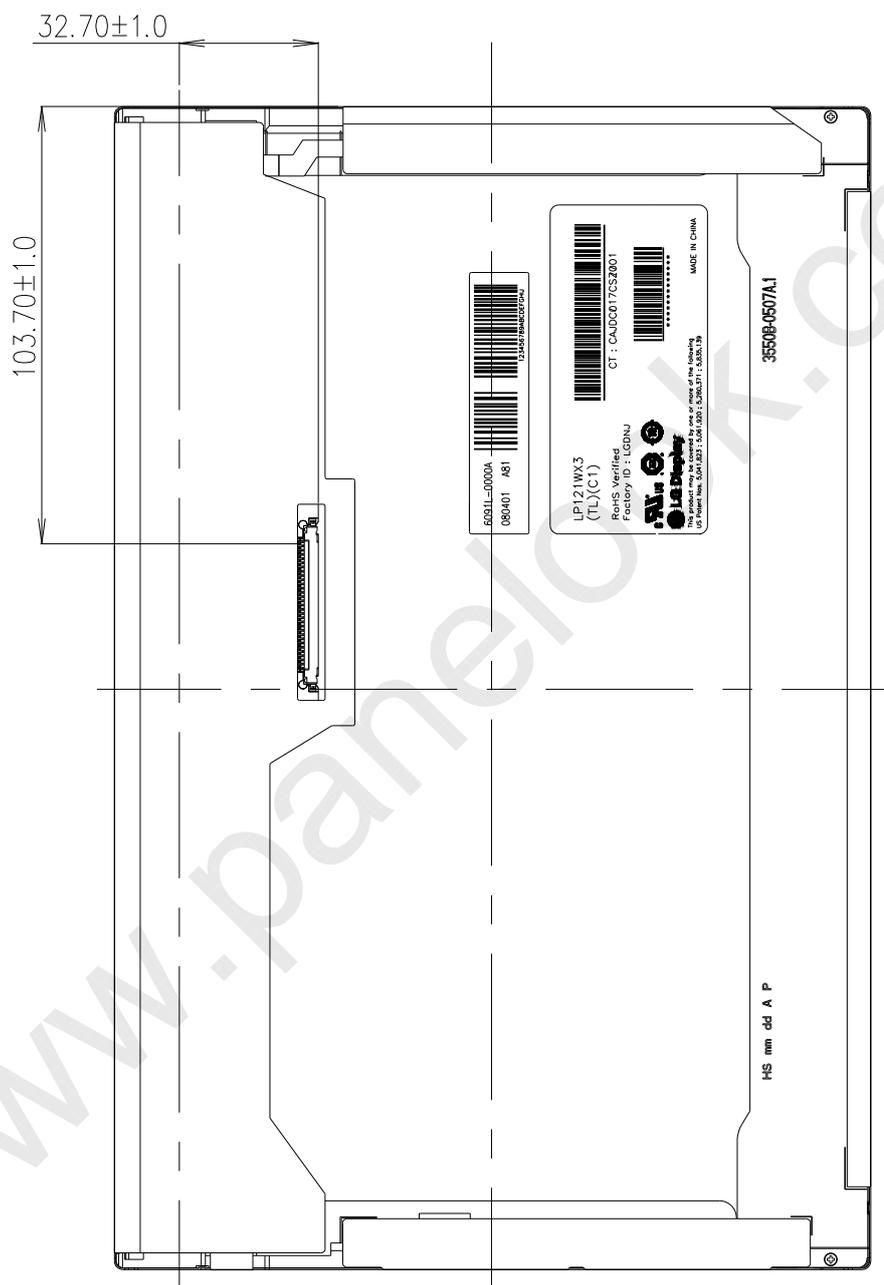


LP121WX3
Liquid Crystal Display

Product Specification

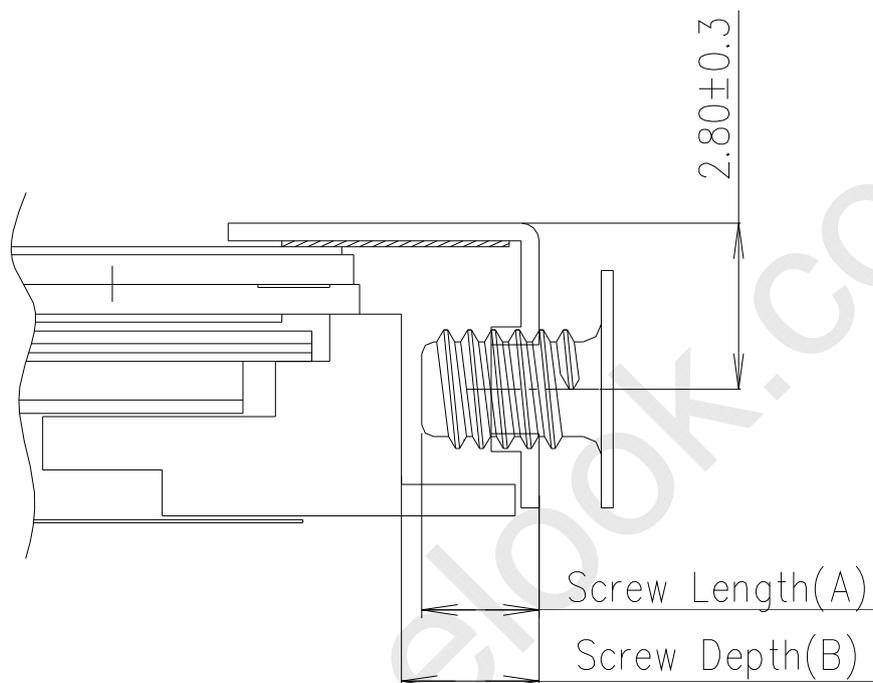
<REAR VIEW>

Note) Unit:[mm], General tolerance: $\pm 0.5\text{mm}$



Product Specification

[DETAIL DESCRIPTION OF SIDE MOUNTING SCREW]



*** Screw Length(A) : Max : 1.8, Min : 1.5**

*** Screw Depth(B) : Min 1.8**

*** Screw Torque : 2.0~2.3kgf.cm**

(Measurement Gauge :Torque Meter)

Notes : 1. Screw plated through the method of non-electrolytic nickel plating is preferred to reduce possibility that results in vertical and/or horizontal line defect due to the conductive particles from screw surface.

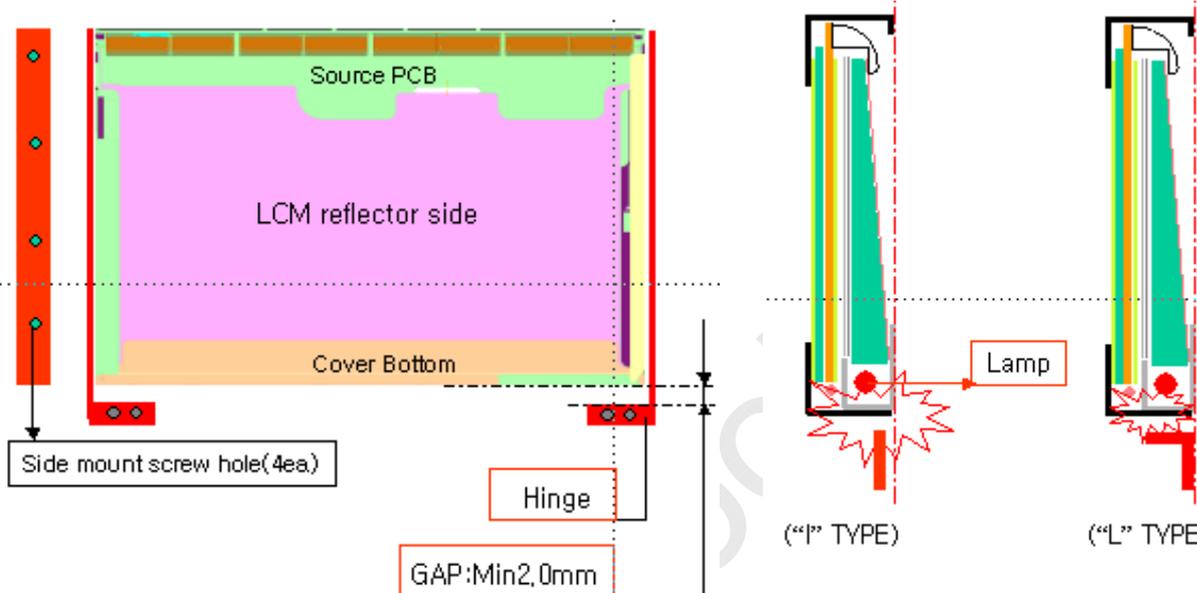
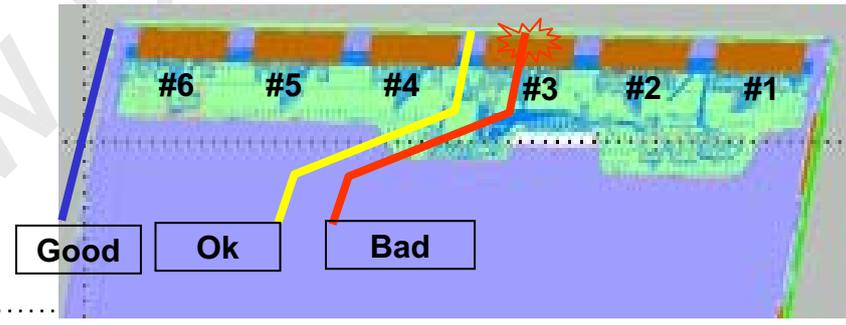
Product Specification

LGD Proposal for system cover design.(Appendix)

| | | |
|---|---|--|
| 1 | Gap check for securing the enough gap between LCM and System cover. | |
| <p>The diagram illustrates the assembly of the LCM and system cover. On the left, a top-down view of the LCM reflector side is shown with various colored regions. In the center, a cross-sectional view shows the LCM with a 'Max thickness' dimension and a 'A boundary line' indicated by a red dashed line. On the right, a side view shows the 'System cover' with a 'Sponge' layer between it and the LCM. Red arrows point from the sponge area towards the LCM boundary line.</p> | | |
| Define | <p>1.Rear side of LCM is sensitive against external stress,and previous check about interference is highly needed.</p> <p>2.In case there is something from system cover comes into the boundary above,mechanical interference may cause the FOS defects. (Eg:Ripple,White spot..)</p> | |
| 2 | Check if antenna cable is sufficiently apart from T-CON of LCD Module. | |
| Define | <p>The diagram compares two scenarios for antenna cable placement. The left scenario, labeled 'NO GOOD', shows the antenna cable (red) overlapping with the T-CON (black) on the LCD module. The right scenario, labeled 'GOOD', shows the antenna cable (red) routed away from the T-CON, maintaining a clear gap. Labels include 'Antenna', 'T-CON', and 'Antenna Cable'.</p> | |
| 1.If system antenna is overlapped with T-CON,it might be cause the noise. | | |

Product Specification

LGD Proposal for system cover design.

| | | |
|--|--|---------|
| 3 | Gap check for securing the enough gap between LCM and System hinge. | |
|  | | |
| Define | 1. At least 2.0mm of gap needs to be secured to prevent the shock related defects. 2. "L" type of hinge is recommended than "I" type under shock test. | |
| 4 | Checking the path of the System wire. | |
|  | | |
| Define | 1. COF area needs to be handled with care. 2. GOOD → Wire path design to system side. OK → Wire path is located between COFs. BAD → Wire path overlapped with COF area. | |
| Ver. 0.6 | Jul. 07, 2009 | 21 / 32 |

Product Specification

6. Reliability

Environment test condition

| No. | Test Item | Conditions |
|-----|---|---|
| 1 | High temperature storage test | Ta= 60°C, 240h |
| 2 | Low temperature storage test | Ta= -20°C, 240h |
| 3 | High temperature operation test | Ta= 50°C, 50%RH, 240h |
| 4 | Low temperature operation test | Ta= 0°C, 240h |
| 5 | Vibration test (non-operating) | Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis |
| 6 | Shock test (non-operating) | Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G, 2ms for all six faces) |
| 7 | Altitude operating storage / shipment | 0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr |

{ Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

7. International Standards

7-1. Safety

- a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc., Standard for Safety of Information Technology Equipment.
- b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association, Standard for Safety of Information Technology Equipment.
- c) EN 60950-1:2001, First Edition, European Committee for Electrotechnical Standardization(CENELEC) European Standard for Safety of Information Technology Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz." American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)



LP121WX3
Liquid Crystal Display

Product Specification

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

| | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| A | B | C | D | E | F | G | H | I | J | K | L | M |
|---|---|---|---|---|---|---|---|---|---|---|---|---|

A,B,C : SIZE(INCH)

E : MONTH

D : YEAR

F ~ M : SERIAL NO.

Note

1. YEAR

| | | | | | | | | | | |
|------|------|------|------|------|------|------|------|------|------|------|
| Year | 2001 | 2002 | 2003 | 2004 | 2005 | 2006 | 2007 | 2008 | 2009 | 2010 |
| Mark | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 |

2. MONTH

| | | | | | | | | | | | | |
|-------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
| Mark | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | A | B | C |

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module.
This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box : 30 pcs

b) Box Size : 480mm × 348mm × 243mm



LP121WX3
Liquid Crystal Display

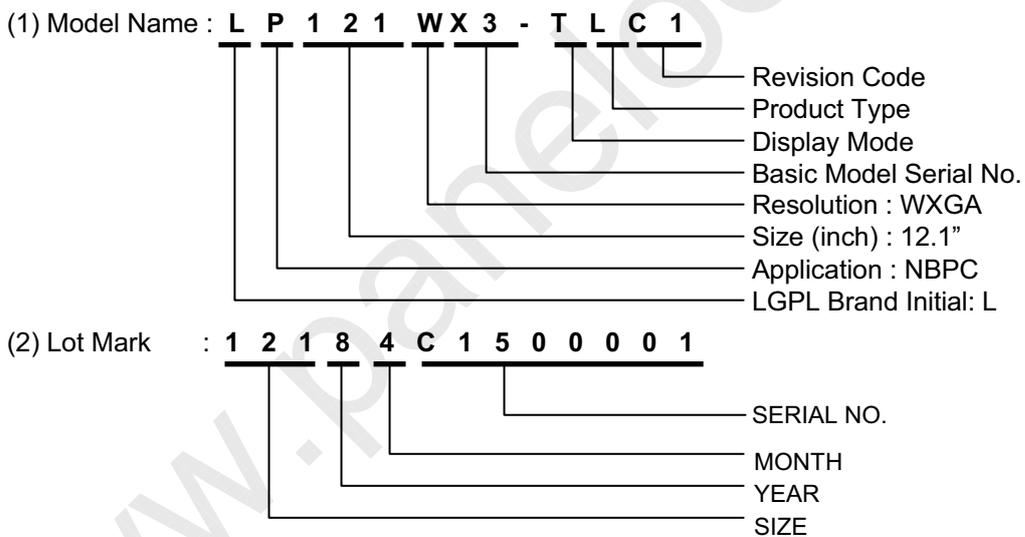
Product Specification

8-3. Label Description



LPL Code

Lot Mark



Lenovo Code

- 1)P/N : 42T0710
- 2)FRU : 42T0711

9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
 $V = \pm 200\text{mV}$ (Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer.
This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



LP121WX3
Liquid Crystal Display

Product Specification

APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

| | Byte (Dec) | Byte (Hex) | Field Name and Comments | Value (Hex) | Value (Bin) | | |
|---------------------------------|--------------------------------|------------|---|--|-------------|----------|----------|
| Header | 0 | 00 | Header | 00 | 00000000 | | |
| | 1 | 01 | Header | FF | 11111111 | | |
| | 2 | 02 | Header | FF | 11111111 | | |
| | 3 | 03 | Header | FF | 11111111 | | |
| | 4 | 04 | Header | FF | 11111111 | | |
| | 5 | 05 | Header | FF | 11111111 | | |
| | 6 | 06 | Header | FF | 11111111 | | |
| | 7 | 07 | Header | 00 | 00000000 | | |
| EDID | 8 | 08 | EISA manufacture code (3 Character ID) | LEN | 30 | 00110000 | |
| | 9 | 09 | EISA manufacture code (Compressed ASCII) | | AE | 10101110 | |
| | 10 | 0A | Panel Supplier Reserved - Product Code | 4011h | 11 | 00010001 | |
| | 11 | 0B | (Hex LSB first) | (12.1 WXGA 1280x800 , LED B/L) | 40 | 01000000 | |
| | 12 | 0C | LCD Module Serial No - Preferred but Optional ("0" If not used) | | 00 | 00000000 | |
| | 13 | 0D | LCD Module Serial No - Preferred but Optional ("0" If not used) | | 00 | 00000000 | |
| | 14 | 0E | LCD Module Serial No - Preferred but Optional ("0" If not used) | | 00 | 00000000 | |
| | 15 | 0F | LCD Module Serial No - Preferred but Optional ("0" If not used) | | 00 | 00000000 | |
| | 16 | 10 | Week of Manufacture | 00 weeks | 00 | 00000000 | |
| | 17 | 11 | Year of Manufacture | 2009 years | 13 | 00010011 | |
| Vendor / Product Version | 18 | 12 | EDID structure version # = 1 | | 01 | 00000001 | |
| | 19 | 13 | EDID revision # = 3 | | 03 | 00000011 | |
| | 20 | 14 | Video input Definition = Digital signal | | 80 | 10000000 | |
| | 21 | 15 | Max H image size (Rounded cm) = 26 cm | | 1A | 00011010 | |
| | 22 | 16 | Max V image size (Rounded cm) = 16 cm | | 10 | 00010000 | |
| | 23 | 17 | Display gamma = (gamma*100)-100 = Example:(2.2*100)-100=120 = 2.2 Gamma | | 78 | 01111000 | |
| | 24 | 18 | Feature Support (Standby, Suspend, Active Off/Very Low Power, RGB color display, Timing BLK 1,no_GTF) | | EA | 11101010 | |
| | 25 | 19 | Red/Green Low Bits (RxRy/GxGy) | | BA | 10111010 | |
| | 26 | 1A | Blue/White Low Bits (BxBY/WxWy) | | 95 | 10010101 | |
| | 27 | 1B | Red X Rx = 0.592 | | 97 | 10010111 | |
| Display Parameters | 28 | 1C | Red Y Ry = 0.351 | | 59 | 01011001 | |
| | 29 | 1D | Green X Gx = 0.334 | | 55 | 01010101 | |
| | 30 | 1E | Green Y Gy = 0.549 | | 8C | 10001100 | |
| | 31 | 1F | Blue X Bx = 0.154 | | 27 | 00100111 | |
| | 32 | 20 | Blue Y By = 0.130 | | 21 | 00100001 | |
| | 33 | 21 | White X Wx = 0.313 | | 50 | 01010000 | |
| | 34 | 22 | White Y Wy = 0.329 | | 54 | 01010100 | |
| | Panel Color Coordinates | 35 | 23 | Established timing 1 (00h if not used) | | 00 | 00000000 |
| | | 36 | 24 | Established timing 2 (00h if not used) | | 00 | 00000000 |
| | | 37 | 25 | Manufacturer's timings (00h if not used) | | 00 | 00000000 |
| Established Timin | 38 | 26 | Standard timing ID1 (01h if not used) | | 01 | 00000001 | |
| | 39 | 27 | Standard timing ID1 (01h if not used) | | 01 | 00000001 | |
| | 40 | 28 | Standard timing ID2 (01h if not used) | | 01 | 00000001 | |
| | 41 | 29 | Standard timing ID2 (01h if not used) | | 01 | 00000001 | |
| | 42 | 2A | Standard timing ID3 (01h if not used) | | 01 | 00000001 | |
| | 43 | 2B | Standard timing ID3 (01h if not used) | | 01 | 00000001 | |
| | 44 | 2C | Standard timing ID4 (01h if not used) | | 01 | 00000001 | |
| | 45 | 2D | Standard timing ID4 (01h if not used) | | 01 | 00000001 | |
| | 46 | 2E | Standard timing ID5 (01h if not used) | | 01 | 00000001 | |
| | 47 | 2F | Standard timing ID5 (01h if not used) | | 01 | 00000001 | |
| | 48 | 30 | Standard timing ID6 (01h if not used) | | 01 | 00000001 | |
| | 49 | 31 | Standard timing ID6 (01h if not used) | | 01 | 00000001 | |
| | 50 | 32 | Standard timing ID7 (01h if not used) | | 01 | 00000001 | |
| | 51 | 33 | Standard timing ID7 (01h if not used) | | 01 | 00000001 | |
| | 52 | 34 | Standard timing ID8 (01h if not used) | | 01 | 00000001 | |
| | 53 | 35 | Standard timing ID8 (01h if not used) | | 01 | 00000001 | |
| Standard Timing ID | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |
| | | | | | | | |



LP121WX3
Liquid Crystal Display

Product Specification

APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

| | Byte (Dec) | Byte (Hex) | Field Name and Comments | Value (Hex) | Value (Bin) |
|----------------------|------------|---|--|-------------|-------------|
| Timing Descriptor #1 | 54 | 36 | Pixel Clock/10,000 (LSB) 69.3 MHz @ 60Hz | 12 | 00010010 |
| | 55 | 37 | Pixel Clock/10,000 (MSB) | 1B | 00011011 |
| | 56 | 38 | Horizontal Active (lower 8 bits) 1280 Pixels | 00 | 00000000 |
| | 57 | 39 | Horizontal Blanking(Thp-HA) (lower 8 bits) 128 Pixels | 80 | 10000000 |
| | 58 | 3A | Horizontal Active / Horizontal Blanking(Thp-HA) (upper 4:4bits) | 50 | 01010000 |
| | 59 | 3B | Vertical Active 800 Lines | 20 | 00100000 |
| | 60 | 3C | Vertical Blanking (Tvp-HA) (DE Blanking typ.for DE only panels) 20 Lines | 14 | 00010100 |
| | 61 | 3D | Vertical Active : Vertical Blanking (Tvp-HA) (upper 4:4bits) | 30 | 00110000 |
| | 62 | 3E | Horizontal Sync. Offset (Thfp) 24 Pixels | 18 | 00011000 |
| | 63 | 3F | Horizontal Sync Pulse Width (HSPW) 32 Pixels | 20 | 00100000 |
| | 64 | 40 | Vertical Sync Offset(Tvfp) : Sync Width (VSPW) 4 Lines : 4 Lines | 44 | 01000100 |
| | 65 | 41 | Horizontal Vertical Sync Offset/Width (upper 2bits) | 00 | 00000000 |
| | 66 | 42 | Horizontal Image Size (mm) 261 mm | 05 | 00000101 |
| | 67 | 43 | Vertical Image Size (mm) 163 mm | A3 | 10100011 |
| 68 | 44 | Horizontal Image Size / Vertical Image Size | 10 | 00010000 | |
| 69 | 45 | Horizontal Border = 0 (Zero for Notebook LCD) | 00 | 00000000 | |
| 70 | 46 | Vertical Border = 0 (Zero for Notebook LCD) | 00 | 00000000 | |
| 71 | 47 | Non-Interlace, Normal display, no stereo, Digital Separate (Vsync_POS, Hsync_POS), DE only note : LSB is set to '1' if panel is DE-timing only. H/V can be ignored. | 1F | 00011111 | |
| Timing Descriptor #2 | 72 | 48 | Pixel Clock/10,000 (LSB) 57.79 MHz @ 50.1Hz | 93 | 10010011 |
| | 73 | 49 | Pixel Clock/10,000 (MSB) | 16 | 00010110 |
| | 74 | 4A | Horizontal Active (lower 8 bits) 1280 Pixels | 00 | 00000000 |
| | 75 | 4B | Horizontal Blanking(Thp-HA) (lower 8 bits) 128 Pixels | 80 | 10000000 |
| | 76 | 4C | Horizontal Active / Horizontal Blanking(Thp-HA) (upper 4:4bits) | 50 | 01010000 |
| | 77 | 4D | Vertical Active 800 Lines | 20 | 00100000 |
| | 78 | 4E | Vertical Blanking (Tvp-HA) (DE Blanking typ.for DE only panels) 20 Lines | 14 | 00010100 |
| | 79 | 4F | Vertical Active : Vertical Blanking (Tvp-HA) (upper 4:4bits) | 30 | 00110000 |
| | 80 | 50 | Horizontal Sync. Offset (Thfp) 24 Pixels | 18 | 00011000 |
| | 81 | 51 | Horizontal Sync Pulse Width (HSPW) 32 Pixels | 20 | 00100000 |
| | 82 | 52 | Vertical Sync Offset(Tvfp) : Sync Width (VSPW) 4 Lines : 4 Lines | 44 | 01000100 |
| | 83 | 53 | Horizontal Vertical Sync Offset/Width (upper 2bits) | 00 | 00000000 |
| | 84 | 54 | Horizontal Image Size (mm) 261 mm | 05 | 00000101 |
| | 85 | 55 | Vertical Image Size (mm) 163 mm | A3 | 10100011 |
| 86 | 56 | Horizontal Image Size / Vertical Image Size | 10 | 00010000 | |
| 87 | 57 | Horizontal Border = 0 (Zero for Notebook LCD) | 00 | 00000000 | |
| 88 | 58 | Vertical Border = 0 (Zero for Notebook LCD) | 00 | 00000000 | |
| 89 | 59 | Non-Interlace, Normal display, no stereo, Digital Separate (Vsync_POS, Hsync_POS), DE only note : LSB is set to '1' if panel is DE-timing only. H/V can be ignored. | 1F | 00011111 | |
| Timing Descriptor #3 | 90 | 5A | Flag | 00 | 00000000 |
| | 91 | 5B | Flag | 00 | 00000000 |
| | 92 | 5C | Flag | 00 | 00000000 |
| | 93 | 5D | Data Type Tag : Descriptor Defined by manufacturer | 0F | 00001111 |
| | 94 | 5E | Flag | 00 | 00000000 |
| | 95 | 5F | (Horizontal active pixel /8)-31 129 (1280 pixels) | 81 | 10000001 |
| | 96 | 60 | Image Aspect Ratio(16:10) 16:10 | 0A | 00001010 |
| | 97 | 61 | Low Refresh Rate #1(60Hz) 60 Hz | 3C | 111100 |
| | 98 | 62 | (Horizontal active pixel /8)-31 129 (1280 pixels) | 81 | 10000001 |
| | 99 | 63 | Image Aspect Ratio(16:10) 16:10 | 0A | 00001010 |
| | 100 | 64 | Low Refresh Rate #2(50Hz) 50 Hz | 32 | 110010 |
| | 101 | 65 | Brightness(1/10nit) 220 nits | 16 | 00010110 |
| | 102 | 66 | Feature flag (TN Technology ,LED Backlight) No definition | 09 | 00001001 |
| | 103 | 67 | Reserved 00h | 00 | 00000000 |
| 104 | 68 | EISA manufacturer code(3 Character ID) LGD | 30 | 00110000 | |
| 105 | 69 | Compressed ASCII | E4 | 11100100 | |
| 106 | 6A | Panel Supplier Reserved - Product code 01F0 | F0 | 11110000 | |
| 107 | 6B | (Hex, LSB first) | 01 | 00000001 | |



LP121WX3
Liquid Crystal Display

Product Specification

APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

| | Byte (Dec) | Byte (Hex) | Field Name and Comments | Value (Hex) | Value (Bin) |
|-----------------------------|---------------|--------------------------|--|----------------|----------------|
| <i>Timing Descriptor #4</i> | 108 | 6C | Flag | 00 | 00000000 |
| | 109 | 6D | Flag | 00 | 00000000 |
| | 110 | 6E | Flag | 00 | 00000000 |
| | 111 | 6F | Data Type Tag : Data String (ASCII String) | FE | 11111110 |
| | 112 | 70 | Flag | 00 | 00000000 |
| | 113 | 71 | Panel supplier P/N #1 = | L | 01001100 |
| | 114 | 72 | Panel supplier P/N #2 = | P | 01010000 |
| | 115 | 73 | Panel supplier P/N #3 = | 1 | 00110001 |
| | 116 | 74 | Panel supplier P/N #4 = | 2 | 00110010 |
| | 117 | 75 | Panel supplier P/N #5 = | 1 | 00110001 |
| | 118 | 76 | Panel supplier P/N #6 = | W | 01010111 |
| | 119 | 77 | Panel supplier P/N #7 = | X | 01011000 |
| | 120 | 78 | Panel supplier P/N #8 = | 3 | 00110011 |
| | 121 | 79 | Panel supplier P/N #9 = | - | 00101101 |
| | 122 | 7A | Panel supplier P/N #10 = | T | 01010100 |
| | 123 | 7B | Panel supplier P/N #11 = | L | 01001100 |
| 124 | 7C | Panel supplier P/N #12 = | C | 01000011 | |
| 125 | 7D | Panel supplier P/N #13 = | 1 | 00110001 | |
| <i>Checksum</i> | 126 | 7E | Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0) | 00 | 00000000 |
| | 127 | 7F | Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0) | AC | 10101100 |