



Product Specification

SPECIFICATION FOR APPROVAL

(•) Preliminar	y Specification
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() Final Specification

Title	12.5" HD TFT LCD

Customer	Fujitsu
MODEL	

SUPPLIER	LG Display Co., Ltd.		
*MODEL	LP125WH2		
Suffix	SLT3		

*When you obtain standard approval,
please use the above model name without suffi
x

APPROVED BY	SIGNATURE
11,	
Please return 1 copy for you	our confirmation wit

your signature and comments.

APPROVED BY	SIGNATURE
J. Y. Lee / Manager	
REVIEWED BY	
Y. S. Ha / Manager	
PREPARED BY	
H. W. Lee / Engineer J. M. Shin / Engineer	
Products Engineerir LG Display Co.,	

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3-2. Interface Connections

RECORD OF REVISIONS

Revision N o	Revision Dat e	Page	Description	EDID ver
0.0	Apr. 16. 2012	All	First Draft (Preliminary Specification)	1.3
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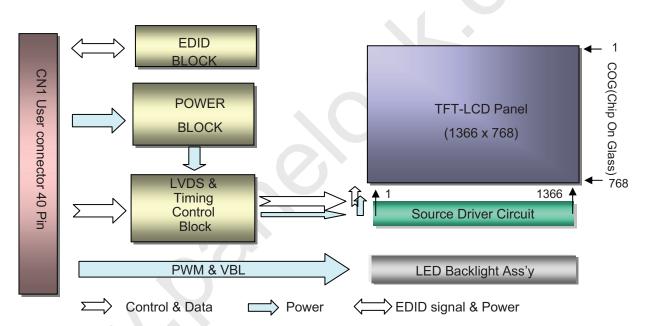
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1. General Description

The LP125WH2 is a Color Active Matrix Liquid Crystal Display with an integral LED backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 12.5inches diagonally measured active display area with HD re solution(1366 horizontal by 768 vertical pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP125WH2 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP125WH2 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP125W H2 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	12.5 inches diagonal			
Outline Dimension	290.5(H) × 170.2(V) × 2.85(D,Max.) [mm] (w/o PCB)			
Pixel Pitch	0.201mm × 0.201 mm			
Pixel Format	1366 horiz. By 768 vert. Pixels RGB strip arrangement			
Color Depth	6-bit, 262,144 colors			
Luminance, White	300 cd/m ² (Typ.5 point)			
Power Consumption	Total 4.33 W(Typ.) Logic :0.83 W (Typ.@ Mosaic), B/L : 3.5W (Typ.@ VLED 12V))			
Weight	205g (Max.)			
Display Operating Mode	AH-IPS, Normally Black			
Surface Treatment	Anti Glare treatment of the front polarizer			
RoHS Compliance	Yes			
BFR / PVC / As Free	Yes for all			

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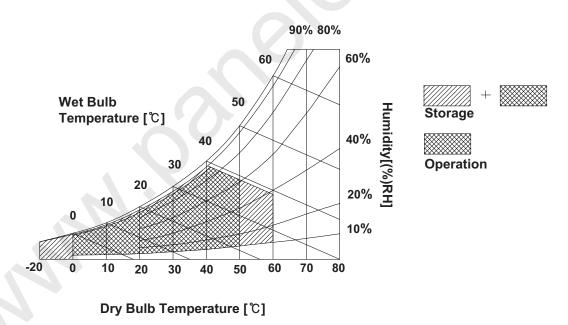
2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
raiailletei	Syllibol	Min	Max	Office	Notes	
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39°C Max, and no condensation of water.







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3. Electrical Specifications

3-1. Electrical Characteristics

The LP125WH2 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the LED BL.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter		Symbol	Values				
			Min	Тур	Max	Unit	Notes
LOGIC :							
Power Supply Input Voltage)	V cc	3.0	3.3	3.6	V	1
Power Supply Input Curre nt	Mosaic	Icc	-	252	290	mA	2
Power Consumption		Pcc	-(0.83	0.96	W	2
Power Supply Inrush Curre	nt	ICC_P	-		1500	mA	3
LVDS Impedance		ZLVDS	90	100	110	Ω	4
BACKLIGHT : (with LED Driv	ver)						
LED Power Input Voltage		VLED	7.0	12.0	21.0	V	5
LED Power Input Current		ILED	-	292	336	mA	6
LED Power Consumption		PLED	-	3.5	4.1	w	6
LED Power Inrush Current		ILED_P	-		2000	mA	7
PWM Duty Ratio			1	-	100	%	8
PWM Jitter		-	0	-	0.2	%	9
PWM Impedance		Zpwm	20	40	60	kΩ	
PWM Frequency		FPWM	-	210	-	Hz	10
PWM High Level Voltage	V _{PWM_H}	2.2	-	5.3	V		
PWM Low Level Voltage		V _{PWM_L}	0	-	0.3	V	
LED_EN Impedance		Zpwm	20	40	60	kΩ	
LED_EN High Voltage		VLED_EN_ H	2.2	-	5.3	V	
LED_EN Low Voltage		VLED_EN_L	0	-	0.3	V	
Life Time			12,000	-	-	Hrs	11

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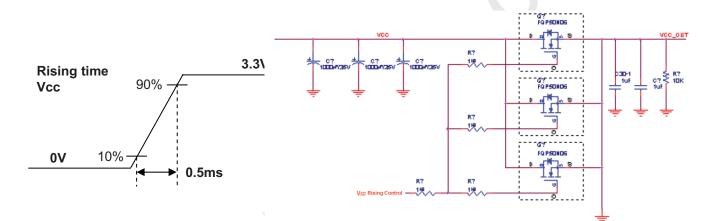




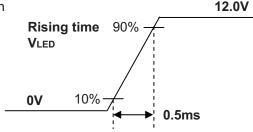
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Note)

- 1. The measuring position is the connector of LCM and the test conditions are under 25 ℃, fv = 60Hz, Black pattern.
- 2. The specified Icc current and power consumption are under the Vcc = 3.3V , 25 $^{\circ}$ C, fv = 60Hz condition and Mosaic pattern.
- 3. The below figures are the measuring Vcc condition and the Vcc control block LGD used. The Vcc condition is same as the minimum of T1 at Power on sequence.



- 4. This impedance value is needed for proper display and measured form LVDS Tx to the mating connector.
- 6. The current and power consumption with LED Driver are under the Vled = 12.0V, $25\,^{\circ}$ C, Dimming of Max luminance and White pattern with the normal frame frequency operated(60Hz).
- The below figures are the measuring Vled condition and the Vled control block LGD used.
 VLED control block is same with Vcc control block.



- 8. The operation of LED Driver below minimum dimming ratio may cause flickering or reliability issue.
- 9. This Spec. is not effective at 100% dimming ratio as an exception because it has DC level equivalent to 0Hz. In spite of acceptable range as defined, the PWM Frequency should be fixed and stable for more consistent brightness control at any specific level desired.
- 10. The life time is determined as the time at which brightness of LCD is 50% compare to that of minimum value specified in table 7. under general user condition.





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3-2. Interface Connections

This LCD employs two interface connections, a 40 pin connector used for the module electronics interface and t he other connector used for the integral backlight system.

Pin	Symbol	Description	Notes
1	NC	No Connection	[Interface Chip]
2	VCC	LCD Logic and driver power (3.3V Typ.)	1. LCD :
3	VCC	LCD Logic and driver power (3.3V Typ.)	SiW, SW0632 (LCD Controller)
4	V EEDID	DDC Power (3.3V)	Including LVDS Receiver.
5	NC	No Connection	2. System :
6	CIk EEDID	DDC Clock	* Pin to Pin compatible with LVDS
7	DATA EEDID	DDC Data	
8	ORX0-	Negative LVDS differential data input	[Connector]
9	ORX0+	Positive LVDS differential data input	LSMtron GT05Q-40S-H10 or equivalent
10	GND	LCM Ground	
11	ORX1-	Negative LVDS differential data input	[Mating Connector]
12	ORX1+	Positive LVDS differential data input	20345-#40E-## series or equivalent
13	GND	LCM Ground	[Connector pin arrangement]
14	ORX2-	Negative LVDS differential data input	[Connector pin arrangement]
15	ORX2+	Positive LVDS differential data input	
16	GND	LCM Ground	401
17	ORXC-	Negative LVDS differential clock input	<u></u>
18	ORXC+	Positive LVDS differential clock input	
19	GND	LCM Ground	
20	NC	No Connection	[LCD Module Rear View]
21	NC	No Connection	
22	GND	LCM Ground	
23	NC	No Connection	
24	NC NC	No Connection	
25	GND	LCM Ground	
26	NC	No Connection	
27	NC NC	No Connection	
28	GND	LCM Ground	
29	NC NC	No Connection	
30	NC	No Connection	
31	GND	LCM Ground (LED Backlight Ground)	
32	GND	LCM Ground (LED Backlight Ground)	
33	GND	LCM Ground (LED Backlight Ground)	
34	NC	No Connection	
35	PWM	System PWM Signal input for dimming	
36	LED_EN		
37	NC	LED Backlight On/Off No Connection	
37 	VLED	LED Backlight Power (7V-21V)	
38 39	VLED	LED Backlight Power (7V-21V)	
40	VLED	LED Backlight Power (7V-21V)	

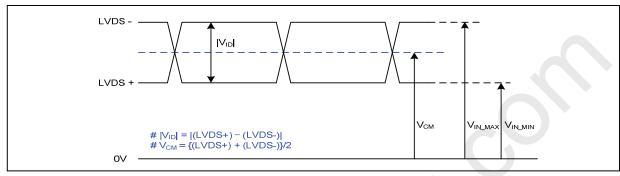




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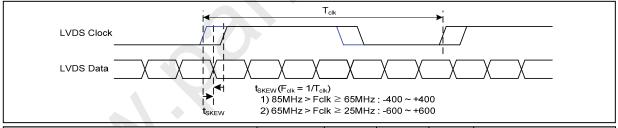
3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



Description	Symb ol	Min	Max	Unit	Notes
LVDS Differential Voltage	V _{ID}	100	600	mV	-
LVDS Common mode Voltage	V _{CM}	0.6	1.8	٧	-
LVDS Input Voltage Range	V _{IN}	0.3	2.1	V	-

3-3-2. AC Specification



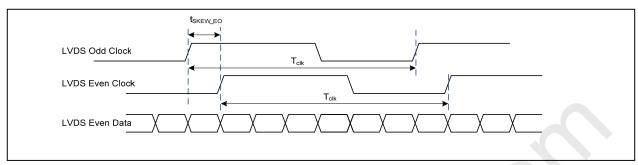
Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skew Margin	t _{skew}	- 400	+ 400	ps	85MHz > Fclk ≥ 65M Hz
LVDS Clock to Data Skew Margin	t _{skew}	- 600	+ 600	ps	65MHz > Fclk ≥ 25M Hz
LVDS Clock to Clock Skew Margin (Even to Odd)	t _{SKEW_EO}	- 1/7	+ 1/7	T _{clk}	-
Maximum deviation of input clock frequency during SSC	F _{DEV}	-	± 3	%	-
Maximum modulation frequency of input clock during SSC	F _{MOD}	-	200	KHz	-

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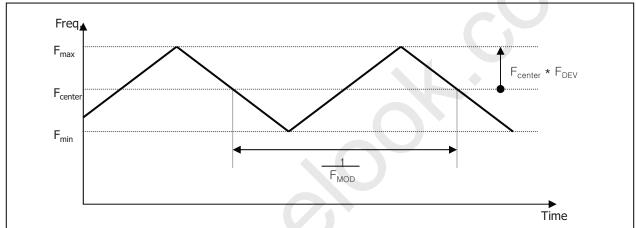




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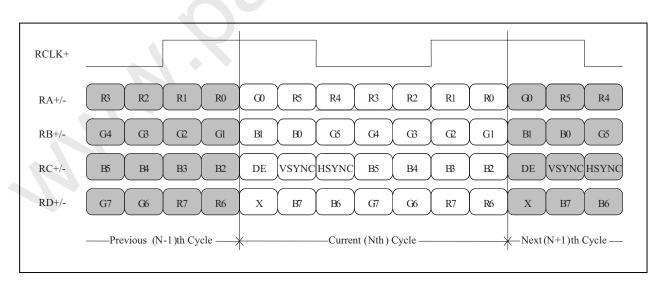
< Clock skew margin between channel >



< Spread Spectrum >

3-3-3. Data Format

- LVDS 1 Port



< LVDS Data Format >

Condition : VCC =3.3V





LP125WH2 Liquid Crystal Display

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3-4. Signal Timing Specifications

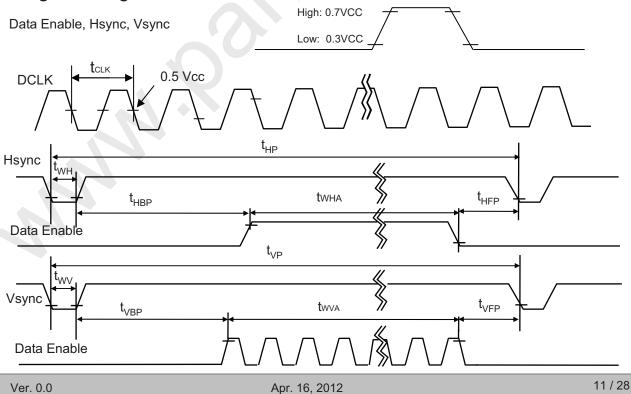
This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 5. TIMING TABLE

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	f _{CLK}	-	74.6	-	MHz	
	Period	Thp	1490	1578	1884		
Hsync	Width	t _{wh}	32	32	32	tCLK	
	Width-Active	t _{WHA}	1366	1366	1366		
	Period	t _{VP}	777	790	801		
Vsync	Width	t _{wv}	2	5	8	tHP	
	Width-Active	t _{wva}	768	768	768		
	Horizontal back porch	t _{HBP}	59	132	438	+CL I/	
Data	Horizontal front porch	t _{HFP}	33	48	48	tCLK	
Enable	Vertical back porch	t _{VBP}	6	15	20		
Appendix) Xertedabflious poech pecified	for tim tin ers	peci fi catio	n ba 2 ed o	n refr s sh r	tHP ate of 60	Hz.

Even though actual performance in 50Hz and 40Hz for low power is displayed normally, remark and inform to user that display quality in 40 Hz and 50 Hz is out of guarantee range.

3-5. Signal Timing Waveforms







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3-6. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

									Inp	ut Co	olor E	Data							
	Color			RI	ED					GRE	EEN					BL	UE		
`	30101	MSE	3				LSB	MSI	3				LSB	MSI	В				LSB
	T	R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	В3	B 2	В1	B 0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	. 0	0	0
	Red	1	1	.1	. 1	. 1	1	0	0	0	0	0	0	0	0	0		0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	. 1		1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED																			
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN																			
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE		ļ															 		
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

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3-7. Power Sequence

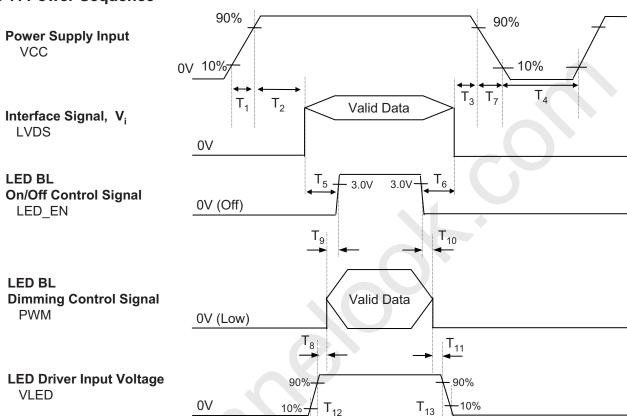


Table 6. POWER SEQUENCE TABLE

Logic		Value		Units	LED		Units		
Parameter	Min.	Тур.	Max.	Units	Parameter	Min.	Тур.	Max.	Ullits
T ₁	0.5		10	ms	T ₈	10	-	-	ms
T ₂	0	-	50	ms	T ₉	0	-	-	ms
T ₃	0	-	50	ms	T ₁₀	0	-	-	ms
T ₄	400	-	-	ms	T ₁₁	10	-	-	ms
T ₅	200	-	-	ms	T ₁₂	0.5	-	-	ms
T ₆	200	-	-	ms	T ₁₃	0	-	5000	ms
T ₇	3	-	10	ms					

- 1. Do not insert the mating cable when system turn on.
- 2. Valid Data have to meet "3-3. LVDS Signal Timing Specifications"
- 3. LVDS, LED_EN and PWM need to be on pull-down condition on invalid status.
- 4. LGD recommend the rising sequence of VLED after the Vcc and valid status of LVDS turn on.

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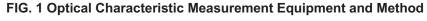


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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.



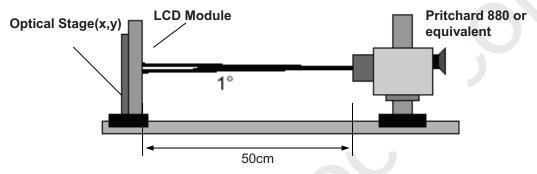


Table 8. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, fv=60Hz, f_{CLK}= 74.6 MHz

Davamatar	Symbo		Values		Units	Notes
Parameter		Min	Тур	Max	Units	notes
Contrast Ratio	CR	500	-	.	.]]	1
Surface Luminance, white	L _{WH}	255	300	-	cd/m ²	2
Luminance Variation	δ _{WHITE}	-	1.4	1.6		3
Response Time	Tr _R + Tr	-	35	50	ms	4
Color Coordinates			1		.]]	
RED	RX	0.563	0.593	0.623	.	
	RY	0.323	0.353	0.383	1	
GREEN	GX	0.305	0.335	0.365	1	
	GY	0.540	0.570	0.600		
BLUE	вх	0.125	0.155	0.185		
	BY	0.085	0.115	0.145		
WHITE	wx	0.283	0.313	0.343		
	WY	0.299	0.329	0.359		
Viewing Angle	1		1		. []	5
x axis, right(Φ=0°)	⊚r	80	<u>-</u>	-	degree	
x axis, left (Φ=180°)	ΘI	80	-	-	degree	
y axis, up (Φ=90°)	Θu	80	-	-	degree	
'ý axis, down (Φ=270	Θd	80	-	-	degree	
Gray Scale						6
Color Gamut	C/G	-	50	-	%	

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Note)

1. Contrast Ratio(CR) is defined mathematically as

Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$L_{WH} = Average(L_1, L_2, ... L_5)$$

3. The variation in surface luminance , The panel total variation (δ_{WHITE}) is determined by measuring L_N at each test position 1 through 13 and then defined as followed numerical formula. For more information see FIG 2.

$$\delta_{\text{WHITE}} = \frac{\text{Maximum}(\mathsf{L}_1, \mathsf{L}_2, \, \dots \, \mathsf{L}_{13})}{\text{Minimum}(\mathsf{L}_1, \mathsf{L}_2, \, \dots \, \mathsf{L}_{13})}$$

- 4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

*
$$f_V = 60Hz$$

Gray Level	Luminance [%] (Typ)
LO	TBD
L7	TBD
L15	TBD
L23	TBD
L31	TBD
L39	TBD
L47	TBD
L55	TBD
L63	100





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FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

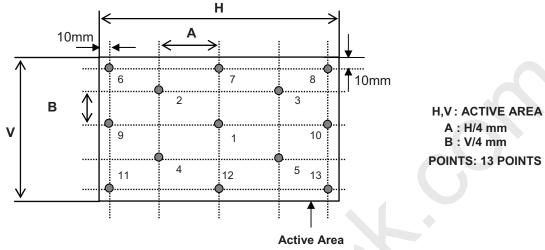


FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

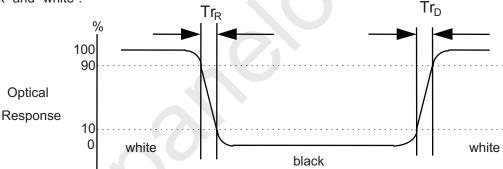
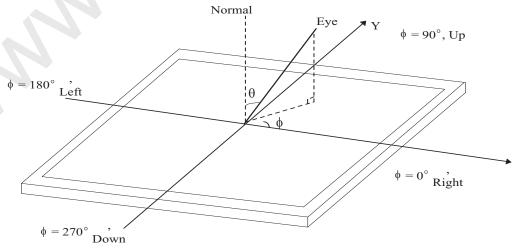


FIG. 4 Viewing angle







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5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP125WH2. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	290.5 ± 0.5 mm		
Outline Dimension	Vertical	170.2 ± 0.5 mm		
	Thickness	2.85mm (max) (w/o PCB)		
Bezel Area	Horizontal	280.1 ± 0.5 mm		
Bezei Area	Vertical	159.5 ± 0.5 mm		
Astina Diambon Area	Horizontal	275.59 mm		
Active Display Area	Vertical	154.94 mm		
Weight	205.0g (Max.)			
Surface Treatment	Anti Glare treatment of the front po	plarizer		

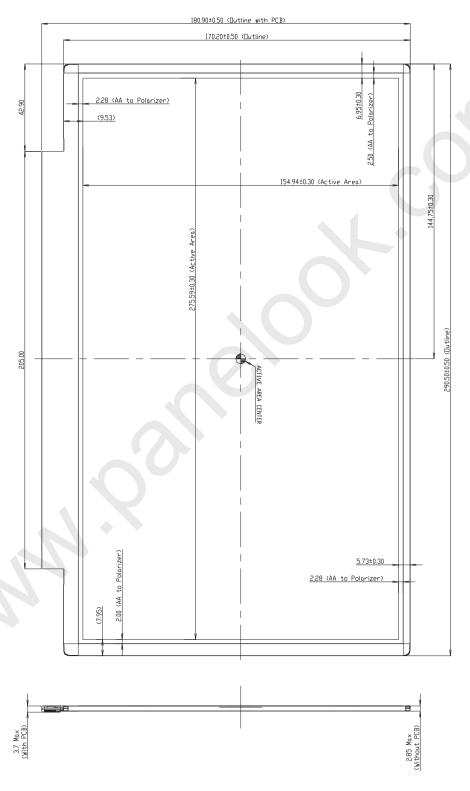




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<FRONT VIEW>

Note) Unit:[mm], General tolerance: \pm 0.5mm



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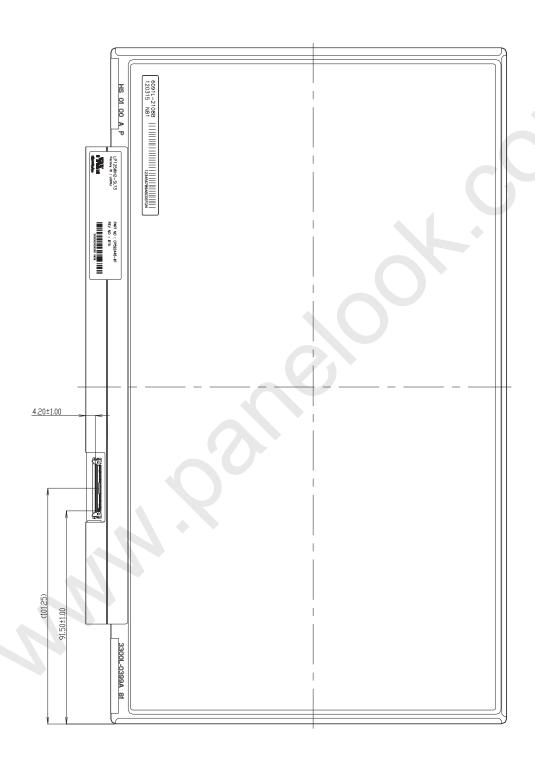




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<REAR VIEW>

Note) Unit:[mm], General tolerance: \pm 0.5mm



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6. Reliability

Environment test condition

No	Test Item	Conditions
1	High temperature storage test	Ta= 60°C, 240h
2	Low temperature storage test	Ta= -20°C, 240h
3	High temperature operation test	Ta= 50°C, 50%RH, 240h
4	Low temperature operation test	Ta= 0°C, 240h
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 6ms for all six faces)
7	Altitude operati ng	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr

storage / shipme

{ Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.





Product Specification

7. International Standards

7-1. Safety

- a) UL 60950-1, Second Edition, Underwriters Laboratories Inc.
 Information Technology Equipment Safety Part 1: General Requirements.
- b) CAN/CSA C22.2 No.60950-1-07, Second Edition, Canadian Standards Association. Information Technology Equipment Safety Part 1 : General Requirements.
- c) EN 60950-1:2006 + A11:2009, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment Safety Part 1 : General Requirements.
- d) IEC 60950-1:2005, Second Edition, The International Electrotechnical Commission (IEC). Information Technology Equipment Safety Part 1 : General Requirements.

7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003





Product Specification

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

Α	В	С	D	E	F	G	Н	I	J	K	L	М	
---	---	---	---	---	---	---	---	---	---	---	---	---	--

A,B,C : SIZE(INCH)

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	А	В	С	D	Е	F	G	Н	J	K

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

D:YEAR

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 30 pcs

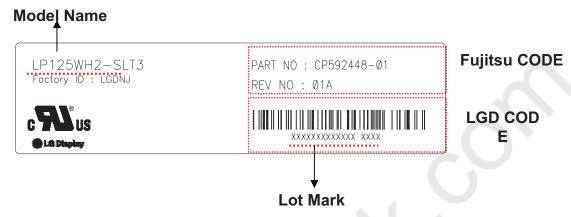
b) Box Size: 478 x 365 x 244



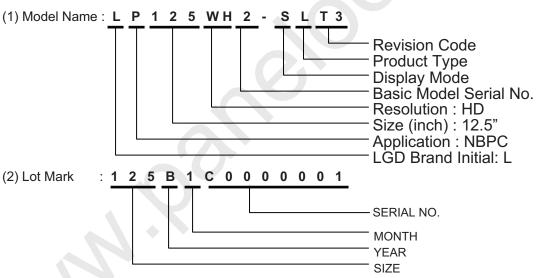


Product Specification

8-3. Label Description



LGD Code



Fujistu Code

1)Fujistu P/N : CP592448-01

2)REV NO: 01A





Product Specification

9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=± 200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.





Product Specification

9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.





Product Specification

APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	0	00	Header	00	00000000
	1	01	Header	FF	111111111
Header	2	02	Header	FF	11111111
	3	03	Header	FF	11111111
ea	4	04	Header	FF	11111111
H	5	05	Header	FF	11111111
	6	06	Header	FF	11111111
	7	07	Header	00	00000000
	8	08	EISA manufacture code (3 Character ID) LGD	30	00110000
Vendor / Product EDID Version	9	09	EISA manufacture code (Compressed ASC II)	E4	11100100
	10	0A	Panel Supplier Reserved - Product Code 039Bh	9B	10011011
	11	0B	(Hex. LSB first)	03	00000011
od Sic	12	0C	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
endor / Produ EDID Version	13	0D	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
7 2	14	0E	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
	15	0F	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
	16	10	Week of Manufacture 00 weeks	00	00000000
Ze	17	11	Year of Manufacture 2012 years	16	00010110
	18	12	EDID structure version # = 1	01	00000001
	19	13	EDID revision # = 3	03	00000011
Display Parameters	20	14	Video input Definition = Digital signal	80	10000000
	21	15	Max H image size (Rounded cm) = 28 cm	1C	00011100
	22	16	Max V image size (Rounded cm) = 16 cm	10	00010000
	23	17	Display gamma = (gamma*100)-100 = Example:(2.2*100)-100=120 = 2.2 Gamma	78	01111000
	23	17	Feature Support (no DPMS, no Active Off/Very Low Power, RGB color display, Timing BLK		01111000
	24	18	1,no GTF)	0 A	00001010
	25	19	Red/Green Low Bits (RxRy/GxGy)	DC	110111100
Panel Color Coordinates	26	1A	Blue/White Low Bits (BxBy/WxWy)	E5	11100101
	27	1B	Red X Rx = 0.593	97	10010111
	28	1C	$Red Y \qquad Ry = 0.353$	5A	01011010
	29	1D	Green X $Gx = 0.335$	55	01010101
	30	1 E	Green Y Gy = 0.570	92	10010010
70	31	1F	Blue X Bx = 0.155	27	00100111
0	32	20	Blue Y By = 0.115	1D	00011101
anel	33	21	White X $Wx = 0.313$	50	01010000
				_	
P	34	22	White Y $Wy = 0.329$	54	01010100
Established Timings	35	23	Established timing 1 (00h if not used)	00	00000000
	36	24	Established timing 2 (00h if not used)	00	00000000
	37	25	Manufacturer's timings (00h if not used)	00	00000000
	38	26	Standard timing ID1 (01h if not used)	01	00000001
	39	27	Standard timing ID1 (01h if not used)	01	00000001
	40	28	Standard timing ID2 (01h if not used)	01	00000001
	41	29	Standard timing ID2 (01h if not used)	01	00000001
gID	42	2A	Standard timing ID3 (01h if not used)	01	00000001
	43	2B	Standard timing ID3 (01h if not used)	01	00000001
in	44	2C	Standard timing ID4 (01h if not used)	01	00000001
Standard Timing ID	45	2D	Standard timing ID4 (01h if not used)	01	00000001
	46	2E	Standard timing ID5 (01h if not used) Standard timing ID5 (01h if not used)	01	00000001
rd		2F	i a a a a a a a a a a a a a a a a a a a	01	00000001
da	47 48		Standard timing ID5 (01h if not used) Standard timing ID6 (01h if not used)		
un		30	Standard timing ID6 (01h if not used) Standard timing ID6 (01h if not used)	01	00000001
St	49	31		01	00000001
	50	32	Standard timing ID7 (01h if not used)	01	00000001
	51	33	Standard timing ID7 (01h if not used)	01	00000001
	52	34	Standard timing ID8 (01h if not used)	01	00000001
	53	35	Standard timing ID8 (01h if not used)	01	00000001

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Product Specification

APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

	By te (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	54	36	Pixel Clock/10,000 (LSB) 74.6 MHz @ 59.8Hz	24	00100100
	55	37	Pixel Clock/10,000 (MSB)	1D	00011101
	56	38	Horizontal Active (lower 8 bits) 1366 Pixels	56	01010110
Timing Descriptor #1	57	39	Horizontal Blanking(Thp-HA) (lower 8 bits) 212 Pixels	D4	11010100
	58	3A	Horizontal Active / Horizontal Blanking(Thp-HA) (upper 4:4bits)	50	01010000
	59	3 B	Vertical Avtive 768 Lines	00	00000000
	60	3C	Vertical Blanking (Tvp-HA) (DE Blanking typ.for DE only panels) 22 Lines	16	00010110
	61	3D	Vertical Active : Vertical Blanking (Tvp-HA) (upper 4:4bits)	30	00110000
	62	3E	Horizontal Sync. Offset (Thfp) 48 Pixels	30	00110000
	63	3F	Horizontal Sync Pulse Width (HSPW) 32 Pixels	20	00100000
	64	40	Vertical Sync Offset(Tvfp): Sync Width (VSPW) 2 Lines: 5 Lines	25	00100101
	65	41	Horizontal Vertical Sync Offset/Width (upper 2bits)	00	00000000
mi	66	42	Horizontal Image Size (mm) 277 mm	15	00010101
Ë	67	43	Vertical Image Size (mm) 156 mm	9C	10011100
- 1	68		Horizontal Image Size / Vertical Image Size		
		44		10	00010000
	69	45	Horizontal Border = 0 (Zero for Notebook LCD)	00	
	70	46	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
	71	47	Non-Interlace, Normal display, no stereo, Digital Separate (Vsync_NEG, Hsync_POS), DE only note: LSB is set to '1' if panel is DE-timing only. H/V can be ignored.	1B	00011011
	72	48	Flag	00	00000000
	73	49	Flag	00	00000000
	74	4A	Flag	00	00000000
	75	4B	Data Type Tag (Descriptor Defined by manufacturer)	00	00000000
	76	4C	Flag	00	00000000
#2	77	4D	Descriptor Defined by manufacturer	00	00000000
± .	78	4E	Descriptor Defined by manufacturer	00	00000000
pto	79	4F	Descriptor Defined by manufacturer	00	00000000
Timing Descriptor #2	80	50	Descriptor Defined by manufacturer	00	00000000
S	81	51	Descriptor Defined by manufacturer	00	00000000
Q	82	52	Descriptor Defined by manufacturer	00	00000000
ing.	83	53	Descriptor Defined by manufacturer Descriptor Defined by manufacturer	00	00000000
in in	84	54	Descriptor Defined by manufacturer Descriptor Defined by manufacturer	00	00000000
	85	55			00000000
	_		Descriptor Defined by manufacturer	00	
	86	56	Descriptor Defined by manufacturer	00	00000000
	87	57	Descriptor Defined by manufacturer	00	00000000
	88	58	Descriptor Defined by manufacturer	00	00000000
	89	59	Descriptor Defined by manufacturer	00	00000000
ptor #3	90	5A	Flag	00	00000000
	91	5B	Flag	00	00000000
	92	5C	Flag	00	00000000
	93	5D	Data Type Tag (ASCII String)	FE	11111110
	94	5E	Flag	00	00000000
	95	5F	ASCII String L	4C	01001100
	96	60	ASCII String G	47	01000111
	97	61	ASCII String	20	00100000
cr	98	62	ASCII String D	44	01000100
Timing Descriptor #3	99	63	ASCII String i	69	01101001
	100	64	ASCII String s	73	01110011
	101	65	ASCII String p	70	01110000
	102	66	ASCII String 1	6C	01101100
	102				01100100
		67		61	
	104	68	ASCII String y	79	01111001
	105	69	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h	0A	00001010
	106	6A	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h	20	00100000
	107	6 B	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah,set remaining char = 20h	20	00100000

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Product Specification

APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	108	6C	Flag	00	00000000
	109	6D	Flag	00	00000000
	110	6E	Flag	00	00000000
	111	6F	Data Type Tag (ASCII String)	FE	11111110
	112	70	Flag	00	00000000
#	113	71	ASCII String L	4C	01001100
)r	114	72	ASCII String P	50	01010000
ipte	115	73	ASCII String 1	31	00110001
CL	116	74	ASCII String 2	32	00110010
Timing Descriptor #4	117	75	ASCII String 5	35	00110101
20	118	76	ASCII String W	57	01010111
ıin	119	77	ASCII String H	48	01001000
Ţ.	120	78	ASCII String 2	32	00110010
` '	121	79	ASCII String -	2 D	00101101
	122	7A	ASCII String S	53	01010011
	123	7B	ASCII String L	4C	01001100
	124	7C	ASCII String T	54	01010100
	125	7D	ASCII String 3	33	00110011
Checksum	126	7E	Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0)	00	00000000
Chec	127	7F	Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0)	02	00000010

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