LG.PHILIPS LCD PHILIPS

LP154WU1
Liquid Crystal Display

Product Specification

SPECIFICATION
FOR
APPROVAL

（　　）　**Preliminary Specification**

（ ● ）　**Final Specification**

Title	15.4" WUXGA TFT LCD

BUYER	DELL
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LP154WU1
Suffix	A1K1

*When you obtain standard approval,
please use the above model name without suffix

SIGNATURE	DATE
_____ / _____	_____
_____ / _____	_____
_____ / _____	_____

Please return 1 copy for your confirmation with your signature and comments.

SIGNATURE	DATE
J.H. LEE / G.Manager	_____
REVIEWED BY	
J.H. Park / Manager	
_____ / Manager	_____
PREPARED BY	
M.H.KIM / Engineer	
_____ / Engineer	_____

Products Engineering Dept.
LG. Philips LCD Co., Ltd

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RECORD OF REVISIONS

| Revision No	Revision Date	Page	Description	EDID ver
0.0	June.24.2004	-	First Draft	V0.0
0.1	July.05.2004	25~27	EDID change : Manufacturer code, Product code, Check Sum	V0.1
0.2	July.22.2004	25~27	EDID change : BIST code(DEC122;HEX 7A), Check Sum	V0.2
0.3	Aug. 04. 2004	25	EDID change : Field Name and Comments change (16,17,18,19)	V0.3
0.4	Sept.04.2004	25~27	EDID format change , Check sum	V0.4
0.5	Oct.21.2004	4,12,16	Weight(with inverter),RT,Weight(with inverter)	V0.4

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1. General Description

The LP154WU1 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp (CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 15.4 inches diagonally measured active display area with WUXGA resolution(1920 horizontal by 1200 vertical pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP154WU1 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP154WU1 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP154WU1(A1K1) characteristics provide an excellent flat display for office automation products such as Notebook PC.
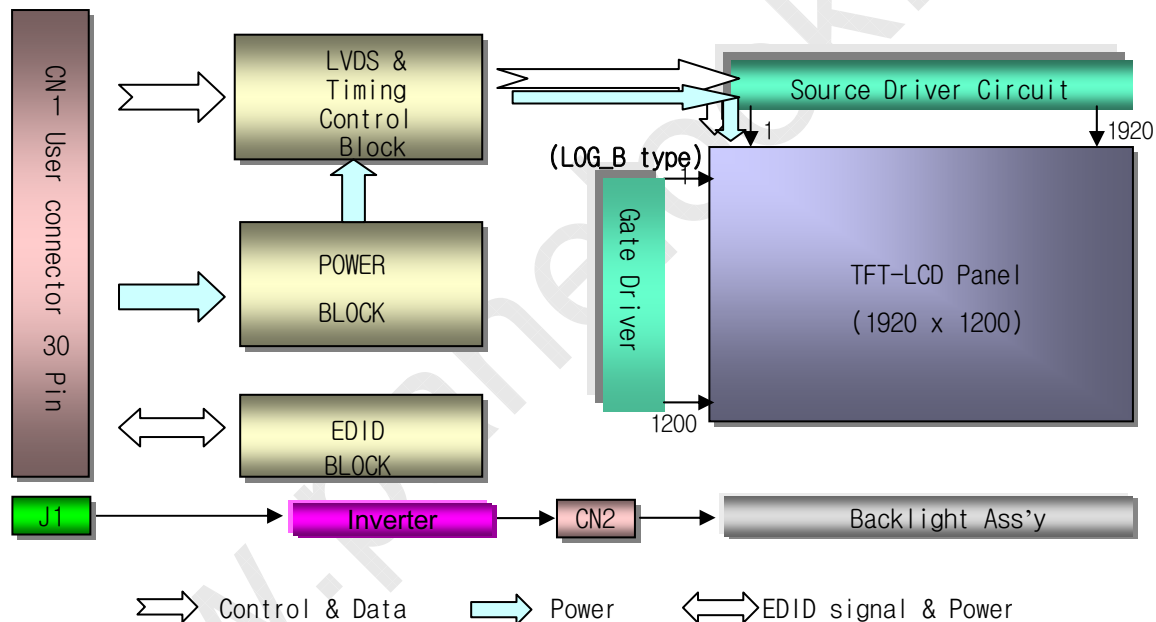
General Features

Active Screen Size	15.4 inches diagonal
Outline Dimension	344.5 (H) × 222.5 (V) × 6.5(D, max) mm
Pixel Pitch	0.1725 mm × 0. 1725 mm
Pixel Format	1920 horiz. by 1200 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	185 cd/m^2(Typ.) , 5 point
Power Consumption	Total 6.13 Watt(Typ.) @ LCM circuit 1.75 Watt(Typ.), B/L input 4.38 Watt(Typ.)
Weight	570 g (Max. with inverter)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer

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2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Values		Units	Notes
		Min	Max		
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C
Operating Temperature	TOP	0	50	°C	1
Storage Temperature	HST	-20	60	°C	1
Operating Ambient Humidity	HOP	10	90	%RH	1
Storage Humidity	HST	10	90	%RH	1

Note : 1. Temperature and relative humidity range are shown in the figure below.
        Wet bulb temperature should be 39°C Max, and no condensation of water.

Dry Bulb Temperature [°C]
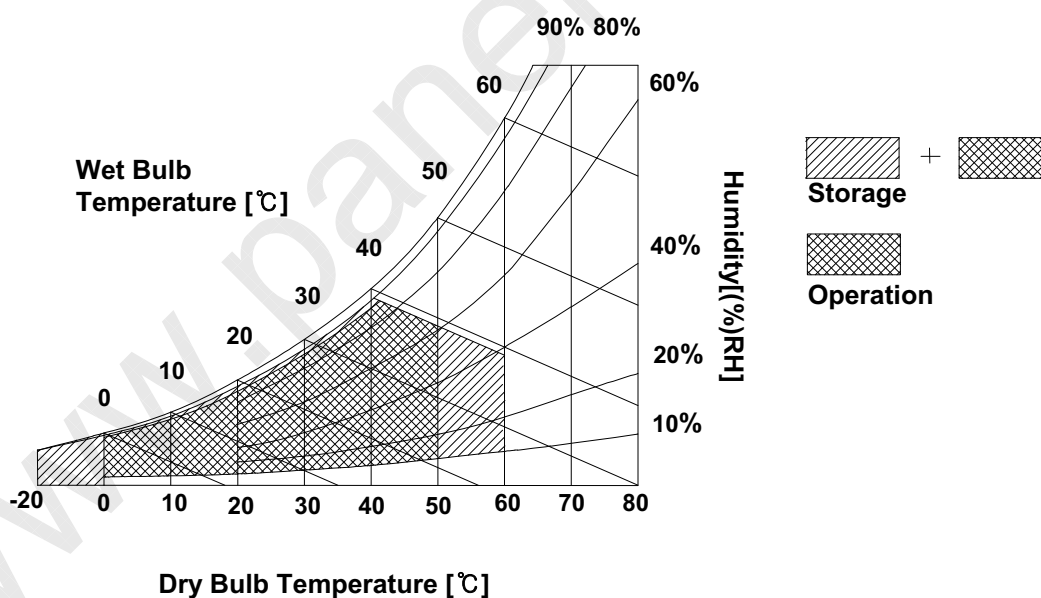
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3. Electrical Specifications

3-1. Electrical Characteristics

The LP154WU1(A1K1)requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Values Min	Values Typ	Values Max	Unit	Notes
MODULE :						
Power Supply Input Voltage	VCC	3.0	3.3	3.6	V_{DC}	
Power Supply Input Current	I_{CC}	450	530	610	mA	1
Power Consumption	Pc	1.49	1.75	2.01	Watt	1
Differential Impedance	Zm	90	100	110	Ohm	2
LAMP :						
Operating Voltage	V_{BL}	660 (7mA)	675 (6.5mA)	880 (2mA)	V_{RMS}	3
Operating Current	I_{BL}	2	6.5	7	mA_{RMS}	4
Power Consumption	P_{BL}	-	4.38	4.6		9
Operating Frequency	f_{BL}	40	60	80	kHz	7
Discharge Stabilization Time	Ts	-	-	3	Min	5
Life Time		10,000	-	-	Hrs	6
Established Starting Voltage at 25℃ at 0 ℃	Vs			1200 1560	V_{RMS} V_{RMS}	8

Note)
1. The specified current and power consumption are under the Vcc = 3.3V , 25℃, fv = 60Hz condition whereas Mosaic pattern is displayed and fv is the frame frequency.
2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
3. The variance of the voltage is ± 10%.
4. The typical operating current is for the typical surface luminance (L_{WH}) in optical characteristics.
5. Define the brightness of the lamp after being lighted for 5 minutes as 100%, Ts is the time required for the brightness of the center of the lamp to be not less than 95%.
6. The life time is determined as the time at which brightness of lamp is 50% compare to that of initial value at the typical lamp current.
7. The output of the inverter must have symmetrical(negative and positive) voltage waveform and symmetrical current waveform.(Asymmetrical ratio is less than 10%) Please do not use the inverter which has asymmetrical voltage and asymmetrical current and spike wave.
   Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the
    horizontal synchronous frequency and from its harmonics in order to prevent interference.
8. The voltage above VS should be applied to the lamps for more than 1 second for start-up. Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.
9. The lamp power consumption shown above does not include loss of external inverter.
   The applied lamp current is a typical one.

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Note)
9. Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following.
It shall help increase the lamp lifetime and reduce leakage current.

    a. The asymmetry rate of the inverter waveform should be less than 10%.

    b. The distortion rate of the waveform should be within $\sqrt{2} \pm 10\%$.

      * Inverter output waveform had better be more similar to ideal sine wave.

I_p

I_{-p}
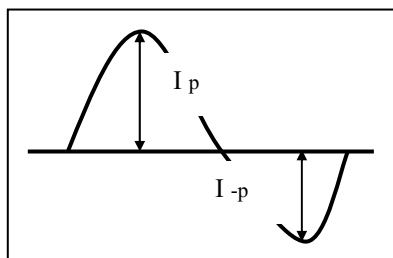
* Asymmetry rate:

$$| I_p - I_{-p} | / I_{rms} * 100\%$$

* Distortion rate

$$I_p \text{ (or } I_{-p}) / I_{rms}$$

※ Do not attach a conducting tape to lamp connecting wire.
If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.
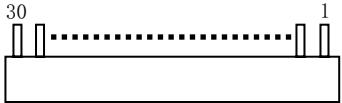
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3-2. Interface Connections

This LCD employs two interface connections, a 30 pin connector is used for the module electronics interface and the other connector is used for the integral backlight system.

The electronics interface connector is a model MDF76LBRW-30S-1H manufactured by HIROSE.

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	GND	Ground	1, Interface chips
2	VCC	Power Supply, 3.3V Typ.	1.1 LCD : KZ4E053G23CFP(LCD Controller)
3	VCC	Power Supply, 3.3V Typ.	including LVDS Receiver
4	V EEDID	DDC 3.3V power	(THINE, THC63LVD824A)
5	NC	Reserved for supplier test point	1.2 System : THC63LVD823A or equivalent
6	Clk EEDID	DDC Clock	* Pin to Pin compatible with THINE LVDS
7	DATA EEDID	DDC Data	
8	$R_{IN}0-$	Odd channel differential data input	2. Connector
9	$R_{IN}0+$	Odd channel differential data input	2.1 LCD : MDF76LBRW-30S-1H
10	GND	Ground	(HIROSE) or
11	$R_{IN}1-$	Odd channel differential data input	its compatibles
12	$R_{IN}1+$	Odd channel differential data input	2.2 Mating : FI-X30M or equivalent.
13	GND	Ground	2.3 Connector pin arrangement
14	$R_{IN}2-$	Odd channel differential data input	
15	$R_{IN}2+$	Odd channel differential data input	
16	GND	Ground	
17	CLKIN-	Odd channel differential clock input	
18	CLKIN+	Odd channel differential clock input	[LCD Module Rear View]
19	GND	Ground	
20	RA2-	Even channel differential data input	
21	RA2+	Even channel differential data input	
22	GND	Ground	
23	RB2-	Even channel differential data input	
24	RB2+	Even channel differential data input	
25	GND	Ground	
26	RC2-	Even channel differential data input	
27	RC2+	Even channel differential data input	
28	GND	Ground	
29	RCLK2-	Even channel differential clock input	
30	RCLK2+	Even channel differential clock input	

The backlight interface connector is a model BHSR-02VS-1, manufactured by JST or Compatible.
The mating connector part number is SM02B-BHSS-1 or equivalent.

Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION (J3)

Pin	Symbol	Description	Notes
1	HV	Power supply for lamp (High voltage side)	1
2	LV	Power supply for lamp (Low voltage side)	1

Notes : 1. The high voltage side terminal is colored pink and the low voltage side terminal is blue.

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3-3. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 6. TIMING TABLE

ITEM	Symbol		Min	Typ	Max	Unit	Note
DCLK	Frequency	f_{CLK}	80	81	82	MHz	
Hsync	Period	t_{HP}	1030	1080	1170	t_{CLK}	
	Width	t_{WH}	8	44	104		
	Active	t_{WHA}	960	960	960		
Vsync	Period	t_{VP}	1207	1250	1400	t_{HP}	
	Width	t_{WV}	1	3	6		
	Active	t_{WVA}	1200	1200	1200		
Data Enable	Horizontal back porch	t_{HBP}	8	-	-	t_{CLK}	
	Horizontal front porch	t_{HFP}	8	-	-		
	Vertical back porch	t_{VBP}	5	-	-	t_{HP}	
	Vertical front porch	t_{VFP}	1	-	-		

3-4. Signal Timing Waveforms

Condition : VCC =3.3V
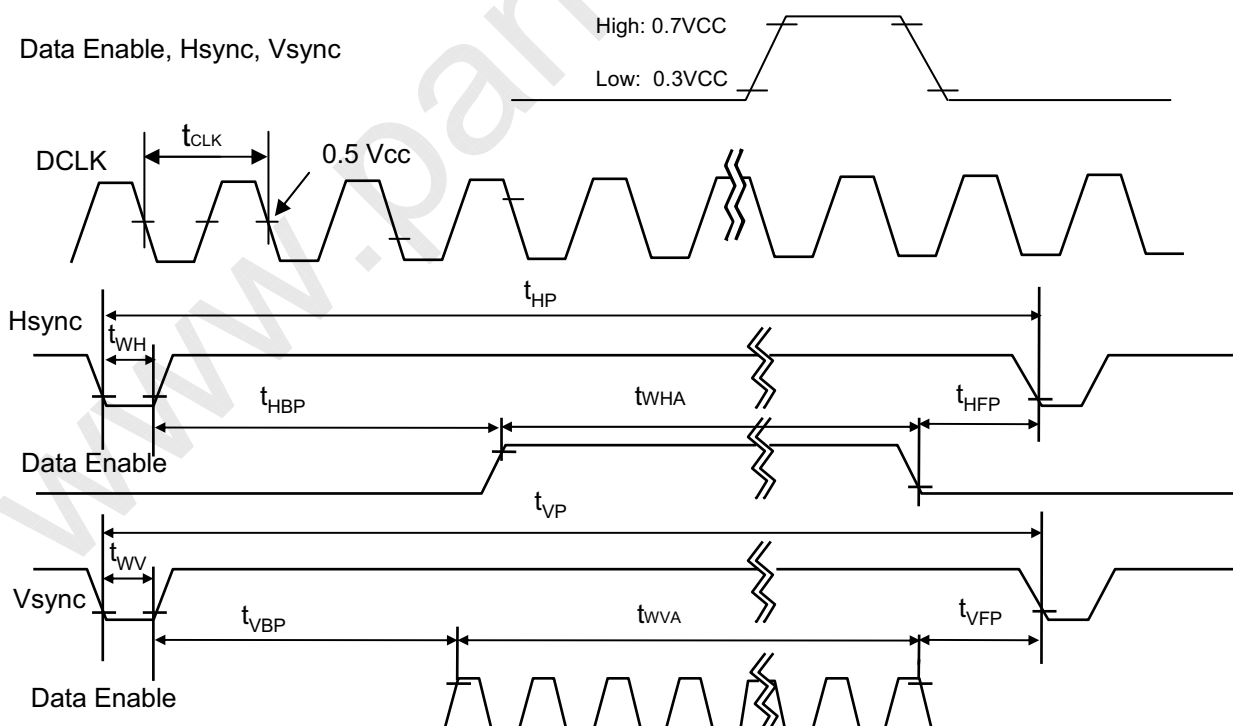
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3-5. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color ; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

Color		Input Color Data																			
		RED						GREEN						BLUE						
		MSB					LSB	MSB					LSB	MSB					LSB		
		R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	B 3	B 2	B 1	B 0	
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1	
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
RED	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	
	…			…						…						…				
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	
GREEN	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	
	…			…						…						…				
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0	
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	
BLUE	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
	…			…						…						…				
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0	
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	

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3-6. Power Sequence
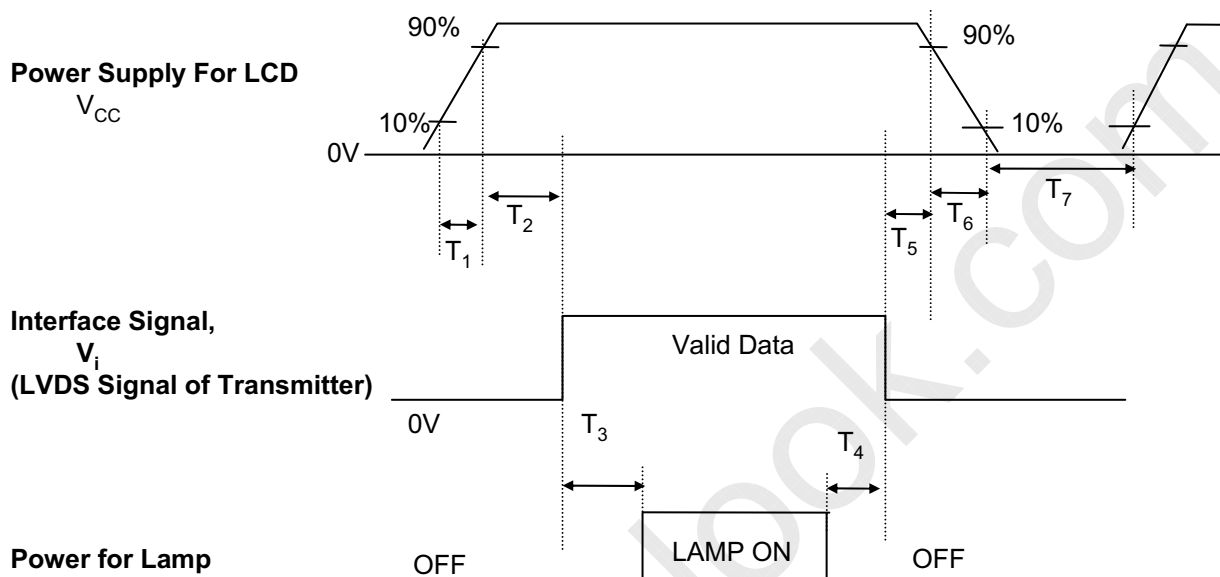
Table 8. POWER SEQUENCE TABLE

Parameter	Value			Units
	Min.	Typ.	Max.	
T_1	-	-	10	(ms)
T_2	0	-	50	(ms)
T_3	200	-	-	(ms)
T_4	200	-	-	(ms)
T_5	0	-	50	(ms)
T_6	0	-	10	(ms)
T_7	400	-	-	(ms)

Note)
   1. Please avoid floating state of interface signal at invalid period.
   2. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
   3. Lamp power must be turn on after power supply for LCD and interface signal are valid.

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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0°.
FIG. 1 presents additional information concerning the measurement equipment and method.

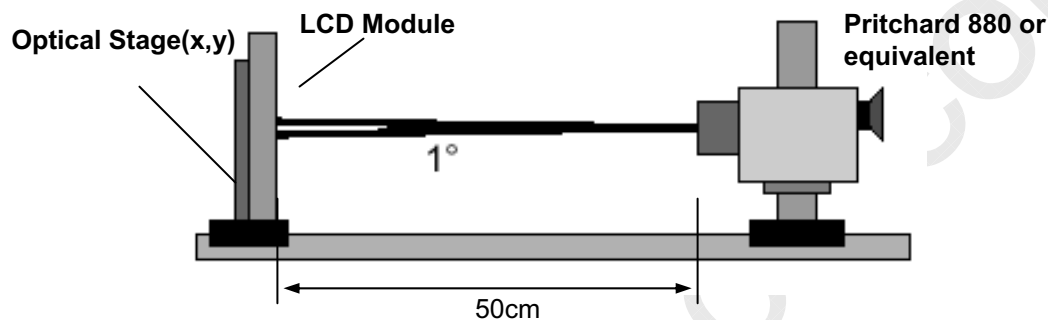## FIG. 1 Optical Characteristic Measurement Equipment and Method

Optical Stage(x,y)   LCD Module   Pritchard 880 or equivalent

1°

50cm

Table 9. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, fv=60Hz, f_{CLK}= 162MHz, Iout = 6.5mA

Parameter		Symbol	Values			Units	Notes
			Min	Typ	Max		
Contrast Ratio		CR	300	-	-		1
Surface Luminance, white		L_{WH}	160	185	-	cd/m^2	2
Luminance Variation		δ_{WHITE}	-	-	2.0		2
Response Time							3
	Rise Time+Decay Time	$Tr_R + Tr_D$	-	25	35	ms	
Color Coordinates							±0.03
	RED	RX	0.555	0.585	0.615		
		RY	0.302	0.332	0.362		
	GREEN	GX	0.292	0.322	0.352		
		GY	0.513	0.543	0.573		
	BLUE	BX	0.125	0.155	0.185		
		BY	0.105	0.135	0.165		
	WHITE	WX	0.283	0.313	0.343		
		WY	0.299	0.329	0.359		
Viewing Angle							5
	x axis, right(Φ=0°)	Θr	65	-	-	degree	
	x axis, left (Φ=180°)	Θl	65	-	-	degree	
	y axis, up (Φ=90°)	Θu	50	-	-	degree	
	y axis, down (Φ=270°)	Θd	50	-	-	degree	
Gray Scale							6

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Note)
1. Contrast Ratio(CR) is defined mathematically as

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

2. Surface luminance is the 5point (1~5)average across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 2.
   When I_{BL}= 6.5mA, $L_{WH=}$185cd/m^2(typ.)

3. Luminance % uniformity is measured for 13 point For more information see FIG 2.
   δ WHITE = Maximum(LN1,LN2, ….. LN13) ÷ Minimum(LN1,LN2, ….. LN13)

4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.

5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.

6. Gray scale specification   * f_V=60Hz

Gray Level	Luminance [%] (Typ)
L0	0.17
L7	0.46
L15	3.22
L23	10.56
L31	24.0
L39	39.2
L47	57.4
L55	77.6
L63	100

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FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

H,V : ACTIVE AREA
A : H/4 mm
B : V/4 mm
POINTS: 13 POINTS
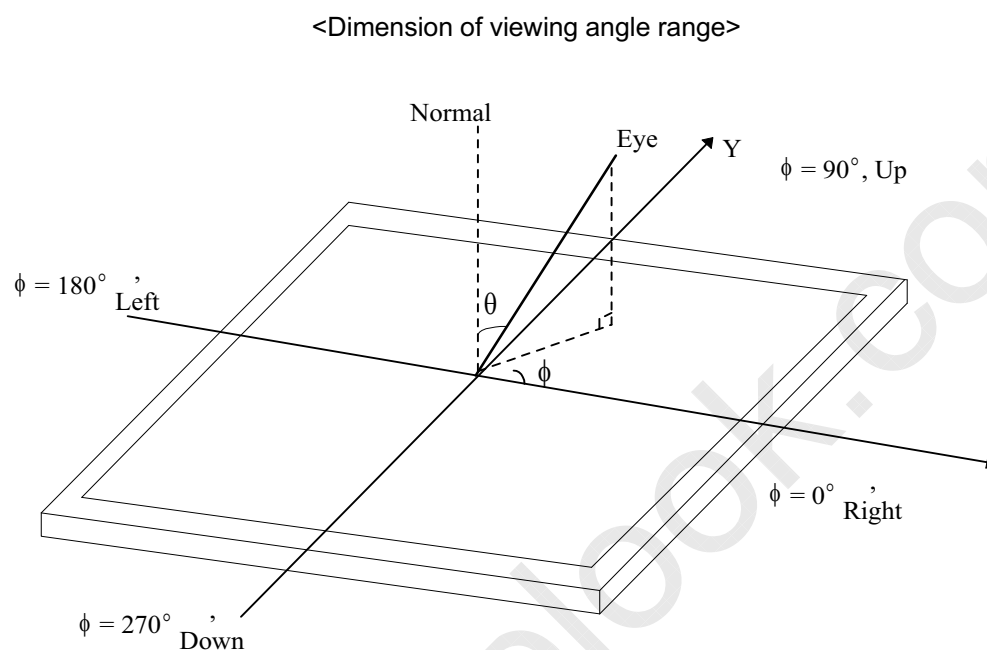
Active Area
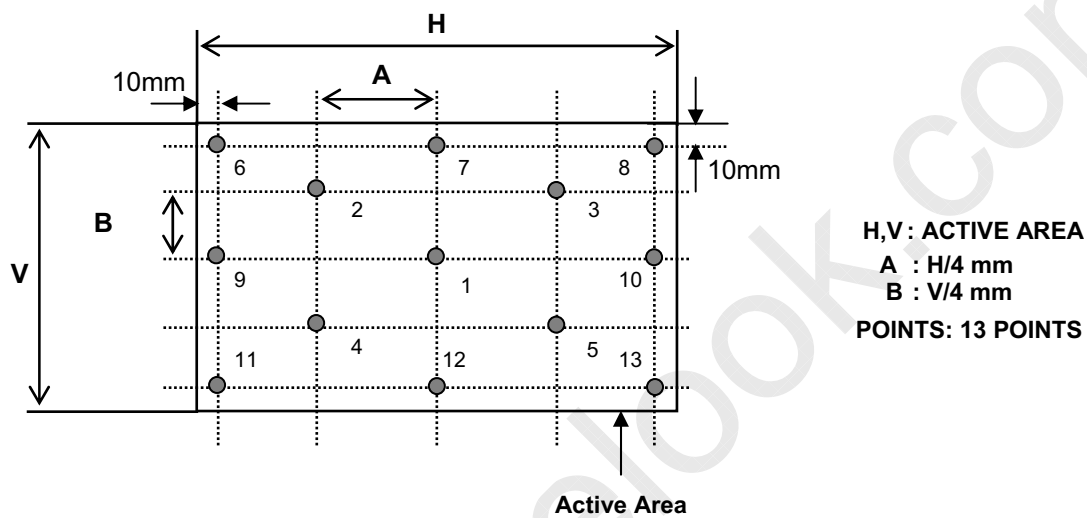
FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

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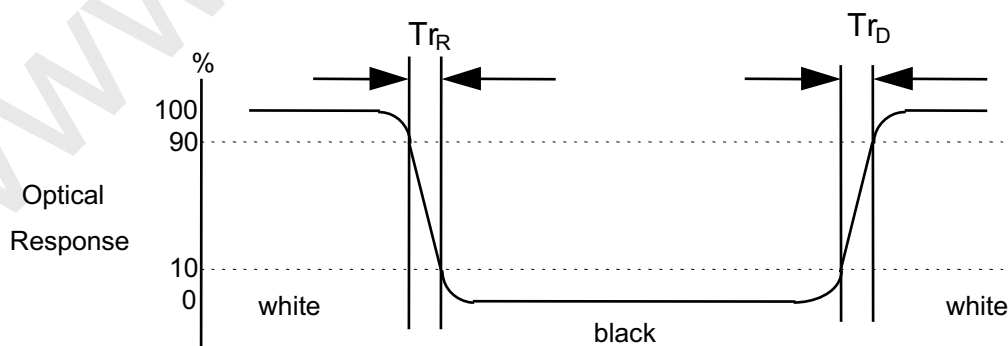
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FIG. 4 Viewing angle

<Dimension of viewing angle range>

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5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP154WU1(A1K1). In addition the figures in the next page are detailed mechanical drawing of the LCD.

|		
Outline Dimension	Horizontal	344.0 ± 0.5mm
	Vertical	222.0 ± 0.5mm
	Depth	6.2 ± 0.3mm
Bezel Area	Horizontal	335.0 ± 0.5mm
	Vertical	210.7 ± 0.5mm
Active Display Area	Horizontal	331.2 mm
	Vertical	207.0 mm
Weight	570g (MAX, with inverter)	
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer	

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<FRONT VIEW>

Note) Unit:[mm], General tolerance: ± 0.5mm

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<REAR VIEW>   Note) Unit:[mm], General tolerance: ± 0.5mm
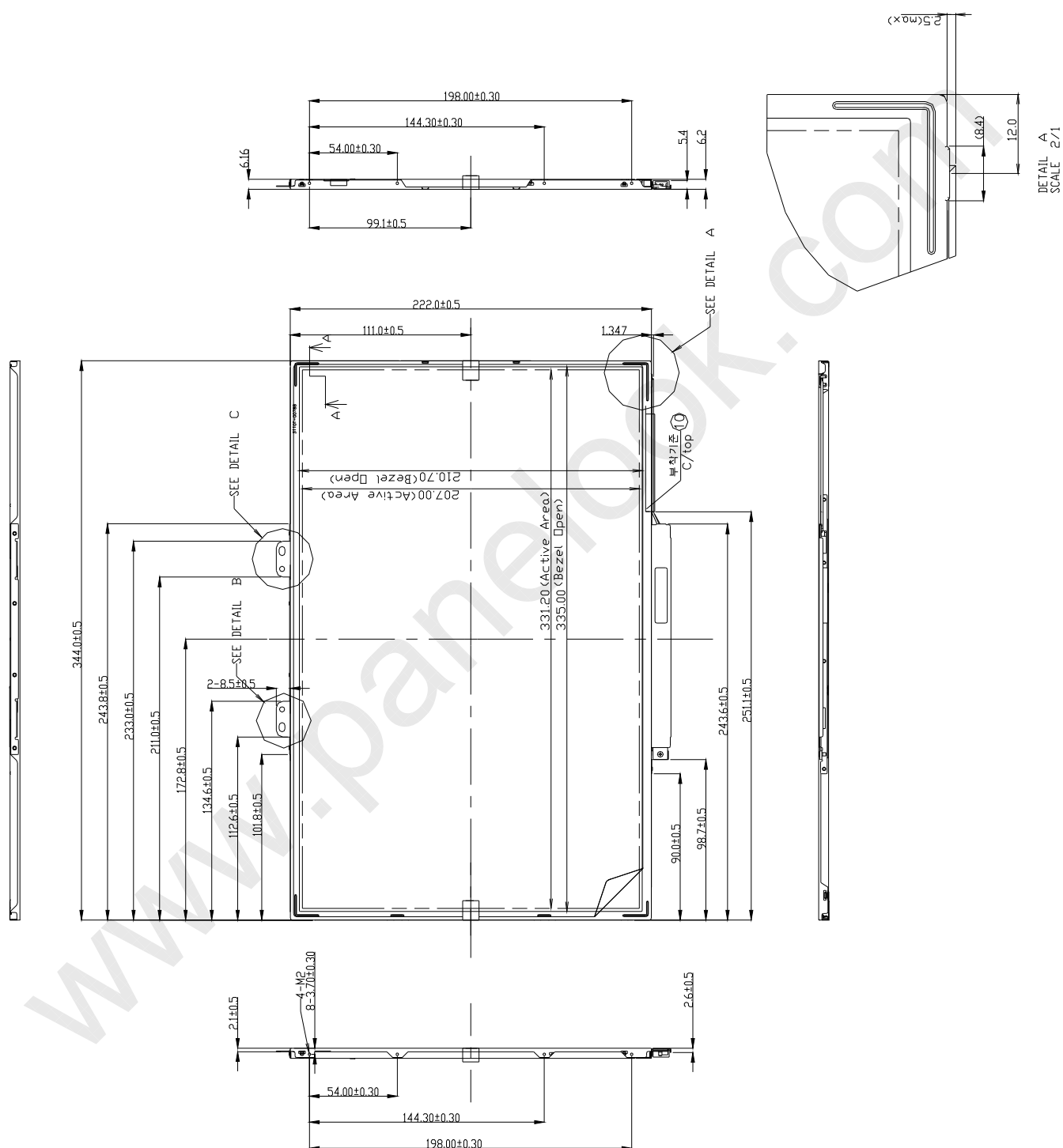
DETAIL C
SCALE 2/1

DETAIL B
SCALE 2/1

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[DETAIL DESCRIPTION OF SIDE MOUNTING SCREW]

*Screw Torque (8 point):

SECTION A-A
SCALE 5/1
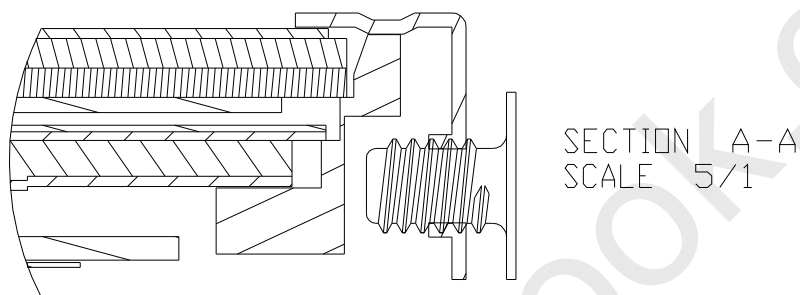
*SCREW(8ea) TORQUE : 2kgf.cm max
*Mounting SCREW Depth : 2.5mm max

Note) Unit:[mm], General tolerance: ± 0.5mm

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6. Reliability

Environment test condition

No.	Test Item	Conditions
1	High temperature storage test	Ta= 60°C, 240h
2	Low temperature storage test	Ta= -20°C, 240h
3	High temperature operation test	Ta= 50°C, 50%RH, 240h
4	Low temperature operation test	Ta= 0°C, 240h
5	Vibration test (non-operating)	Sine wave, 5 ~ 150Hz, 1.5G, 0.37oct/min 3 axis, 30min/axis
6	Shock test (non-operating)	- No functional or cosmetic defects following a shock to all 6 sides delivering at least 200 G in a half sine pulse no longer than 2 ms to the display module - No functional defects following a shock delivering at least 260 g in a half sine pulse no longer than 2 ms to each of 6 sides. Each of the 6 sides will be shock tested with one each display, for a total of 6 displays
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr

{ Result Evaluation Criteria }
 There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

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7. International Standards

7-1. Safety

a) UL 60950, Third Edition, Underwriters Laboratories, Inc., Dated Dec. 11, 2000.
Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.
b) CAN/CSA C22.2, No. 60950, Third Edition, Canadian Standards Association, Dec. 1, 2000.
Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.
c) EN 60950 : 2000, Third Edition
IEC 60950 : 1999, Third Edition
European Committee for Electrotechnical Standardization(CENELEC)
EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business
Equipment.

7-2. EMC

a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and
Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI),
1992
b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information
Technology Equipment." International Special Committee on Radio Interference.
c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information
Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998
(Including A1: 2000)

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8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

| A | B | C | D | E | F | G | H | I | J | K | L | M |

A,B,C : SIZE(INCH)
D : YEAR   E : MONTH
F : PANEL CODE   G : FACTORY CODE
H : ASSEMBLY CODE   I,J,K,L,M : SERIAL NO.

Note

1. YEAR

Year	97	98	99	2000	2001	2002	2003	2004	2005	2006	2007
Mark	7	8	9	0	1	2	3	4	5	6	7

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	A	B	C

3. PANEL CODE

Panel Code	P1 Factory	P2 Factory	P3 Factory	P4 Factory	P5 Factory	Hydis Panel
Mark	1	2	3	4	5	H

4. FACTORY CODE

Factory Code	LPL Gumi	LPL Nanjing
Mark	K	C

5. SERIAL NO.

Year	1 ~ 99999	100000 ~
Mark	00001 ~ 99999	A0001 ~ A9999, ... , Z9999

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module.
This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box : 10 pcs

b) Box Size : 437mm ×369mm × 339mm

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9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

(1) You must mount a module using holes arranged in four corners or four sides.
(2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
(3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
(4) You should adopt radiation structure to satisfy the temperature specification.
(5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
(6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
(8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
(9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

(1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
V=± 200mV(Over and under shoot voltage)

(2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
(3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
(4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
(5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
(6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

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9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
(2) The polarizer surface should not come in contact with any other object.
   It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

(1) When the protection film is peeled off, static electricity is generated between the film and polarizer.
   This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
(2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
   Please carefully peel off the protection film without rubbing it against the polarizer.
(3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
(4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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APPENDIX A. Enhanced Extended Display Identification Data (EEDID™)　　1/3

	Byte (hex)	Field Name and Comments	Value (hex)	Value (binary)
Header	0	Header	00	00000000
	1	Header	FF	11111111
	2	Header	FF	11111111
	3	Header	FF	11111111
	4	Header	FF	11111111
	5	Header	FF	11111111
	6	Header	FF	11111111
	7	Header	00	00000000
Vendor / Product EDID Version	8	EISA manufacture code = 3 Character ID = "LPL"	32	00110010
	9	EISA manufacture code (Compressed ASCII)	0C	00001100
	0A	Panel Supplier Reserved – Product Code	00	00000000
	0B	Panel Supplier Reserved – Product Code	00	00000000
	0C	LCD module Serial No - Preferred but Optional ("0" if not used)	00	00000000
	0D	LCD module Serial No - Preferred but Optional ("0" if not used)	00	00000000
	0E	LCD module Serial No - Preferred but Optional ("0" if not used)	00	00000000
	0F	LCD module Serial No - Preferred but Optional ("0" if not used)	00	00000000
	10	Week of manufacture = 00	00	00000000
	11	Year of manufacture = "2004"	0E	00001110
	12	EDID structure version # = 1	01	00000001
	13	EDID revision # = 3	03	00000011
Display Parameters	14	Video I/P definition = Digital I/P (80h)	80	10000000
	15	Max H image size = (Rounded to cm) = 33.12cm (33)	21	00100001
	16	Max V image size = (Rounded to cm) = 20.70cm (21)	15	00010101
	17	Display gamma = (gamma ×100)-100 = Example: (2.2× 100) – 100 = 120	78	01111000
	18	Feature support (no DPMS, Active off, RGB, timing BLK 1)	0A	00001010
Panel Color Coordinates	19	Red/Green Low bit (RxRy/GxGy)	C8	11001000
	1A	Blue/White Low bit (BxBy/WxWy)	E5	11100101
	1B	Red X　　Rx = 0.585	95	10010101
	1C	Red Y　　Ry = 0.332	55	01010101
	1D	Green X　Gx = 0.322	52	01010010
	1E	Green Y　Gy = 0.543	8B	10001011
	1F	Blue X　　Bx = 0.155	27	00100111
	20	Blue Y　　By = 0.135	22	00100010
	21	White X　Wx = 0.313	50	01010000
	22	White Y　Wy = 0.329	54	01010100
Established Timings	23	Established timings 1 (00h if not used)	00	00000000
	24	Established timings 2 (00h if not used)	00	00000000
	25	Manufacturer's timings (00h if not used)	00	00000000
Standard Timing ID	26	Standard timing ID1 (01h if not used)	01	00000001
	27	Standard timing ID1 (01h if not used)	01	00000001
	28	Standard timing ID2 (01h if not used)	01	00000001
	29	Standard timing ID2 (01h if not used)	01	00000001
	2A	Standard timing ID3 (01h if not used)	01	00000001
	2B	Standard timing ID3 (01h if not used)	01	00000001
	2C	Standard timing ID4 (01h if not used)	01	00000001
	2D	Standard timing ID4 (01h if not used)	01	00000001
	2E	Standard timing ID5 (01h if not used)	01	00000001
	2F	Standard timing ID5 (01h if not used)	01	00000001
	30	Standard timing ID6 (01h if not used)	01	00000001
	31	Standard timing ID6 (01h if not used)	01	00000001
	32	Standard timing ID7 (01h if not used)	01	00000001
	33	Standard timing ID7 (01h if not used)	01	00000001
	34	Standard timing ID8 (01h if not used)	01	00000001
	35	Standard timing ID8 (01h if not used)	01	00000001

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APPENDIX A. Enhanced Extended Display Identification Data (EEDID™)   2/3

Timing Descripter #1	36	Pixel Clock/10,000 (LSB) - 162MHz	48	01001000
	37	Pixel Clock/10,000 (MSB) - 162MHz	3F	00111111
	38	Horizontal Active = 1920 pixels (lower 8 bits)	80	10000000
	39	Horizontal Blanking (Thbp) = 240 pixels (lower 8 bits)	F0	11110000
	3A	Horizontal Active/Horizontal blanking (Thbp) (upper4:4 bits)	70	01110000
	3B	Vertical Active = 1200 lines	B0	10110000
	3C	Vertical Blanking (Tvbp) = 50 lines (DE Blanking typ. for DE only panels)	32	00110010
	3D	Vertical Active : Vertical Blanking (Tvbp) (upper4:4 bits)	40	01000000
	3E	Horizontal Sync, Offset (Thfp) = 100 pixels	64	01100100
	3F	Horizontal Sync, Pulse Width = 88 pixels	58	01011000
	40	Vertical Sync, Offset (Tvfp) = 1 lines Sync Width = 3 lines	13	00010011
	41	Horizontal Vertical Sync Offset/Width upper 2 bits	00	00000000
	42	Horizontal Image Size =331.2 mm	4B	01001011
	43	Vertical image Size = 207 mm	CF	11001111
	44	Horizontal Image Size / Vertical image size	10	00010000
	45	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
	46	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
	47	Non-interlaced, Normal, no stereo, Separate sync, H/V pol Negatives, **DE only note: LSB is set to "1" if panel is DE-timing only. H/V can be ignored.**	19	00011001
Timing Descripter #2	48	Pixel Clock/10,000 (LSB) - 162MHz	48	01001000
	49	Pixel Clock/10,000 (MSB) - 162MHz	3F	00111111
	4A	Horizontal Active = 1920 pixels (lower 8 bits)	80	10000000
	4B	Horizontal Blanking (Thbp) = 240 pixels (lower 8 bits)	F0	11110000
	4C	Horizontal Active/Horizontal blanking (Thbp) (upper4:4 bits)	70	01110000
	4D	Vertical Active = 1200 lines	B0	10110000
	4E	Vertical Blanking (Tvbp) = 50 lines (DE Blanking typ. for DE only panels)	32	00110010
	4F	Vertical Active : Vertical Blanking (Tvbp) (upper4:4 bits)	40	01000000
	50	Horizontal Sync, Offset (Thfp) = 100 pixels	64	01100100
	51	Horizontal Sync, Pulse Width = 88 pixels	58	01011000
	52	Vertical Sync, Offset (Tvfp) = 1 lines Sync Width = 3 lines	13	00010011
	53	Horizontal Vertical Sync Offset/Width upper 2 bits	00	00000000
	54	Horizontal Image Size =331.2 mm	4B	01001011
	55	Vertical image Size = 207 mm	CF	11001111
	56	Horizontal Image Size / Vertical image size	10	00010000
	57	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
	58	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
Timing Descripter #3 Dell specific information	59	Module "A" Revision = Example: 00, 01, 02, 03, etc.	00	00000000
	5A	Flag	00	00000000
	5B	Flag	00	00000000
	5C	Flag	00	00000000
	5D	Dummy Descriptor	FE	11111110
	5E	Flag	00	00000000
	5F	Dell P/N 1st Character = G	47	01000111
	60	Dell P/N 2nd Character = 5	35	00110101
	61	Dell P/N 3rd Character = 9	39	00111001
	62	Dell P/N 4th Character = 8	38	00111000
	63	Dell P/N 5th Character = 2	32	00110010
	64	LCD Supplier EEDID Revision # = VER 0.4	04	00000100
	65	Manufacturer P/N = 1	31	00110001
	66	Manufacturer P/N = 5	35	00110101
	67	Manufacturer P/N = 4	34	00110100
	68	Manufacturer P/N = W	57	01010111
	69	Manufacturer P/N = U	55	01010101
	6A	Manufacturer P/N = 1	31	00110001
	6B	Manufacturer P/N (If <13 char, then terminate with ASCII code 0Ah, set remaining char = 20h)	0A	00001010

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Timing Descripter #4	6C	Flag	00	00000000
	6D	Flag	00	00000000
	6E	Flag	00	00000000
	6F	Data Type Tag:	FE	11111110
	70	Flag	00	00000000
	71	SMBUS Value = 10nits +/-10% (5-point average)	E0	11100000
	72	SMBUS Value = 17nits +/-10% (5-point average)	C8	11001000
	73	SMBUS Value = 24nits +/-10% (5-point average)	B8	10111000
	74	SMBUS Value = 30nits +/-10% (5-point average)	B0	10110000
	75	SMBUS Value = 60nits +/-10% (5-point average)	88	10001000
	76	SMBUS Value = 110nits +/-10% (5-point average)	58	01011000
	77	SMBUS Value = 150nits +/-10% (5-point average)	30	00110000
	78	SMBUS Value = max nits (Typically = 00h, max nits)	00	00000000
	79	Number of LVDS receiver chips = '01' or '02'	02	00000010
	7A	BIST Enable: Yes = '01' No = '00'	01	00000001
	7B	(If <13 char, then terminate with ASCII code 0Ah, set remaining char = 20h)	0A	00001010
	7C	(If <13 char, then terminate with ASCII code 0Ah, set remaining char = 20h)	20	00100000
	7D	(If <13 char, then terminate with ASCII code 0Ah, set remaining char = 20h)	20	00100000
Checksum	7E	Extension flag (# of optional 128 EDID extension blocks to follow, Typ = 0)	00	00000000
	7F	Checksum (The 1-byte sum of all 128 bytes in this EDID block shall = 0)	E3	11100011