

10V/1.2A Single-chip Li-ion and Li-POL Charge

General Description

The LP28052A is a complete constant-current/ constant-voltage linear charger for single cell lithium-ion batteries. Its TDFN-10 package and low external component count make the LP28052A ideally suited for portable applications. Furthermore, the LP28052A is specifically designed to work within USB power specifications. Its have a over voltage protection in input with 6.5V and support over zero voltages of battery can charge enable.

No external sense resistor is needed, and no blocking diode is required due to the internal MOSFET architecture. Thermal feedback regulates the charge current to limit the die temperature during high power operation or high ambient temperature. The charge voltage is fixed at 4.2V, and the charge current can be ISET rammed externally with a single resistor. The LP28052A automatically terminates the charge cycle when the charge current drops to 1/10th the ISET rammed value after the final float voltage is reached.

When the input supply (wall adapter or USB supply) is removed, the LP28052A automatically enters a low current state, dropping the battery drain current to less than 2μA. The LP28052A can be put into shutdown mode, reducing the supply current to 25μA.

Other features include charge current monitor, undervoltage lockout, automatic recharge and a status pin to indicate charge termination and the presence of an input voltage.

Features

- Very Low Power Dissipation
- Vin Over Voltage Protection:6.5V
- Short-circuit protection
- Programmable Charge Current Up to 1500mA
- No MOSFET, Sense Resistor or Blocking Diode Required
- Constant-Current/Constant-Voltage Operation with Thermal Regulation to Maximize Charge Rate Without Risk of Overheating
- Charges Single Cell Li-Ion Batteries Directly from USB Port
- 25μA Supply Current in Shutdown
- Time Pre-charge Conditioning With Safety Timer Reverse Leakage Protection Prevents Battery
- Drainage Charge Current Thermal Regulation Status Outputs for LED or System Interface
- Indicates Charge and Fault Conditions
- Optional Battery Temperature Monitoring Before and During Charge Automatic Sleep Mode for Low-Power
- Consumption Available in SOP-8P Package
- RoHS Compliant and 100% Lead (Pb)-Free

Order Information

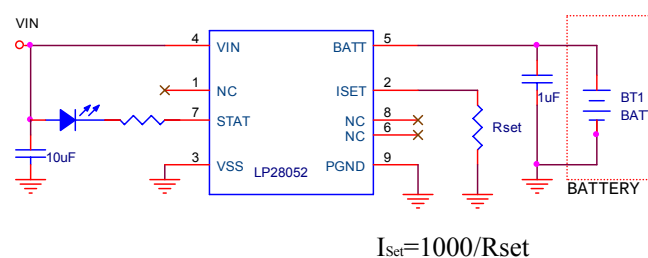
LP28052A □ □ □ F: Pb-Free

Package Type
SP: ESOP-8(P)
SO: SOP-8

Applications

- ✧ Portable Media Players/MP3 players
- ✧ Cellular and Smart mobile phone
- ✧ PDA/DSC
- ✧ Bluetooth Applications

Typical Application Circuit



Marking Information

Please see website.

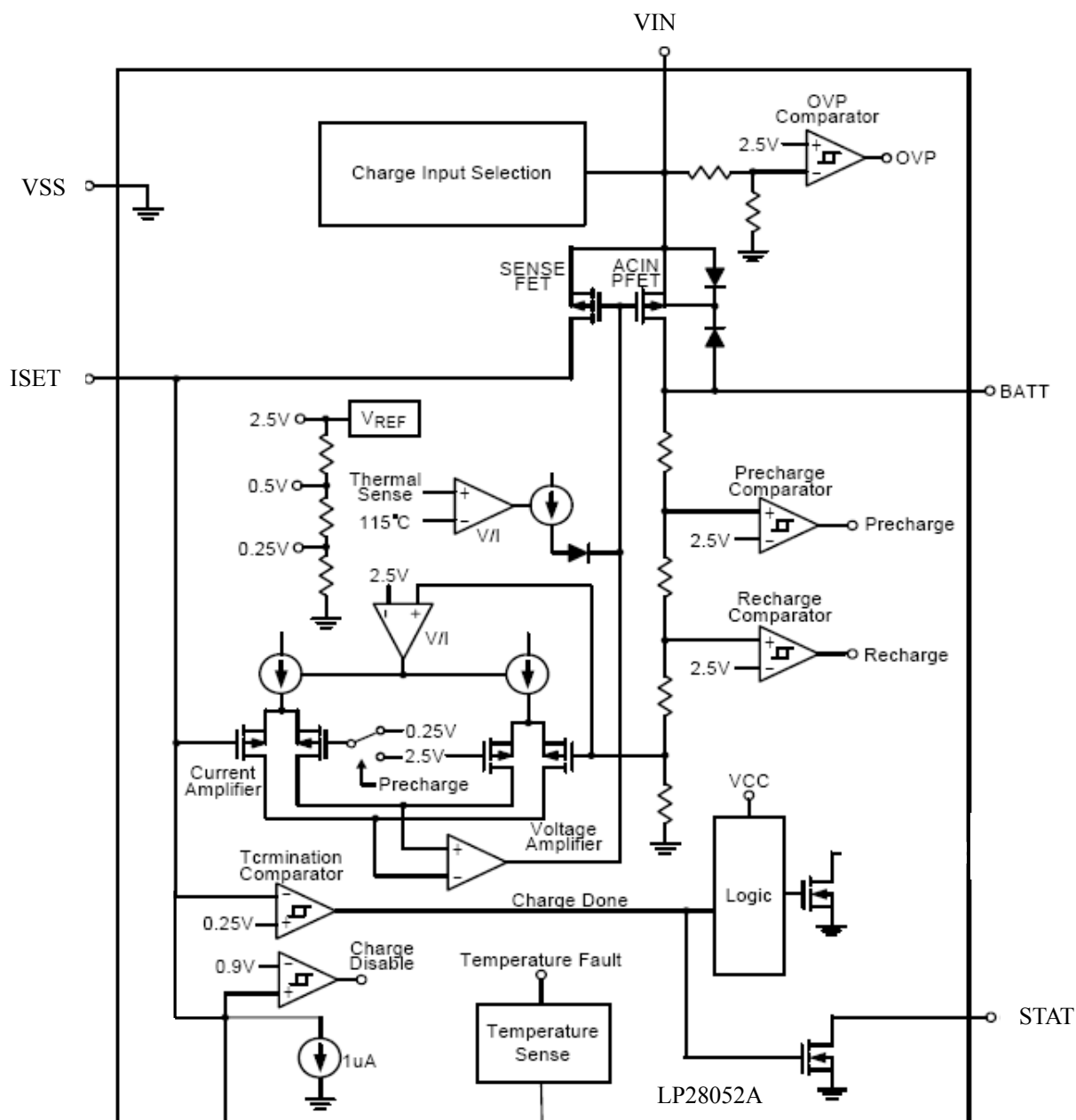
Functional Pin Description

Package Type	Pin Configurations
SOP-8(P)	

Pin Description

PIN	PIN Number	DESCRIPTION
VIN	4	VIN is the input power source. Connect to a wall adapter.
STAT	7	Open-Drain Charge Status Output. When the battery is charging, the STAT pin is pulled low by an internal N-channel MOSFET. When the charge cycle is completed, the pin is pulled High.
NC	1,6,8	No connector.
VSS/PGND	3,9	VSS is the connection to system ground.
ISET	2	Charge Current Program, Charge Current Monitor and Shutdown Pin. The charge current is programmed by connecting a 1% resistor(R_{PROG})to ground. When charging in constant-current mode, this pin serves to 1V. In all modes, the voltage on this pin can be used to measure the charge current using the following formula: $I_{BAT} = (V_{PROG}/R_{PROG}) \cdot 1000$
BATT	5	VBAT is the connection to the battery. Typically a 1μF Tantalum capacitor is needed for stability when there is no battery attached. When a battery is attached, only a 0.1μF ceramic capacitor is required.

Function Block Diagram



Absolute Maximum Ratings

- ✧ Input Voltage to GND (VIN) ----- 2.6V to 10V
- ✧ BAT, ISET, STAT (VX) ----- 0.3V to VIN+0.3V
- ✧ BAT Short-Circuit Duration ----- Continuous
- ✧ BAT Pin Current ----- 1500mA
- ✧ Maximum Junction Temperature ----- 125°C
- ✧ Operating Junction Temperature Range (TJ) ----- -40°C to 85°C
- ✧ Maximum Soldering Temperature (at leads, 10 sec) ----- 260°C

Thermal Information

- ✧ Maximum Power Dissipation (PD, TA<40°C) ----- 1.5W
- ✧ Thermal Resistance (JA) ----- 46°C/W

Electrical Characteristics

(The specifications which apply over the full operating temperature range, otherwise specifications are at TA = 25°C. VCC = 5V, unless otherwise noted.)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNITS
V _{IN}	Adapter/USB Voltage Range		2.65	5	7	V
I _{CC}	Input Supply Current	Charge Mode (Note 4), R _{ISET} = 10k		300	2000	uA
		Standby Mode (Charge Terminated)		200	500	
		Shutdown Mode (R _{ISET} Not Connected, VCC < V _{BAT} , or VCC < V _{UV})		25	50	
V _{FLOAT}	Regulated Output (Float) Voltage	0°C ≤ TA ≤ 85°C, I _{BAT} = 40mA	4.158	4.2	4.242	V
I _{BAT}	BAT Pin Current	R _{ISET} = 1k, Current Mode		1000		mA
		R _{ISET} = 2k, Current Mode		50		
		Standby Mode, V _{BAT} = 4.2V Shutdown Mode (R _{ISET} Not Connected) Sleep Mode, VCC = 0V	0	-2.5 ± 1 ± 1	-6 ± 2 ± 2	uA
I _{TRIKL}	Trickle Charge Current	V _{BAT} < V _{TRIKL} , R _{ISET} = 2k (Note 5)		50		mA
V _{TRIKL}	Trickle Charge Threshold Voltage	R _{ISET} = 10k, V _{BAT} Rising (Note 5)	2.8	2.9	3.0	V
V _{TRHYS}	Trickle Charge Hysteresis Voltage	R _{ISET} = 10k (Note 5)	60	80	110	mV
V _{UV}	VCC Undervoltage Lockout Threshold	From VCC Low to High	3.7	3.8	3.95	V
V _{UVHYS}	VCC Undervoltage Lockout Hysteresis		150	200	300	mV
V _{MSD}	Manual Shutdown Threshold Voltage	ISET Pin Rising	1.15	1.21	1.30	V
		ISET Pin Falling	0.9	1.0	1.1	V
V _{ASD}	VCC – V _{BAT} Lockout Threshold Voltage	VCC from Low to High	70	100	140	mV
		VCC from High to Low	5	30	50	mV
I _{TERM}	C/10 Termination Current Threshold	R _{ISET} = 10k (Note 6)	0.085	0.10	0.115	mA/mA
		R _{ISET} = 2k	0.085	0.10	0.115	mA/mA
V _{ISET}	ISET Pin Voltage	R _{ISET} = 10k, Current Mode	0.9	1.0	1.1	V
I _{STAT}	STAT Pin Weak Pull-Down Current	V _{STAT} = 5V	8	20	35	uA
V _{STAT}	STAT Pin Output Low Voltage	I _{STAT} = 5mA		0.35	0.6	V
ΔV _{RESTAT}	Recharge Battery Threshold Voltage	V _{FLOAT} - V _{RESTAT}	100	150	200	mV
T _{LIM}	Junction Temperature in Constant Temperature Mode				120	°C
R _{ON}	Power FET “ON” Resistance (Between VCC and BAT)				600	mΩ
t _{SS}	Soft-Start Time	I _{BAT} = 0 to I _{BAT} = 850V/R _{ISET}		100		uS
I _{ISET}	ISET Pin Pull-Up Current			3		uA
V _{IN OVP}	Over voltage protection		5.9	6.2	6.5	V

TYPICAL CHARACTERISTICS

DROPOUT VOLTAGE VS

JUNCTION TEMPERATURE

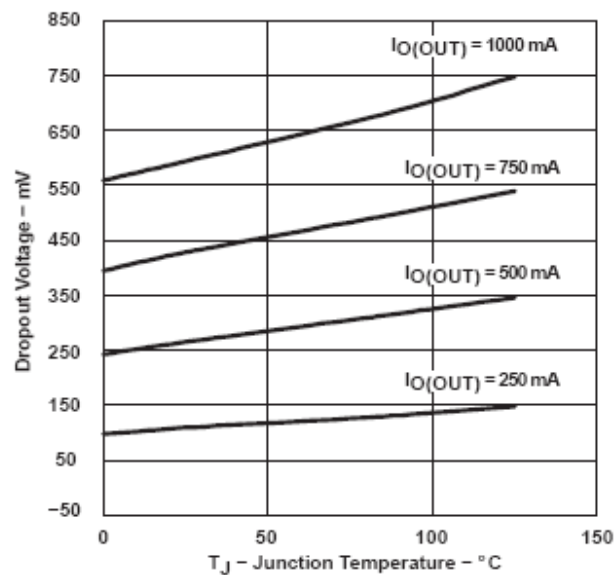


Figure 1

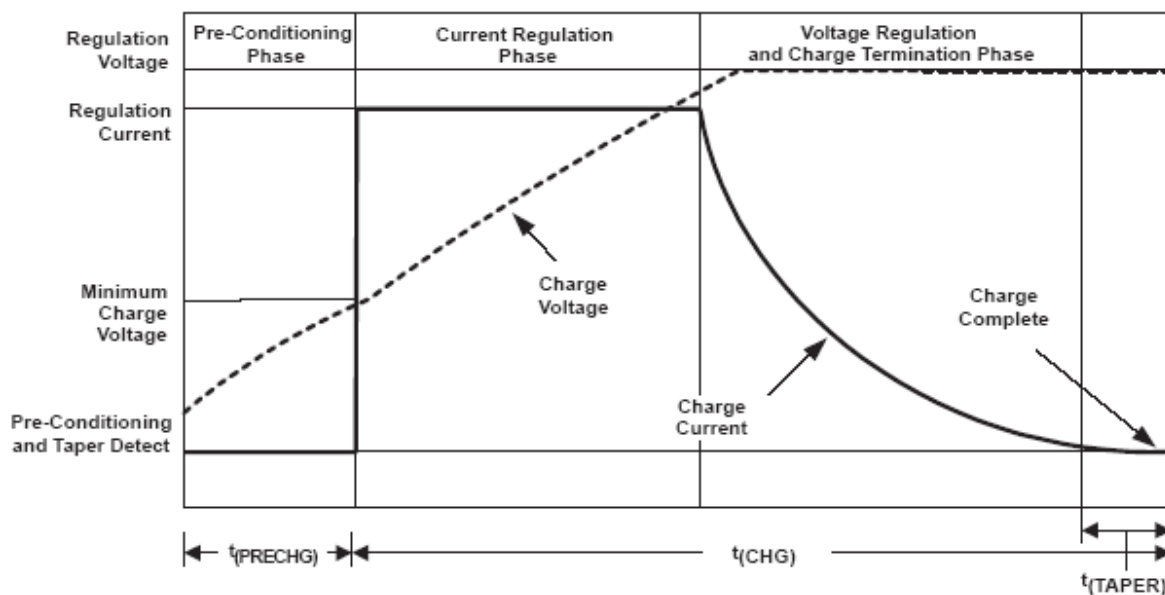
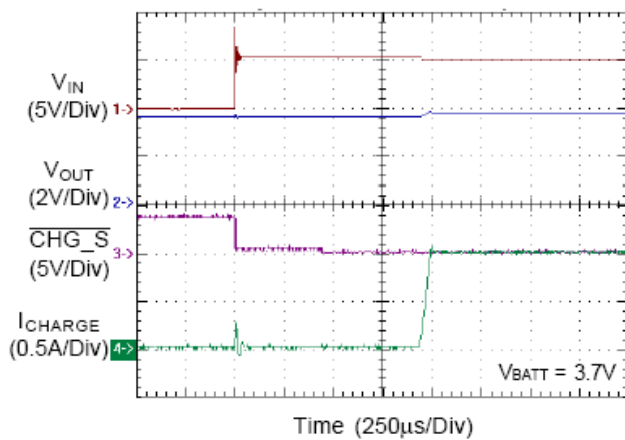


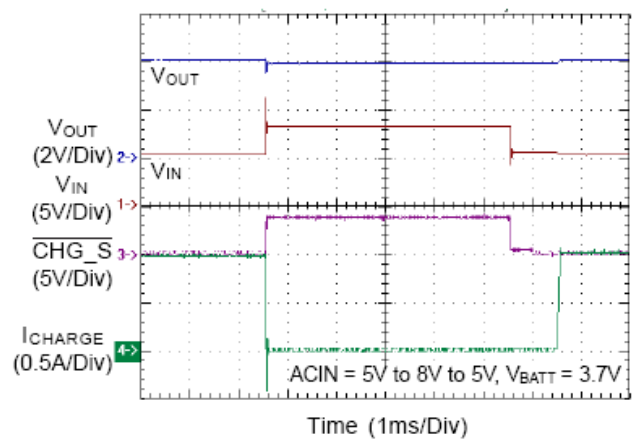
Figure 2. Typical Charging Profile

Typical Operating Characteristics

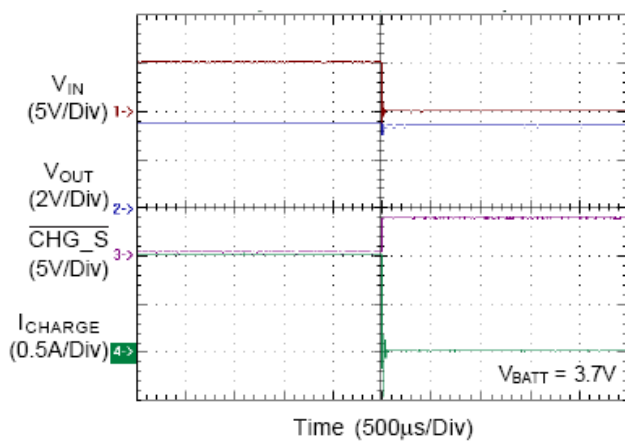
ACIN Power On



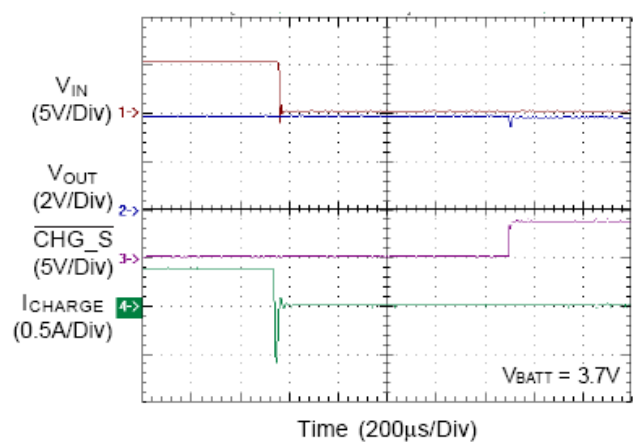
OVP



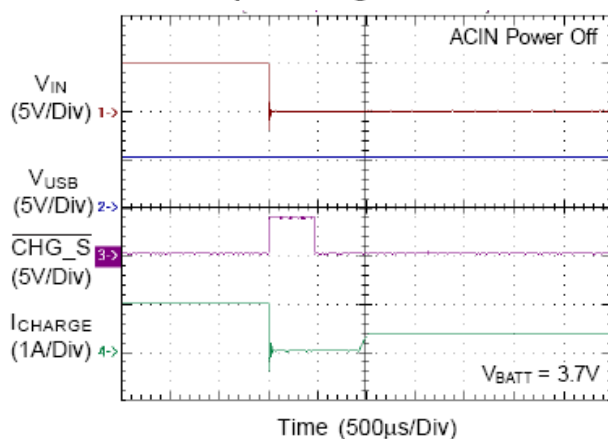
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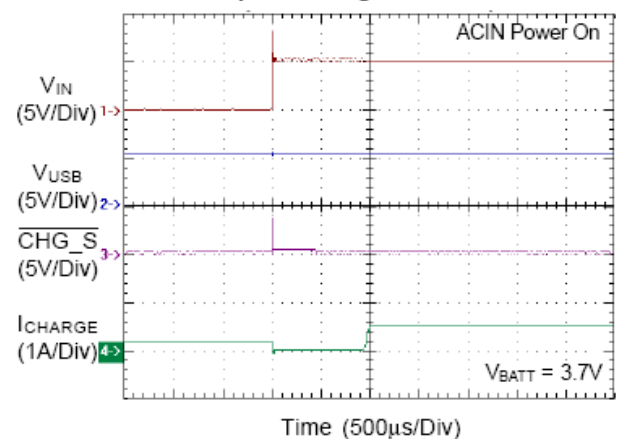
ACIN Power Off



Input Voltage Transition



Input Voltage Transition



Operation

The LP28052A is a single cell lithium-ion battery charger using a constant-current/constant-voltage algorithm. It can deliver up to 1200mA of charge current (using a good thermal PCB layout) with a final float voltage accuracy of $\pm 1\%$. The LP28052A includes an internal P-channel power MOSFET and thermal regulation circuitry. No blocking diode or external current sense resistor is required; thus, the basic charger circuit requires only two external components. Furthermore, the LP28052A is capable of operating from a USB power source.

Normal Charge Cycle

A charge cycle begins when the voltage at the V_{CC} pin rises above the UVLO threshold level and a 1% ISET ram resistor is connected from the ISET pin to ground or when a battery is connected to the charger output. If the BAT pin is less than 2.9V, the charger enters trickle charge mode. In this mode, the LP28052A supplies approximately 1/10 the ISET rammed charge current to bring the battery voltage up to a safe level for full current charging. (Note: The LP28052AX does not include this trickle charge feature).

When the BAT pin voltage rises above 2.9V, the charger enters constant-current mode, where the ISET rammed charge current is supplied to the battery. When the BAT pin approaches the final float voltage (4.2V), the LP28052A enters constant-voltage mode and the charge current begins to decrease. When the charge current drops to 1/10 of the ISET rammed value, the charge cycle ends.

ISET ramming Charge Current

The charge current is ISET rammed using a single resistor from the ISET pin to ground. The battery charge current is 1000 times the current out of the ISET pin. The ISET ram resistor and the charge current are calculated using the following equations:

$$R_{SET} = 1000V/I_{CHG}, \quad I_{CHG} = 1000V/R_{SET}$$

The charge current out of the BAT pin can be determined at any time by monitoring the ISET pin voltage using the following equation:

$$I_{BAT} = V_{SET} \times 1000/R_{SET}$$

Charge Termination

A charge cycle is terminated when the charge current falls to 1/10th the ISET rammed value after the final float voltage is reached. This condition is detected by using an internal, filtered comparator to monitor the ISET pin. When the ISET pin voltage falls below 100mV¹ for longer than t_{TERM} (typically 1ms), charging is terminated. The charge

current is latched off and the LP28052A enters standby mode, where the input supply current drops to 200 μ A. (Note: C/10 termination is disabled in trickle charging and thermal limiting modes).

When charging, transient loads on the BAT pin can cause the ISET pin to fall below 100mV for short periods of time before the DC charge current has dropped to 1/10th the ISET rammed value. The 1ms filter time (t_{TERM}) on the termination comparator ensures that transient loads of this nature do not result in premature charge cycle termination. Once the average charge current drops below 1/10th the ISET rammed value, the LP28052A terminates the charge cycle and ceases to provide any current through the BAT pin. In this state, all loads on the BAT pin must be supplied by the battery.

The LP28052A constantly monitors the BAT pin voltage in standby mode. If this voltage drops below the 4.05V recharge threshold (V_{RESTAT}), another charge cycle begins and current is once again supplied to the battery. To manually restart a charge cycle when in standby mode, the input voltage must be removed and reapplied, or the charger must be shut down and restarted using the ISET pin. Figure 1 shows the state diagram of a typical charge cycle.

Charge Status Indicator (STAT)

The charge status output has three different states: strong pull-down (~ 10 mA), weak pull-down ($\sim 20\mu$ A) and high impedance. The strong pull-down state indicates that the LP28052A is in a charge cycle. Once the charge cycle has terminated, the pin state is determined by undervoltage lockout conditions. A weak pull-down indicates that V_{CC} meets the UVLO conditions and the LP28052A is ready to charge. High impedance indicates that the LP28052A is in undervoltage lockout mode: either V_{CC} is less than 100mV above the BAT pin voltage or insufficient voltage is applied to the V_{CC} pin. A microprocessor can be used to distinguish between these three states—this method is discussed in the Applications Information section.

Thermal Limiting

An internal thermal feedback loop reduces the ISET rammed charge current if the die temperature attempts to rise above a preset value of approximately 120°C. This feature protects the LP28052A from excessive temperature and allows the user to push the limits of the power handling capability of a given circuit board without risk of damaging the LP28052A. The charge current can be set according to typical (not worst-case) ambient temperature with the

assurance that the charger will automatically reduce the current in worst-case conditions. TDFN power considerations are discussed further in the Applications Information section.

Undervoltage Lockout (UVLO)

An internal undervoltage lockout circuit monitors the input voltage and keeps the charger in shutdown mode until V_{CC} rises above the undervoltage lockout threshold. The UVLO circuit has a built-in hysteresis of 200mV. Furthermore, to protect against reverse current in the power MOSFET, the UVLO circuit keeps the charger in shutdown mode if V_{CC} falls to within 30mV of the battery voltage. If the UVLO comparator is tripped, the charger will not come out of shutdown mode until V_{CC} rises 100mV above the battery voltage.

Manual Shutdown

At any point in the charge cycle, the LP28052A can be put into shutdown mode by removing R_{ISET} thus floating the ISET pin. This reduces the battery drain current to less than 2μA and the supply current to less than 50μA. A new charge cycle can be initiated by reconnecting the ISETram resistor.

In manual shutdown, the STAT pin is in a weak pull-down state as long as V_{CC} is high enough to exceed the UVLO conditions. The STAT pin is in a high impedance state if the LP28052A is in undervoltage lockout mode: either V_{CC} is within 100mV of the BAT pin voltage or insufficient voltage is applied to the V_{CC} pin.

Automatic Recharge

Once the charge cycle is terminated, the LP28052A continuously monitors the voltage on the BAT pin using a comparator with a 2ms filter time ($t_{RECHARGE}$). A charge cycle restarts when the battery voltage falls below 4.05V (which corresponds to approximately 80% to 90% battery capacity). This ensures that the battery is kept at or near a fully charged condition and eliminates the need for periodic charge cycle initiations. STAT output enters a strong pull-down state during recharge cycles.

Power Dissipation

The conditions that cause the LP28052A battery charger to reduce charge current through thermal feedback can be approximated by considering the total power dissipated in the IC. For high charge currents, the LP28052A power dissipation is approximately:

$$P_D = (V_{ADP} - V_{BAT}) \times I_{CHG} + P_{D_BUCK} + (V_{INA} - V_{OUTA}) \times I_{OUTA}$$

Where P_D is the total power dissipated within the IC, ADP is the input supply voltage, V_{BAT} is the battery voltage, I_{BAT} is the charge current and P_{D_BUCK} is the power dissipation due to the regulator. P_{D_BUCK} can be calculated as:

$$P_{D_BUCK} = V_{OUTB} \times I_{OUTB} \left(\frac{1}{\eta} - 1 \right)$$

Where V_{OUTB} is the regulated output of the switching regulator, I_{OUTB} is the regulator load and η is the regulator efficiency at that particular load.

It is not necessary to perform worst-case power dissipation scenarios because the LP28052A will automatically reduce the charge current to maintain the die temperature at approximately 125°C. However, the approximate ambient temperature at which the thermal feedback begins to reduce the IC is:

$$T_A = 115^\circ\text{C} - P_D \theta_{JA}$$

$$T_A = 115^\circ\text{C} - (V_{ADP} - V_{BAT}) \times I_{CHG} \times \theta_{JA}$$

if the regulator is off.

Example: Consider the extreme case when an LP28052A is operating from a 6V supply providing 250mA to a 3V Li-Ion battery, the switching regulator and the LDO are off. The ambient temperature above which the LP28052A will begin to reduce the 250mA charge current is approximately: (Correctly soldered to a 2500mm² double-sided 1 oz. copper board, the LP28052A has a thermal resistance of approximately 43°C/W.)

$$T_A = 115^\circ\text{C} - (6\text{V} - 3\text{V}) \times (250\text{mA}) \times 43^\circ\text{C/W}$$

$$T_A = 115^\circ\text{C} - 0.75\text{W} \times 43^\circ\text{C/W} = 115^\circ\text{C} - 32.25^\circ\text{C}$$

$$T_A = 82.75^\circ\text{C}$$

If there is more power dissipation due to the switching regulator or the LDO, the thermal regulation will kick in at a somewhat lower temperature than this. In the above circumstances, the LP28052A can be used above 82.75°C, but the charge current will be reduced from 250mA. The approximate current at a given ambient temperature can be calculated:

$$I_{CHG} = \frac{115^\circ\text{C} - T_A}{(V_{ADP} - V_{BAT}) \times \theta_{JA}}$$

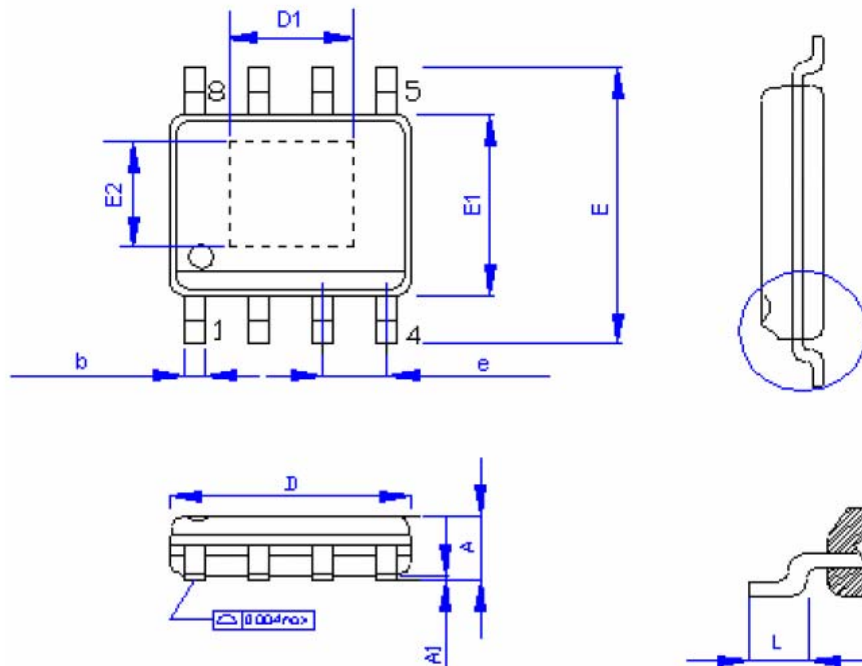
$$I_{CHG} = \frac{115^\circ\text{C} - 85^\circ\text{C}}{(6\text{V} - 3\text{V}) \times 43^\circ\text{C/W}} = \frac{30^\circ\text{C}}{129^\circ\text{C/A}} = 232.6\text{mA}$$

Note: 1V = 1J/C = 1W/A

Furthermore, the voltage at the ISET pin will change proportionally with the charge current as discussed in the ISET ramming Charge Current section.

Packaging Information

SOP-8 (FD)



SYMBOLS	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	1.35	1.75	0.053	0.069
A1	0.05	0.25	0.002	0.010
D	4.90		0.193	
E1	3.90		0.153	
E	5.80	6.20	0.228	0.244
L	0.40	1.27	0.016	0.050
b	0.33	0.51	0.013	0.020
e	1.27		0.500	
D1	2.06		0.081	
E2	2.06		0.081	