

ADSL Line LPAnformer

Link-PP Int'l Technology

- ✧ For use with TI's TNET D 4000 C/P chipset
- ✧ Excellent THD performance
- ✧ Small footprint
- ✧ Designed to meet UL 1950
- ✧ For RoHS part add suffix NL
- ✧ RoHS NL peak solder rating 235°C



Electrical Specifications @ 25°C --- Operating Temperature -40°C to 85°C

Part Number	Application	Mounting	Turns Ratio (±2%)(10-7):(1-4)	OCL @10KHz,0.1 (1-4) with (2-3) shorted	Leakage Inductance @10KHz, 0.1V (1-4) with (10-7),shorted
LPA2132	CO	SMT	1:1.95(Chip to Line)	1.5 mH (± 10%)	12.0 μH MAX

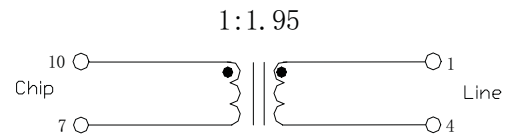
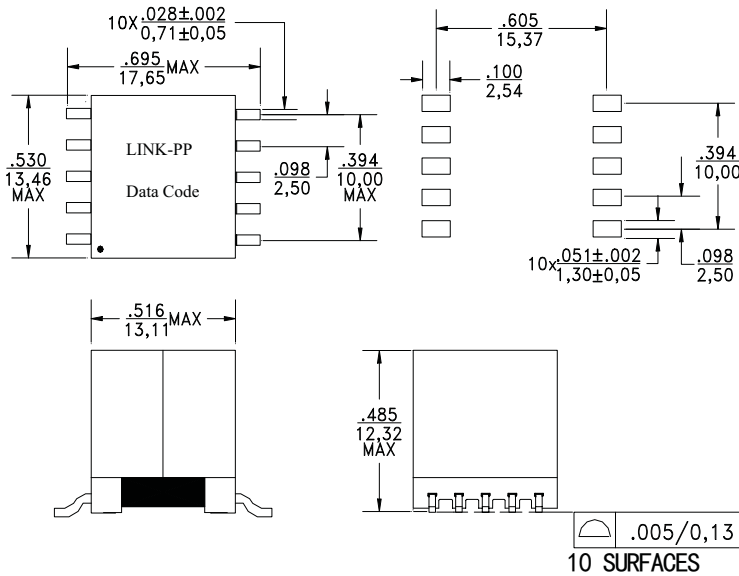
Additional Specifications

Part Number	DC Resistance (1-4)(w/2-3 shorted)	Isolation Voltage (Vrms)	Interwinding capacitance @ 10KHz,0.1 V (1-10)
LPA2132	2.0 Ω MAX	1500	35 pF MAX

Mechanical

Schematic

LPA2132



Dimensions: $\frac{\text{Inches}}{\text{mm}}$
 Unless otherwise specified,
 all tolerances are $\pm \frac{0.10}{0.25}$