

ADSL Line LPAnformer

Link-PP Int'l Technology

※For Use with Texas Instruments TNETD4500C CO chipsets

※For RoHS part add suffix NL

※RoHS NL peak solder rating 235°C



Electrical Specifications @25°C — Operating Temperature -40°C to +85°C

Part Number	Turns Ratio (Line:Chip)	OCL Primary ($\mu\text{H} \pm 6\%$)	Leakage Inductance ($\mu\text{H} \text{ MAX}$)	DCR Primary (1-3 & 2-4) ($\Omega \text{ MAX}$)	DCR Secondary (10-8 & 9-7) ($\Omega \text{ MAX}$)	Isolation Voltage (Vrms)	THD@100Khz 5.0 Vp-p (dB MIN)
LPA2243 ¹	2:1	400	5.0	0.5	0.3	1500	-80
LPA2133 ²	2:1	75	5.0	0.5	0.3	1500	-80

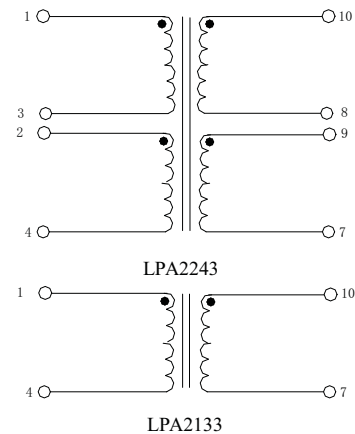
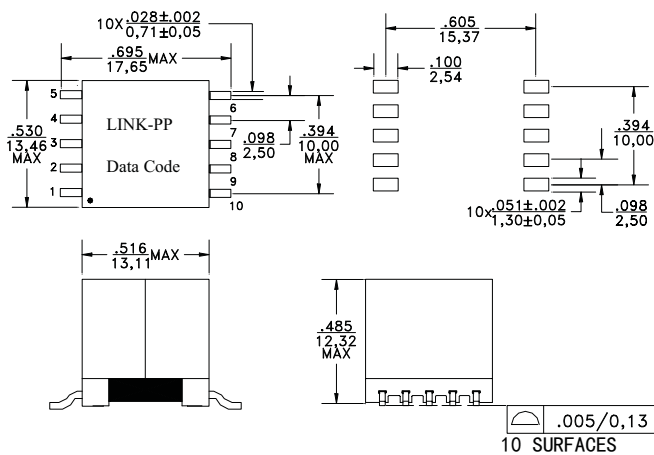
¹LPA2243 is designed for ADSL over POTS applications where THS6012/72 Line Drivers and Active termination is used.

²LPA2133 is designed for ADSL over ISDN applications where THS6012/72 Line Drivers and Active termination is used.

Mechanical

Schematic

LPA2243/LPA2133



Dimensions: $\frac{\text{Inches}}{\text{mm}}$
 Unless otherwise specified, all tolerances are $\pm \frac{.010}{0.25}$