

AVC Liquid Crystal Displays Group

# LQ104V1DC31 TFT-LCD Module

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	SPECIFICATION	GROUP
1		REVISION: DEC. 13. 2005
	DEVICE SPECIFICATION FOR	
	FFT-LCD Modul	e
	MODEL No.	
	LQ104V1DC31	
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#### 1. Application

This specification applies to color TFT-LCD module, LQ104V1DC31

(This specification is only applied for the module which has letter "A" at the end of the lot number of the module.)

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The device listed in these specification sheets was designed and manufactured for use in general electronic equipment.

In case of using the device for applications such as control and safety equipment for transportation(aircraft, trains, automobiles, etc. ), rescue and security equipment and various safety related equipment which require higher reliability and safety, take into consideration that appropriate measures such as fail-safe functions and redundant system design should be taken .

Do not use the device for equipment that requires an extreme level of reliability, such as aerospace applications, telecommunication equipment(trunk lines), nuclear power control equipment and medical or other equipment for life support.

SHARP assumes no responsibility for any damage resulting from the use of the device which does not comply with the instructions and the precautions specified in these specification sheets .

Contact and consult with a SHARP sales representative for any questions about this device .

#### 2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit and power supply circuit. Graphics and texts can be displayed on a  $640 \times 3 \times 480$  dots panel with 262,144 colors by supplying 18 bit data signal(6bit/color), four timing signals, +3.3V/+5V DC supply voltage for TFT-LCD panel driving .

The TFT-LCD panel used for this module is a low-reflection and higher-color-saturation type. Therefore, this module is also suitable for the multimedia use.

Optimum viewing direction is 6 o'clock.

#### 3. Mechanical Specifications

Parameter	Specifications	Unit
Display size	26 (10.4") Diagonal	cm
Active area	211.2(H)×158.4(V)	mm
Pixel format	640(H)×480(V)	pixel
	(1  pixel=R+G+B  dots)	
Pixel pitch	0.330(H)×0.330(V)	mm
Pixel configuration	R,G,B vertical stripe	
Display mode	Normally white	
Unit outline dimensions *1	265.0(W)×195.0(H)×11.2max(D)	mm
Mass	310(max)	g
Surface treatment	Anti-glare and hard-coating 3H	

\*1.Note: excluding backlight cables.

Outline dimensions is shown in Fig.1

#### 4. Input Terminals

#### 4-1. TFT-LCD panel driving

-1. 11 1-LC	JD panel a	CN1 Used connector:DF9MA-31P-1V	V (Hirose Electric Co., Ltd.)							
1		31 Corresponding connector								
2			DF9A-31S-1V( ")							
CN1	CN1 pin arrangement from module surface DF9B-31S-1V(									
	(Transparent view) DF9M-31S-1V(									
Pin No.	Symbol	Function	DF9M-31S-1V ( " ) Remark							
1	GND									
2	CK	Clock signal for sampling each data signal								
3	Hsync	Horizontal synchronous signal	[Note1]							
4	Vsync	Vertical synchronous signal	[Note1]							
5	GND									
6	R0	R E D data signal(LSB)								
7	R1	R E D data signal								
8	R2	R E D data signal								
9	R3	R E D data signal								
10	R4	R E D data signal								
11	R5	R E D data signal(MSB)								
12	GND									
13	G0	G R E E N data signal(LSB)								
14	G1	G R E E N data signal								
15	G2	GREEN data signal								
16	G3	GREEN data signal								
17	G4	GREEN data signal								
18	G5	GREEN data signal(MSB)								
19	GND									
20	B0	B L U E data signal(LSB)								
21	B1	BLUE data signal								
22	B2	BLUE data signal								
23	B3	BLUE data signal								
24	B4	BLUE data signal								
25	B5	BLUE data signal(MSB)								
26	GND									
27	ENAB	Signal to settle the horizontal display position	[Note2]							
28	Vcc	+3.3/5.0V power supply								
29	Vcc	+3.3/5.0V power supply								
30	R/L	Horizontal display mode select signal	[Note3]							
31	U/D	Vertical display mode select signal	[Note4]							

<u>\* The shielding case is not connected with GND.</u>

[Note1] 480 line, 400 line or 350 line mode

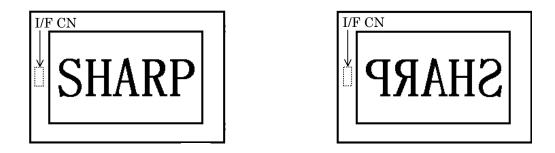
is selected by the polarity combination

of the both synchronous signals.

Mode	480 lines	400 lines	350 lines
Hsync	Negative	Negative	Positive
Vsync	Negative	Positive	Negative

[Note2] The horizontal display start timing is settled in

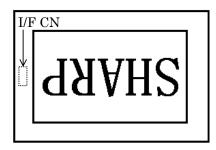
accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in 7-2. Don't keep ENAB" High" during operation.



R/L = H i g h, U/D = L o w R/L = L o w, U/D = L o w



R/L=H i g h, U/D=H i g hR/L=L o w, U/D=H i g h



Parameter	Symbol	Condition	Ratings	Unit	Remark
Input voltage	VI	Ta=25°C	$-0.3 \sim \mathrm{Vcc}{+}0.3$	V	[Note1]
+5V supply voltage	Vcc	Ta=25°C	$0 \sim + 6$	V	
Storage temperature	Tstg	_	$-30 \sim +70$	°C	[Note2]
Operating temperature ①	Торр	_	$-10 \sim +70$	°C	[Note3]
Operating temperature ②	Тор	_	$-10 \sim +65$	°C	[Note4]
Light source wave length	λΙ	_	≧400	nm	[Note5]
Light source luminance	_	_	≦14000	${\rm cd}/{\rm m}^2$	[Note5]

5. Absolute Maximum Ratings

[Note1] CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync,ENAB, R/L, U/L

[Note2] Humidity : 95%RH Max. at Ta $\leq$ 40°C.

Maximum wet-bulb temperature at  $39^{\circ}$ C or less at Ta> $40^{\circ}$ C. No condensation.

- [Note3] Panel surface temperature
- [Note4] Module ambient temperature
- [Note5] Measurement point : panel surface (Backlight mounting side)

The light source used fluorescence lamp with three wave length.

#### 6. Electrical Characteristics

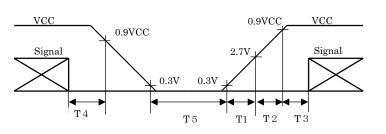
#### 6-1.TFT-LCDpaneldriving

#### $Ta=25^{\circ}C$

	Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Power	Supply voltage	Vcc	+3.0	+3.3 $+5.0$	+5.5	V	[Note1]
Supply	Current dissipation	Icc	—	180	270	mА	Vcc=3.3V [Note2]
		Icc	_	150	230	mA	Vcc=5.0V [Note2]
Permi	issive input ripple voltage	V <sub>RF</sub>	_	—	100	mVp-p	
Input	voltage (Low)	V <sub>IL</sub>	_	—	0.3Vcc	V	
Input	voltage (High)	V <sub>IH</sub>	0.7Vcc	—	—	V	[Note3]
Inp	out current (low)	I <sub>OL1</sub>	_	—	1.0	μΑ	V <sub>I</sub> =0V [Note4]
		I <sub>OL2</sub>			10	μΑ	V <sub>I</sub> =0V [Note5]
		I <sub>OL3</sub>	-	_	800	μΑ	V <sub>I</sub> =0V [Note6]
Input current (High)		I <sub>OH1</sub>	_	_	1.0	μΑ	V <sub>I</sub> =Vcc [Note7]
-		I <sub>OH2</sub>			300	μΑ	V <sub>I</sub> =Vcc [Note8]
		I <sub>OH3</sub>	_	_	800	μΑ	V <sub>I</sub> =Vcc [Note9]

[ NOTE 1] Vcc-turn-on conditions

 $\begin{array}{l} 0 < T \ 1 \leqq 1 \ 5 \ m \ s \\ 0 < T \ 2 \leqq 1 \ 0 \ m \ s \\ 0 < T \ 3 \leqq 1 \ 0 \ 0 \ m \ s \\ 0 < T \ 4 \leqq 1 \ s \\ T \ 5 > 2 \ 0 \ 0 \ m \ s \end{array}$ 



#### Vcc-dip conditions

1) 2. 
$$5 V \leq V c c$$

t d 
$$\leq 1 \ 0 \ m \ s$$

2) 
$$V c c < 2.5$$

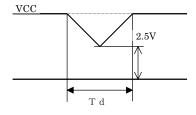
- Vcc-dip condition should also follow The Vcc-turn-on conditions
- [Note2] Typical current situation : 16-gray-bar pattern. 480 line mode/Vcc=+3.3V/+5.0V

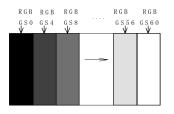
V

- [Note3] CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync,ENAB, R/L,U/D
- [Note4] CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync,

[Note5] U/D,ENAB

- [Note6] R/L
- [Note7] CK,R0~R5,G0~G5,B0~B5,Hsnc,Vsync,R/L
- [Note8] ENAB
- [Note9] U/D





#### 7. Timing Characteristics of input signals

Timing diagrams of input signal are shown in Fig.2 -  $1 \sim 3$  .

Parar	neter	Symbol	Mode	Min.	Тур.	Max.	Unit	Remark
Clock	Frequency	1/Tc	all		25.18	28.33	MHz	
	High time	Tch	]]	5	_	—	ns	
	Low time	Tcl	]]	10	_	—	ns	
Data	Setup time	Tds	]]	5	_	—	ns	
	Hold time	Tdh	]]	10	_	—	ns	
Horizontal	Cycle	TH	]]	30.00	31.78	—	$\mu$ s	
sync. signal			]]	750	800	900	clock	
	Pulse width	ТНр	]]	2	96	200	clock	
Vertical	Cycle	TV	480	515	525	560	line	
sync. signal			400	446	449	480	line	
			350	447	449	510	line	
	Pulsewidth	TVp	all	1	_	34	line	
Horizontal dis	splay period	THd	11	640	640	640	clock	
Hsync-Clock		THc	,,	10	_	Tc-10	ns	
phase differer	phase difference							
Hsync-Vsync	Hsync-Vsync			0	—	TH-THp	clock	
phase differer	nce							

7-1. Timing characteristics

Note) In case of lower frequency, the deterioration of display quality, flicker etc.,may be occurred.

#### 7-2. Horizontal display position

The horizontal display position is determined by ENAB signal and the input data corresponding to the rising edge of ENAB signal is displayed at the left end of the active area.

Parar	symbol	Min.	Тур.	Max.	Unit	Remark	
Enable signal	Setup time	Tes	5	_	Tc-10	ns	
	Pulse width	Тер	2	640	640	clock	
Hsync-Enable	Hsync-Enable signal			—	TH-664	clock	
phase differen	ice						

Note) When ENAB is fixed "Low", the display starts from the data of C104(clock) as shown in Fig.2-①~③. Be careful that the module does <u>not</u> work when ENAB is fixed "High". When the phase difference is below 104 clock, keep the "High level of ENAB is signal longer"

Than 104-The. If it will not be keeped, the display starts from the data of C104(clock).

7-3. Vertical display position

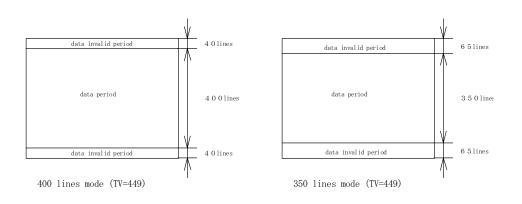
The vertical display position is automatically centered in the active area at each mode of VGA ,480-,400-,and 350-line mode . Each mode is selected depending on the polarity of the synchronous signals described in 4-1(Note1).

In each mode ,the data of TVn is displayed at the top line of the active area. And the display position will be centered on the screen like the following figure when the period of vertical synchronous signal,TV, is typical value.

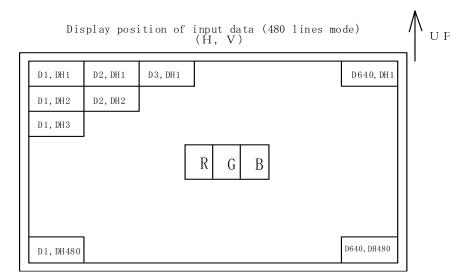
In 400-, and 350-line mode, the data in the vertical data invalid period is also displayed, So , inputting all data "0" is recommended during vertical data invalid period.

Mode	V-data start(TVs)			V-display period	Unit	Remark
		period(TVd)				
480	34	480	34	480	line	
400	34	400	443-TV	480	line	
350	61	350	445-TV	480	line	

ENAB signal has no relation to the vertical display position.



#### 7-4. Input Data Signals and Display Position on the screen



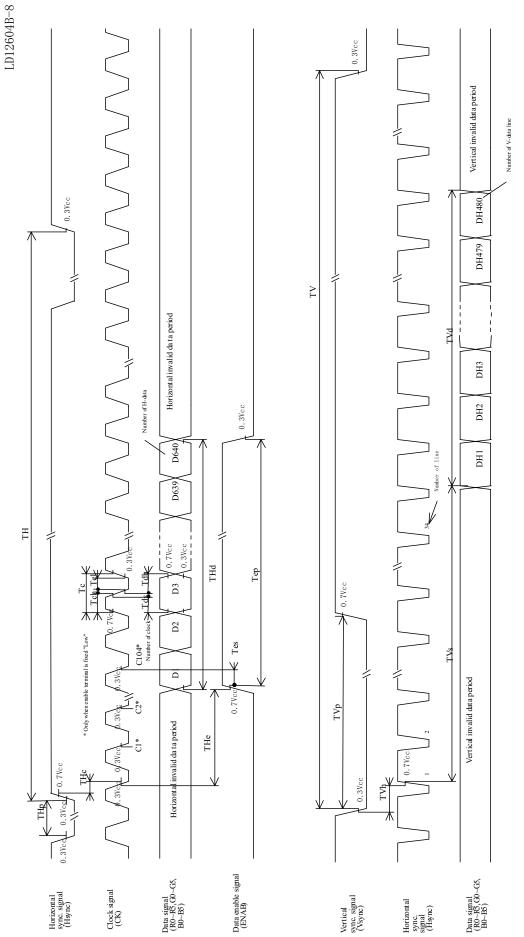


Fig 2-1 Input signal waveforms (480 line mode)



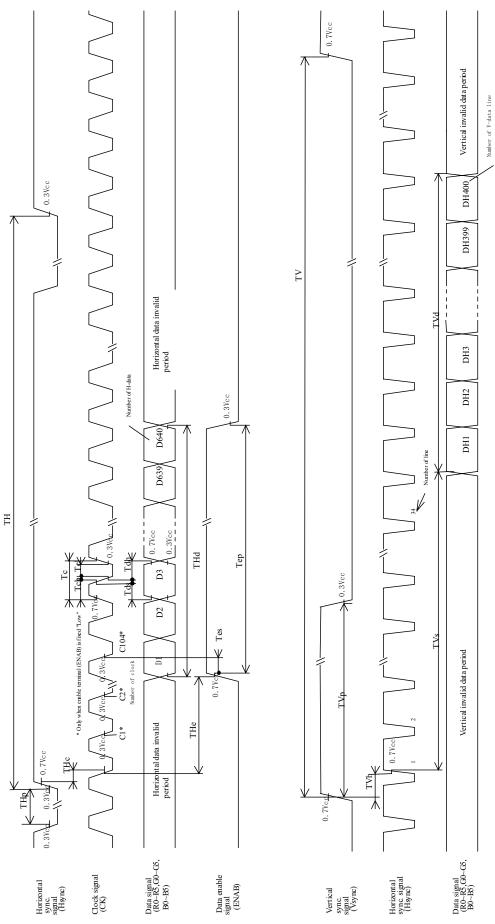
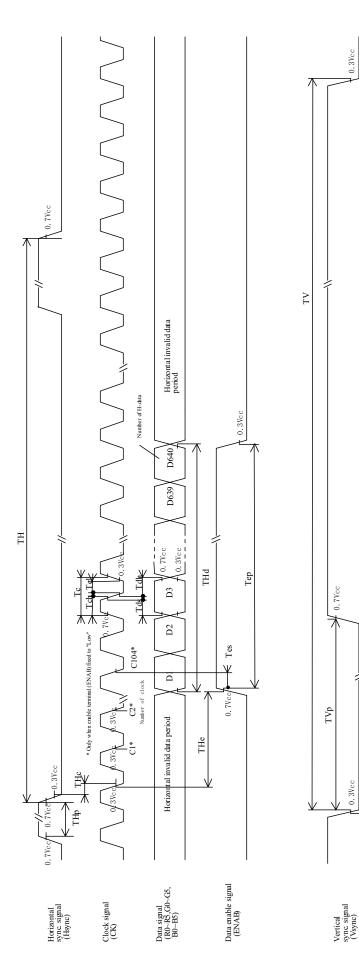


Fig.2-2 Input signal waveforms (400 line mode)

LD12604B-10





-1

TVs

-0.3Vcc

Horizontal sync. signal (Hsync)

TVh

木

Vertical invalid data period

Data signal (R0~R5,G0~G5, B0~B5)

Vertical invalid data period

DH350

DH349

DH3

DH2

DHI

Number of V-data line

	Colors &		spidy	0010	is une	i Oruj	y bea	011			a sign	al								
	Gray scale	Gray	R0	R1	R2	R3	R4	R5	G0	G1	G2		G4	G5	В0	B1	B2	В3	B4	В5
	Gruf Seure	Scale	100	m	112	10	IC I	no	00	01	02	0.5	01	0.2	20	DI	02	23	DI	50
	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	_	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
B	Green	_	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Cyan	_	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
Color	Red	_	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
or	Magenta	_	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	_	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	_	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray	仓	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ay S	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	Ŷ	$\leftarrow$				L						L					``	r		
9 of	Û	$\leftarrow$				L						L					``	r		
Red	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Û	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray	仓	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
y Sc	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
Scale	仓	$\checkmark$				1						1						V		
of	Û	$\checkmark$				1						1						V		
Gree	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
en	Û	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray	仓	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
7 Sc	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
Scale	仓	$\checkmark$		$\checkmark$					$\boldsymbol{k}$					``	r					
f	Û	$\checkmark$				arepsilon						arepsilon					``	V		
Blue	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
	Û	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

8. Input Signals, Basic Display Colors and Gray Scale of Each Color

0 :Low level voltage, 1 : High level voltage

Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

#### 9. Optical Characteristics

Parar	neter	Symbol	Condition	Min	Тур	Max	Unit	Remark
Viewing	Horizontal	θ 21, θ 22	CR > 10	60	70		Deg.	[Note1,4]
Angle	Vertical	θ 11		35	40		Deg.	
Range		θ 12		55	70	—	Deg.	
Contrast rat	tio	C R	$\theta = 0^{\circ}$	150				[Note2,4]
			Optimum	—	300	—	—	
			Viewing Angle					
Response	Rise	τr	$\theta = 0^{\circ}$	_	20		ms	[Note3,4]
Time	Decay	τd		_	40		ms	
Chromat	icity of	x		—	0. 305	—		[Note4]
Wł	nite	У		—	0. 329	_		
Transm	issivity	tr		6.1	7.7	—	%	[Note5]

(It is usually required to measure under the following condition.condition:IL=6.0mA,Ta= $25^{\circ}C \pm 2^{\circ}C$ ,FL=60kHz.)

[Use the backlight of LQ10D421 for measurement]

\*The measurement shall be executed 30 minutes after lighting at rating.

The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig.3 below.

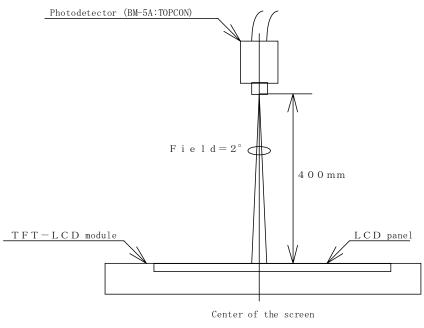
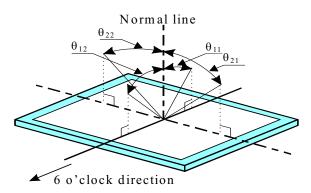


Fig. 3 Optical characteristics measurement method

[Note1] Definitions of viewing angle range:



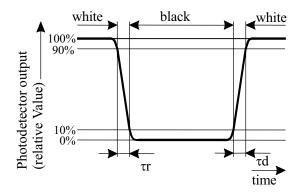
[Note2] Definition of contrast ratio:

The contrast ratio is defined as the following.

	Luminance (brightness) with all pixels white
Contrast Ratio (CR) $=$	Luminance (brightness) with all pixels black

[Note3] Definition of response time:

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



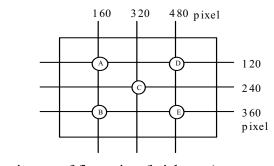
[Note4] This shall be measured at center of the screen.

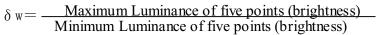
[Note5] Definition of white uniformity:

White uniformity is defined as the

following with five measurements

 $(A \sim E)$ .





10. Display Quality

The display quality of the color TFT-LCD module shall be in compliance with the Incoming Inspection Standard.

- 11. Handling Precautions
  - a) Be sure to turn off the power supply when inserting or disconnecting the cable.
  - b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
  - c) Since the polarizer is easily damaged, pay attention not to scratch it.
  - d) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
  - e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
  - f) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
  - g) Since CMOS LSI is used in this module, take care of static electricity and injure the human earth when handling.Observe all other precautionary requirement in handling electric components.h) Protection film is attached to the module surface to prevent it from being scratched .
  - Peel the film off slowly, just before the use, with strict attention to electrostatic charges. Ionized air shall be blown over during the action. Blow off 'dust' on the polarizer by using an ionized nitrogen.
  - i) The polarizer surface on the panel is treated with Anti-Glare for low reflection. In case of attaching protective board over the LCD, be careful about the optical interface fringe etc. which degrades display quality.
  - j)Do not expose the LCD panel to direct sunlight. Lightproof shade etc. should be attached when LCD panel is used under such environment.
  - k)When you use the module , please apply enough EMI countermeasure by using optimum backlight sysutem etc..
  - When handling LCD modules and assembling them into cabinets, please be noted that long-term storage in the environment of oxidization or deoxidization gas and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the LCD modules.
  - m) When handling LCD modules and assembling them into cabinets, please be noted that long-term storage in the environment of oxidization or deoxidization gas and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the LCD modules.
  - n) Do not expose the LCD panel to direct sunlight. Lightproof shade etc. should be attached when LCD panel is used under such environment.
  - o) When install LCD modules in the cabinet, recommended torque value is "0.294±0.02N·m (3.0±0.2kgf·cm)".

Be sure to confirm it in the same condition as it is installed in your instrument.

- p) Liquid crystal contained in the panel may leak if the LCD is broken. Rinse it as soon as possible if it gets inside your eye or mouth by mistake.
- q) Notice:Never dismantle the module , because it will cause failure.
- r) Be careful when using it for long time with fixed pattern display as it may cause afterimage.
- s) Adjusting volume have been set optimally before shipment, so do not change any adjusted value.
- If adjusted value is changed, the specification may not be satisfied.
- t) If a minute particle enters in the module and adheres to an optical material, it may cause display nonuniformity issue, etc. Therefore, fine-pitch filters have to be installed to cooling and inhalation hole if you intend to install a fan.

Piling number of cartons	7 (Max)
Packing quantity in one carton	20
Carton size [mm]	525 (W)×309(D)×377(H)
Total mass of one carton filled with full modules	10kg
Packing form is shown	Fig.4

#### 12.Packing form

#### 13. Reliability test items

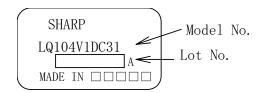
No.	Test item	Conditions	
1	High temperature storage test	Ta=70°C 240h	
2	Low temperature storage test	$Ta = -30^{\circ}C$ 240h	
3	High temperature	Ta=40°C ; 95%RH 240h	
	& high humidity operation test	(No condensation)	
4	High temperature operation test	Topp=70°C 240h(Panel surface temperature)	
5	Low temperature operation test	Ta= -10°C 240h	
6	Vibration test	Frequency: $10 \sim 57$ Hz/Vibration width (one side):0.075mm	
	(non- operating)	: 58 $\sim$ 500Hz/Gravity:9.8m/s <sup>2</sup>	
		Sweep time : 11 minutes	
		Test period : 3 hours	
		(1 hour for each direction of X,Y,Z)	
7	Shock test	Max. gravity : 490m/s <sup>2</sup>	
	(non- operating)	Pulse width : 11ms, half sine wave	
		Direction : $\pm X, \pm Y, \pm Z$	
		once for each direction.	

[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

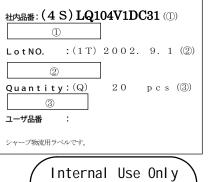
#### 14. Others

1) Label: Module



Packing box

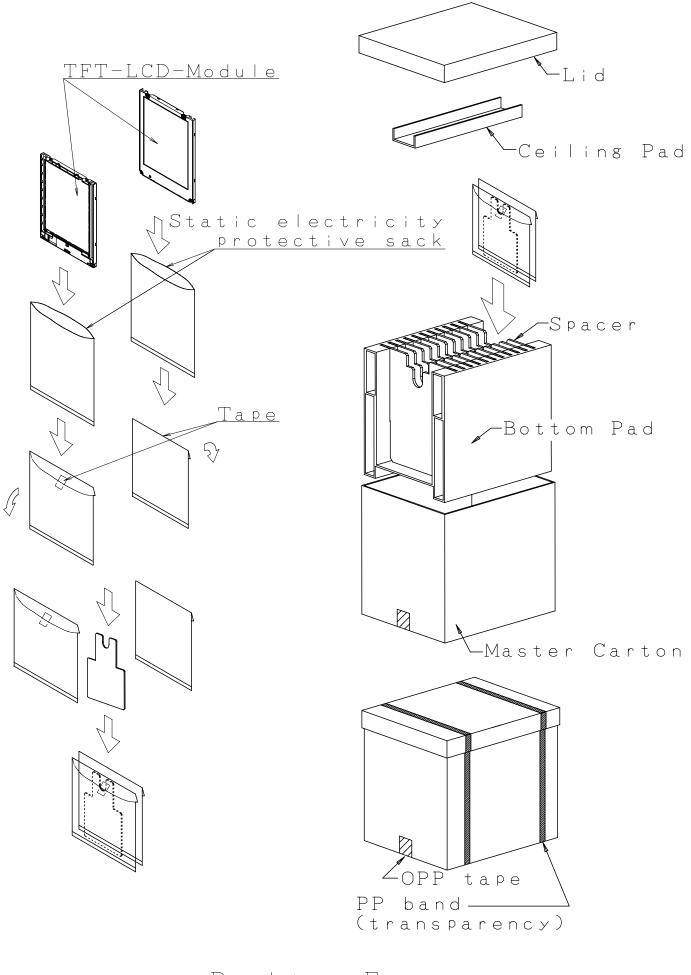
①Model.No ②Shipment Date ③Quntity



%R.C. (RoHS Compliance) means these parts have corresponded with the RoHS directive.

R. C.

- 2) Adjusting volume have been set optimally before shipment, so do not change any adjusted value. If adjusted value is changed, the specification may not be satisfied.
- 3) Disassembling the module can cause permanent damage and should be strictly avoided.
- 4) Please be careful since image retention may occur when a fixed pattern is displayed for a long time
- 5) If any problem occurs in relation to the description of this specification, it shall be resolved through discussion with spirit of cooperation.



Packing Form

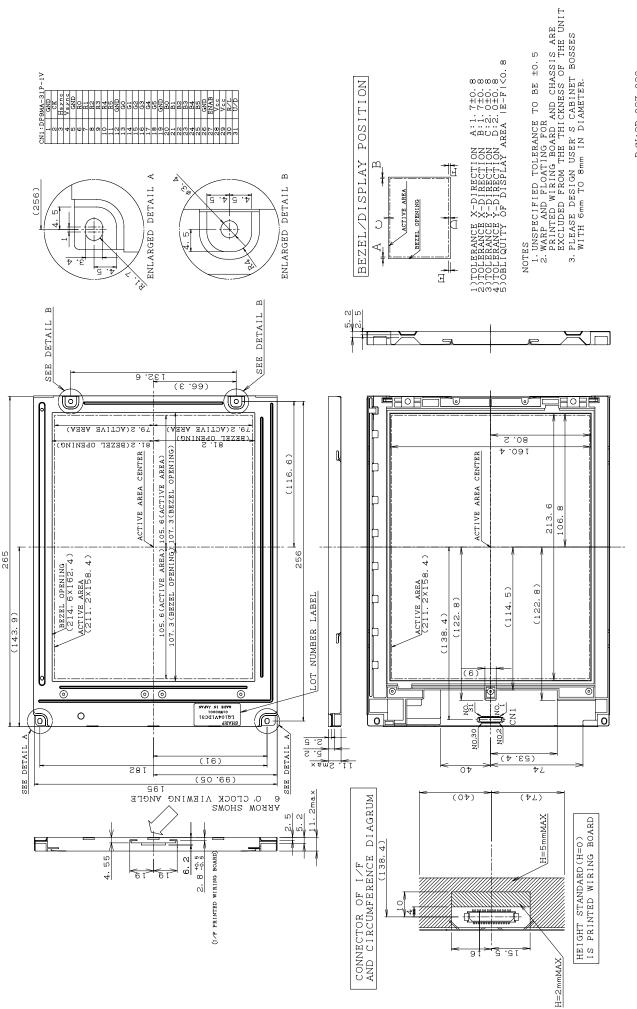


FIG1. LQ104V1DC31 OUTLINE DEMENSIONS

D/N:2D-027-020

## SHARP

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