

PREPARED BY : DATE	<div style="border: 1px dashed black; padding: 5px; text-align: center;"> TFT LIQUID CRYSTAL DISPLAY GROUP SPECIFICATION </div>	SPEC No. LD-11801
APPROVED BY : DATE		FILE No.
		ISSUE : Aug. 19, 1999
	PAGE : 16 pages	
	APPLICABLE GROUP	
	TFT Liquid Crystal Display	
	Group	

DEVICE SPECIFICATION FOR

TFT-LCD Module

MODEL No.

LQ133X1LS20

CUSTOMER'S APPROVAL

DATE _____

BY _____

PRESENTED

BY *Mahoto Takeda*

M. TAKEDA

Department General Manager

DEVELOPMENT ENGINEERING DEPT II

TFT DIVISION II

TFT LIQUID CRYSTAL DISPLAY GROUP

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1. Application

This specification applies to a color TFT-LCD module, LQ133X1LS20.

2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit and power supply circuit and a backlight unit. Graphics and texts can be displayed on a 1024×3×768 dots panel with 262,144 colors by using LVDS (Low Voltage Differential Signaling) to interface and supplying +3.3V DC supply voltage for TFT-LCD panel driving and supply voltage for backlight.

The TFT-LCD panel used for this module has very high aperture ratio. A low-reflection and higher-color-saturation type color filter is also used for this panel. Therefore, high-brightness and high-contrast image, which is suitable for the multimedia use, can be obtained by using this module.

Optimum viewing direction is 6 o'clock.

Backlight-driving DC/AC inverter is not built in this module.

This module has no bezel which mechanically fixes panel and backlight unit.

[Features]

- 1) High aperture panel ; high-brightness or low power consumption. -
- 2) Brilliant and high contrast image.
- 3) Small footprint and thin shape.
- 4) Light weight.

3. Mechanical Specifications

Parameter	Specifications	Unit
Display size	34 (13.3") Diagonal	cm
Active area	270.3 (H)×202.8 (V)	mm
Pixel format	1024 (H)×768 (V)	pixel
	(1 pixel=R+G+B dots)	
Pixel pitch	0.264 (H)×0.264 (V)	mm
Pixel configuration	R,G,B vertical stripe	
Display mode	Normally white	
Unit outline dimensions *1	293.7 (W)×219.3 (H)×5.5Max.(D)	mm
Mass	MAX. 470	g
	TYP. 450	g
Surface treatment	Anti-glare and hard-coating 2H Low reflection (~1.7%)	

*1.Note: excluding backlight cables

(Thickness is partly 5.6Max.)

Outline dimensions is shown in Fig.1

4. Input Terminals

4-1. TFT-LCD panel driving

CN1 (LVDS signals and +3.3V DC power supply)

Using connector : FI-SE20P-HF (JAE)

Corresponding connector : FI-SE20M or FI-S20S (JAE)

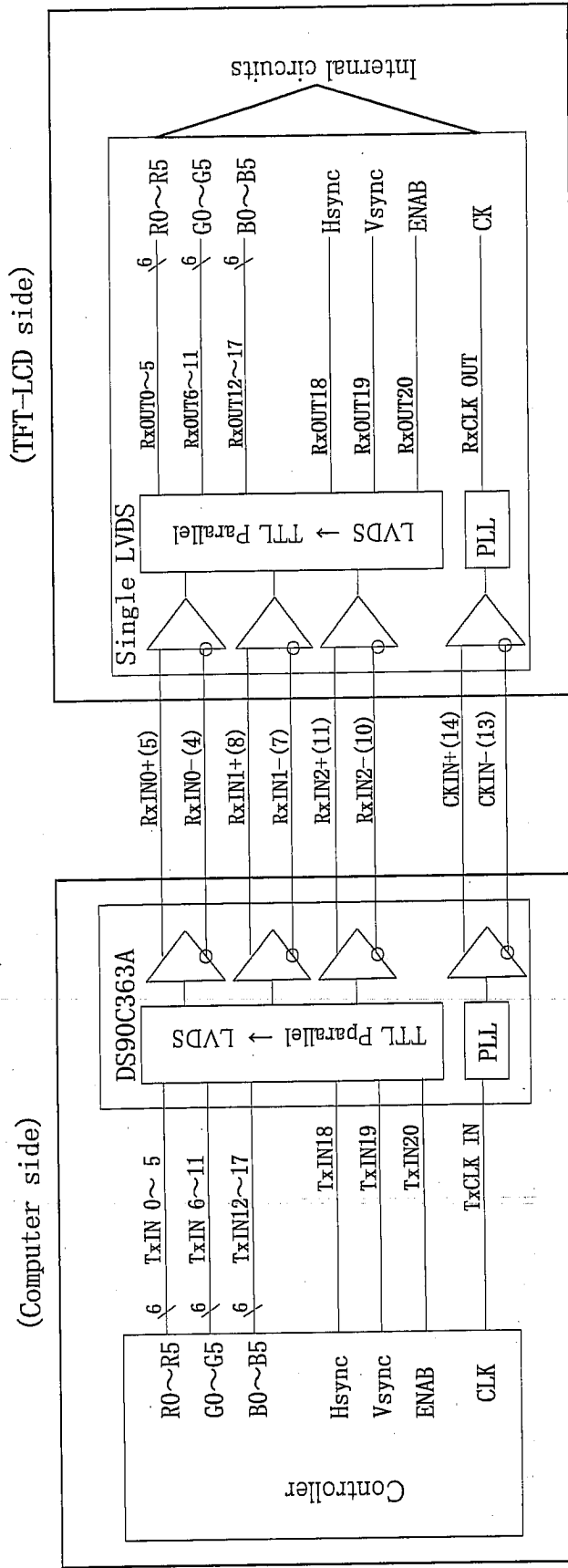
Pin No.	Symbol	Function	Remark
1	V c c	+3.3V power supply	
2	V c c	+3.3V power supply	
3	GND		
4	GND		
5	RxIN0-	Receiver signal (-)	LVDS
6	RxIN0+	Receiver signal (+)	LVDS
7	GND		
8	RxIN1-	Receiver signal (-)	LVDS
9	RxIN1+	Receiver signal (+)	LVDS
10	GND		
11	RxIN2-	Receiver signal (-)	LVDS
12	RxIN2+	Receiver signal (+)	LVDS
13	GND		
14	CK IN-	Clock signal (-)	LVDS
15	CK IN+	Clock signal (+)	LVDS
16	GND		
17	RESERVED	This should be electrically opened during operation.	
18	RESERVED	This should be electrically opened during operation.	
19	GND		
20	GND		

[Note 1] Relation between LVDS signals and actual data shows below section (4-2).

[Note 2] The shielding case is connected with signal GND.

4-2 Interface block diagram

Using receiver : Single LVDS interface, which equals DS90CF364A(National semiconductor) , contained in a control IC
 Corresponding Transmitter : DS90C363,DS90C383,DS90C363A,DS90C383A(National semiconductor)



4-3. Backlight driving

CN2 : BHSR-02VS-1(JST)

Mating connector : SM02B-BHSS-1(JST)

Pin no.	symbol	Function
1	V _{HIGH}	Power supply for lamp (High voltage side)
2	V _{LOW}	Power supply for lamp (Low voltage side)

5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Input voltage	V _I	Ta=25°C	-0.3 ~ V _{CC} +0.3	V	【Note1】
+3.3V supply voltage	V _{CC}	Ta=25°C	0 ~ +4	V	
Storage temperature	T _{stg}	—	-25 ~ +60	°C	【Note2】
Operating temperature (Ambient)	T _{opa}	—	0 ~ +50	°C	

【Note1】 LVDS signals

【Note2】 Humidity : 95%RH Max. at Ta ≤ 40°C.

Maximum wet-bulb temperature at 39°C or less at Ta > 40°C.

No condensation.

6. Electrical Characteristics

6-1. TFT-LCD panel driving

Ta=25°C

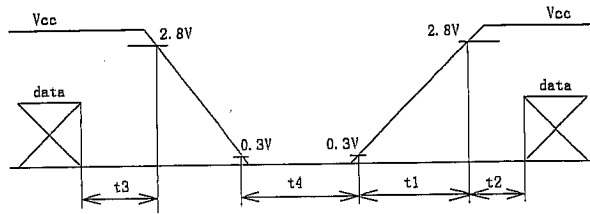
Parameter		Symbol	Min.	Typ.	Max.	Unit	Remark
V _{CC}	Supply voltage	V _{CC}	+3.0	+3.3	+3.6	V	【Note2】
	Current dissipation	I _{CC}	—	290	460	mA	【Note3】
Permissive input ripple voltage		V _{RP}	—	—	100	mV p-p	V _{CC} =+3.3V
Input voltage range		V _I	0	—	2.4	V	LVDS signal
Differential input threshold voltage	High	V _{TH}	—	—	+100	mV	V _{CM} =+1.2V 【Note1】
	Low	V _{TL}	-100	—	—	mV	
Input current (High)		I _{OH}	—	—	±10	μA	V _I =2.4V V _{CC} =3.6V
Input current (Low)		I _{OL}	—	—	±10	μA	V _I =0V V _{CC} =3.6V
Terminal resistor		R _T	—	100	—	Ω	Differential input

【Note1】 V_{CM} : Common mode voltage of LVDS driver.

【Note2】

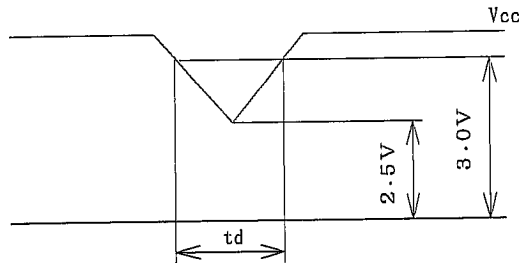
On-off conditions for supply voltage

- $0 < t1 \leq 10\text{ms}$
- $0 < t2 \leq 128\text{ms}$
- $0 < t3 \leq 1\text{s}$
- $t4 > 200\text{ms}$



Vcc-dip conditions

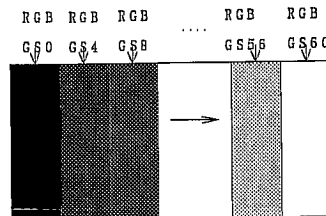
- 1) $2.5\text{V} \leq V_{cc} < 3.0\text{V}$
 $t_d \leq 10\text{ms}$
- 2) $V_{cc} < 2.5\text{V}$



Vcc-dip conditions should also follow the On-off conditions for supply voltage

【Note3】 Typical current situation : 16-gray-bar pattern.

$V_{cc} = +3.3\text{V}$

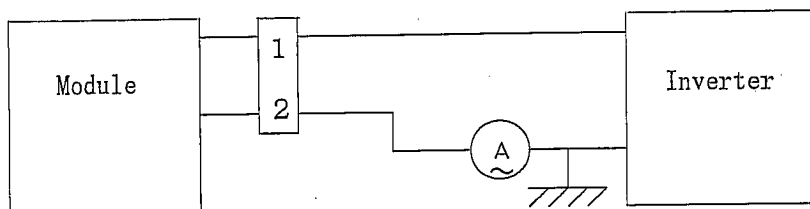


6-2. Backlight driving

The backlight system is an edge-lighting type with single CCFT (Cold Cathode Fluorescent Tube).
The characteristics of the only lamp are shown in the following table.

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Lamp current range	I_L	2.0	5.0	6.0	mArms	【Note1】
Lamp voltage	V_L	—	640	—	Vrms	
Lamp power consumption	P_L	—	3.2	—	W	【Note2】
Lamp frequency	F_L	30	60	70	KHz	【Note3】
Kick-off voltage	V_s	—	—	1450	Vrms	$T_a = 25^\circ\text{C}$
		—	—	1500	Vrms	$T_a = 0^\circ\text{C}$ 【Note4】
Lamp life time	L_L	10000	—	—	Hour	【Note5】

【Note1】 Lamp current is measured with current meter for high frequency as shown below.

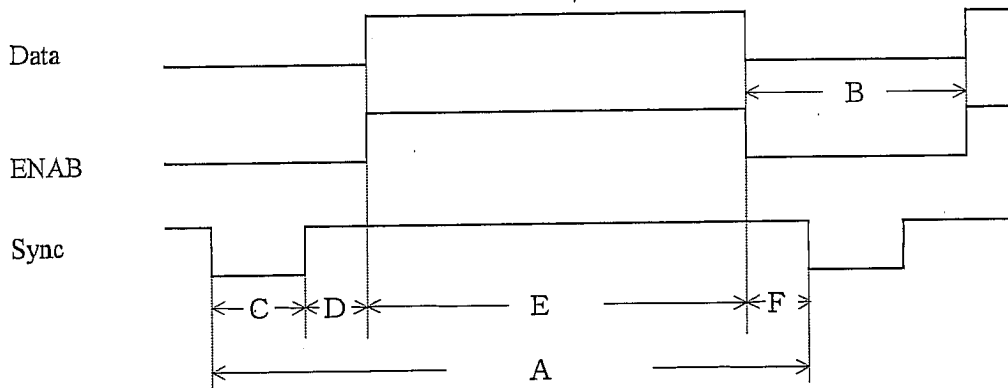


* 2pin is V_{LOW}

- 【Note2】 Calculated value for reference ($I_L \times V_L$)
- 【Note3】 Lamp frequency may produce interference with horizontal synchronous frequency, and this may cause beat on the display. Therefore lamp frequency shall be detached as much as possible from the horizontal synchronous frequency and from the harmonics of horizontal synchronous to avoid interference.
- 【Note4】 The voltage above this value should be applied to the lamp for more than 1 second to start-up. Otherwise the lamp may not be turned on.
- 【Note5】 Lamp life time is defined as the time when either ① or ② occurs in the continuous operation under the condition of $T_a=25^\circ\text{C}$ and $I_L=6.0\text{mA}_{\text{rms}}$.
- ① Brightness becomes 50% of the original value under standard condition.
 - ② Kick-off voltage at $T_a=0^\circ\text{C}$ exceeds maximum value, 1500V_{rms}.
- Note) The performance of the backlight, for example life time or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the backlight and the inverter (miss-lighting, flicker, etc.) never occur. When you confirm it, the module should be operated in the same condition as it is installed in your instrument.

7. Timing characteristics of input signals

7-1. Timing characteristics (This is specified at digital outputs of LVDS driver.)



(Vertical)

Item (symbol)	Min.	Typ.	Max.	Unit	備考
Vsync cycle (T_{VA})	—	16.667	—	ms	Negative
	803	806		line	
Blanking period(T_{VB})	35	38	—	line	
Sync pulse width (T_{VC})	4	6	—	line	
Back porch (T_{VD})	0	29		line	
Sync pulse width+Back porch ($T_{VC}+T_{VD}$)	35	35	35	line	
Active display area (T_{VE})	768	768	768	line	
Front porch (T_{VF})	0	3	—	line	

(Horizontal)

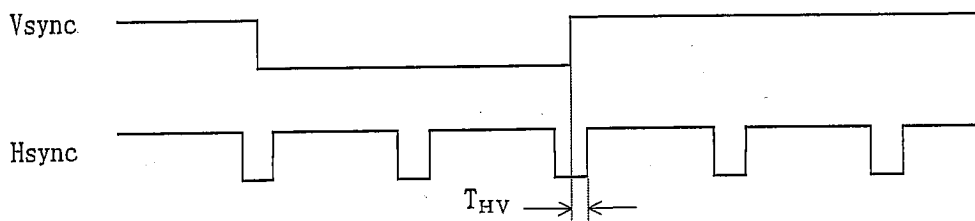
Item (symbol)	Min.	Typ.	Max.	Unit	Remark
Hsync cycle (T_{HA})	19.4	20.677	—	μs	Negative
	1260	1344	1408	clock	
Blanking period (T_{HB})	236	320	—	clock	
Sync pulse width (T_{HC})	8	136	—	clock	
Sync pulse width + Back porch ($T_{HC} + T_{HD}$)	$1500 - T_{HA}$	296	$T_{HA} - 1024$	clock	
Active display area (T_{HE})	1024	1024	1024	clock	
Front porch (T_{HF})	0	24	—	clock	

(Clock)

Item	Min.	Typ.	Max.	Unit	Remark
Frequency	50.0	65.0	65.0	MHz	【Note1】

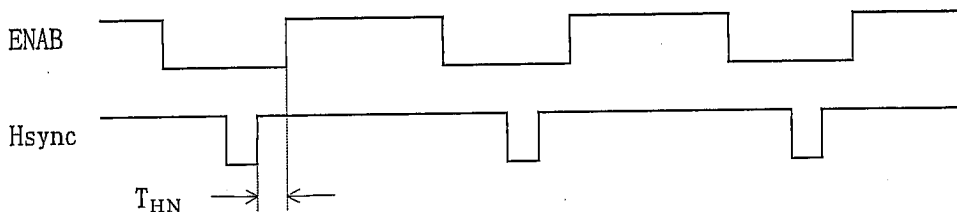
Note) In case of lower frequency, the deterioration of display quality, flicker etc., may be occurred.

(Hsync-Vsync Phase difference)



Item(symbol)	Min.	Typ.	Max.	Unit	Remark
Hsync-Vsync Phase difference(T_{HV})	1	—	$T_{HA} - T_{HC}$	clock	

(Hsync-ENAB Phase difference)



Item	Min.	Typ.	Max.	Unit	Remark
(T_{HN})	0	—	312	clock	

7-2 Display position

Item	Standards	Beginning	Ending	Unit	Remark
Horizontal	rising edge of ENAB	0	1024	clock	
	rising edge of Hsync	296	1320	clock	【Note1】
Vertical	rising edge of Vsync	35	803	clock	

【Note1】 ENAB signal must be fixed to low.

[Note]

(Horizontal display direction)

When ENAB is fixed low, 296 clock are counted from Hsync negative edge and data from after are available. If you need other timing, please use ENAB signal.

(Vertical display direction)

35 lines are counted from Vsync negative edge and data from next line are available.

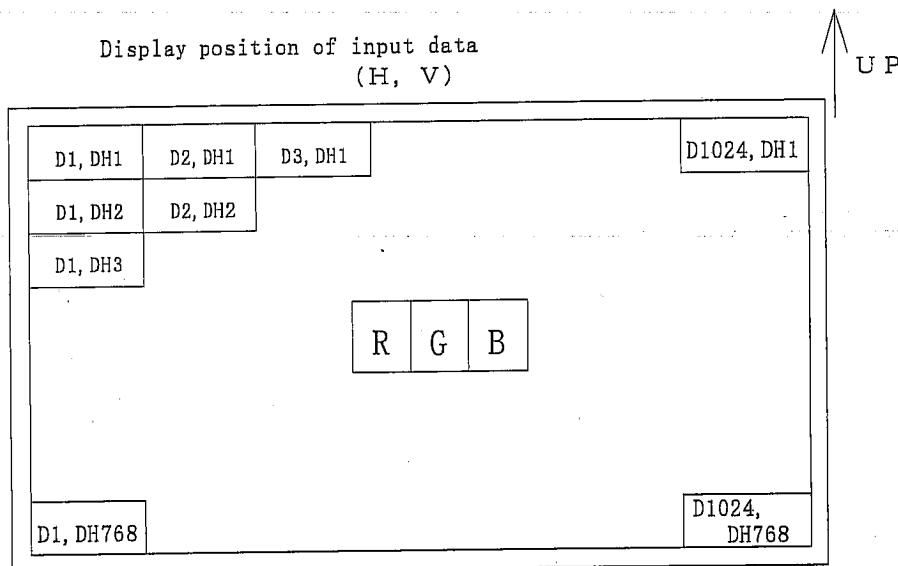
(Note of ENAB signal)

ENAB could not be used for the purpose of the vertical display start timing.

Caution

Image will not be displayed on the right position otherwise.

7-3. Input Data Signals and Display Position on the screen



8. Input Signals, Basic Display Colors and Gray Scale of Each Color

Colors & Gray scale	Data signal																			
	GrayScale	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	B1	B2	B3	B4	B5	
Basic Color	Black	—	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	—	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Green	—	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Cyan	—	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Red	—	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	—	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	—	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	—	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑ Darker	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑ ↓	↓				↓						↓						↓		
		↓				↓						↓						↓		
	Brighter ↓	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
		GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Green	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑ Darker	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
		GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
	↑ ↓	↓				↓						↓						↓		
		↓				↓						↓						↓		
	Brighter ↓	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
		GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Gray Scale of Blue	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑ Darker	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
		GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
	↑ ↓	↓				↓						↓						↓		
		↓				↓						↓						↓		
	Brighter ↓	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
		GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

0 :Low level voltage, 1 : High level voltage

Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

9. Optical Characteristics

Ta=25°C, Vcc=+3.3V

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle Range	Horizontal	$\theta 21, \theta 22$	CR>10	45	—	—	Deg.	【Note1,4】
	Vertical	$\theta 11$		10	—	—	Deg.	
		$\theta 12$		30	—	—	Deg.	
Contrast ratio		CRn	$\theta = 0^\circ$	150	—	—		【Note2,4】
		CRo	Optimum viewing angle	—	300	—		
Response Time	Rise	τr	$\theta = 0^\circ$	—	15	—	m s	【Note3,4】
	Decay	τd		—	30	—	m s	
Chromaticity of White		x	$\theta = 0^\circ$	0.263	0.313	0.363		【Note4】
		y		0.279	0.329	0.379		
Luminance of white		Y_{L1}		50	70	—	cd/m ²	IL=3.0mArms FL=60kHz
		【Note4】		Y_{L2}	90	120	—	
White Uniformity		δw			—	—	1.35	

※The measurement shall be executed 30 minutes after lighting at rating. (typical condition:IL=5.0mArms)
 The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig.3 below.

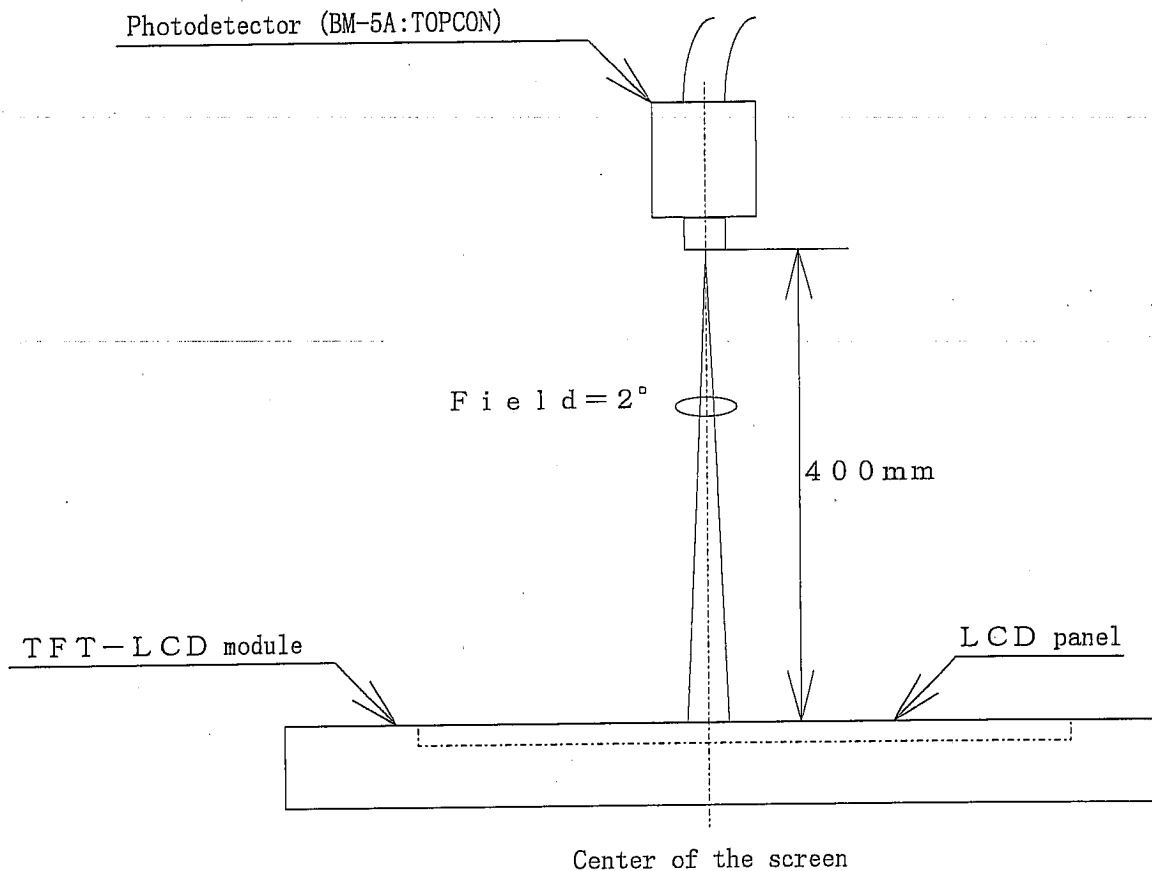
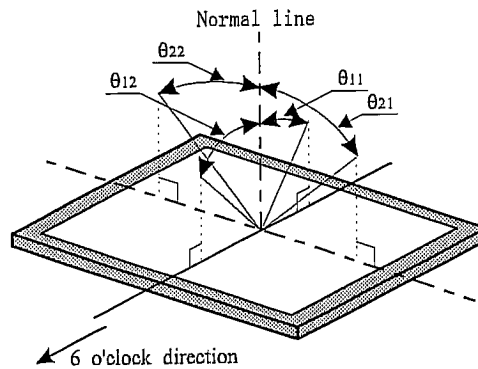


Fig.3 Optical characteristics measurement method

[Note1] Definitions of viewing angle range:



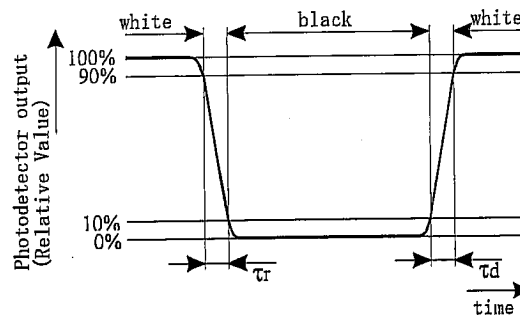
[Note2] Definition of contrast ratio:

The contrast ratio is defined as the following.

$$\text{Contrast Ratio (CR)} = \frac{\text{Luminance (brightness) with all pixels white}}{\text{Luminance (brightness) with all pixels black}}$$

[Note3] Definition of response time:

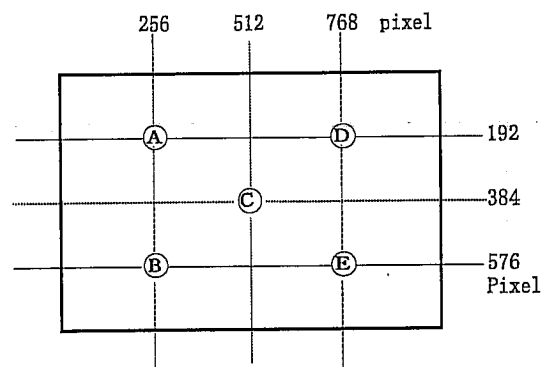
The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



[Note4] This shall be measured at center of the screen.

[Note5] Definition of white uniformity: ($I_L=5.0\text{mA}_{\text{rms}}$, $F_L=60\text{kHz}$)

White uniformity is defined as the following with five measurements (A~E).



$$\delta_w = \frac{\text{Maximum Luminance of five points (brightness)}}{\text{Minimum Luminance of five points (brightness)}}$$

10. Display Quality

The display quality of the color TFT-LCD module shall be in compliance with the Incoming Inspection Standard.

11. Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
- c) Since the front polarizer is easily damaged, pay attention not to scratch it (especially removing bezel for transportation.)
- d) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- f) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
Handle with care.
- g) Since CMOS LSI is used in this module, take care of static electricity and injure the human earth when handling.
- h) Observe all other precautionary requirements in handling components.
- i) This module has its circuitry PCBs on the rear side and should be handled carefully in order not to be stressed.
- j) Laminated film is attached to the module surface to prevent it from being scratched. Peel the film off slowly just before the use with strict attention to electrostatic charges. Ionized air shall be blown over during the action. Blow off the 'dust' on the polarizer by using an ionized nitrogen gun, etc..
- k) Clear PET sheet covers some electric components and handle with special care to avoid mechanical stress and shock on this PET surface.
- l) This module is assembled with bezel for transportation.
Please remove this bezel when you mount LCD module on PC cabinet.
When you remove this bezel, please take precautions below.
 - l-1) When LCD module is handled, please hold P-chassis on the rear side of LCD module by both hands so that LCD module is placed even with the ground.
Please do not make panel surface down, or put LCD module vertically.
(Please make sure that backlight will not be lifted from LCD panel.)
 - l-2) Since electric components (especially chip area of gate-driver and source-driver) are exposed on the surface and rear side of LCD module, please do not touch this area
 - l-3) Please do not leave LCD module without bezel for a long time.

12. Packing form

- a) Piling number of cartons : MAX. 7
- b) Package quantity in one carton : 10pcs
- c) Carton size : 405mm(W)×277mm(H)×356mm(D)
- d) Total mass of one carton filled with full modules : 5.8kg

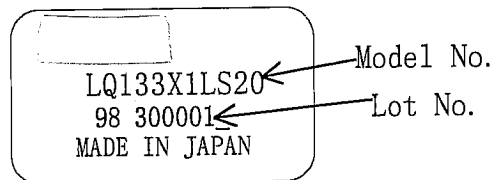
Packing form is shown in Fig.4.

13. Reliability test items

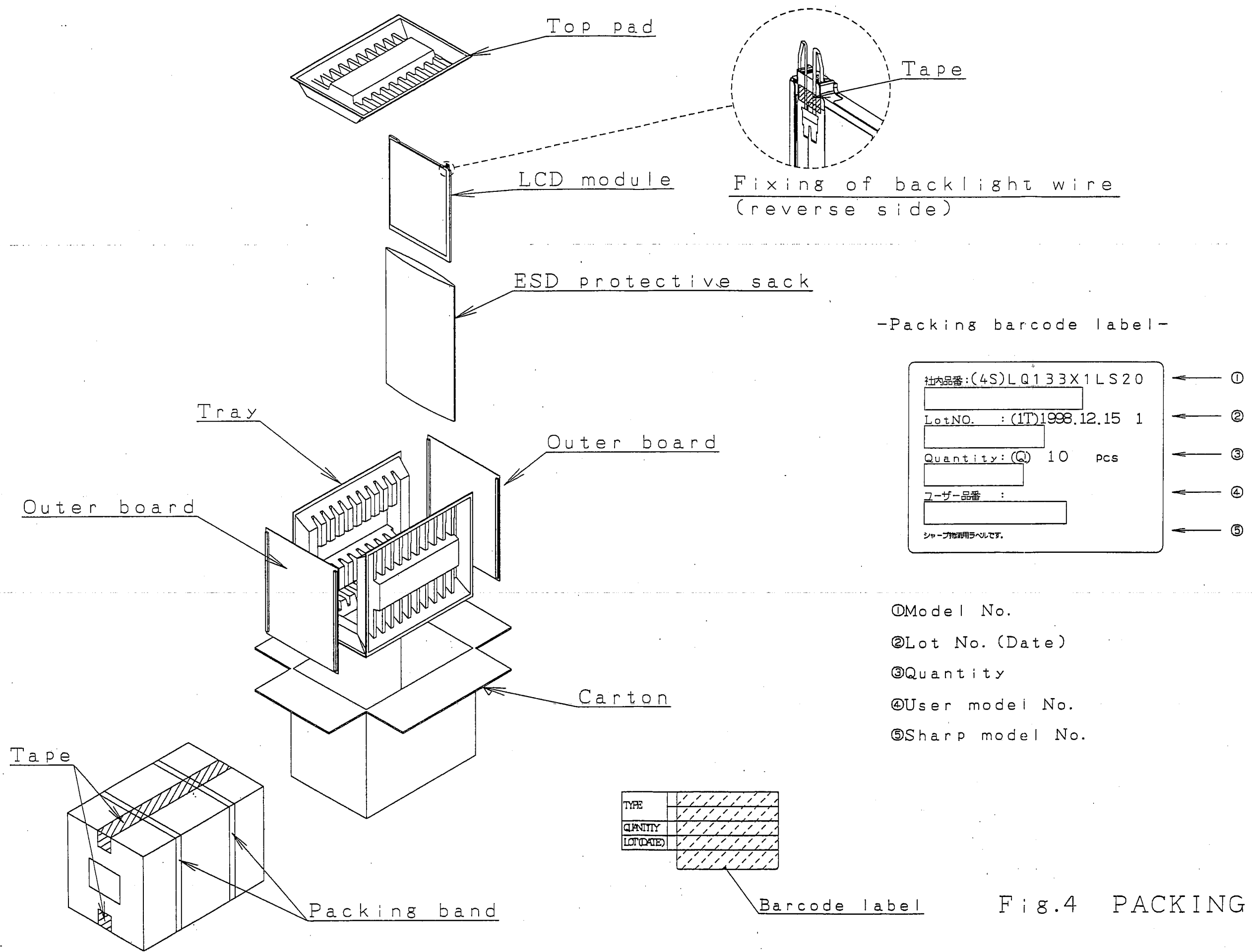
No.	Test item	Conditions
1	High temperature storage test	Ta=60°C 240h
2	Low temperature storage test	Ta=-25°C 240h
3	High temperature & high humidity operation test	Ta=40°C ; 95%RH 240h (No condensation)
4	High temperature operation test	Ta=50°C 240h (The panel temp. must be less than 60°C)
5	Low temperature operation test	Ta=0°C 240H

14. Others

- 1) Lot No. Label:



- 2) Adjusting volume have been set optimally before shipment, so do not change any adjusted value.
If adjusted value is changed, the specification may not be satisfied.
- 3) Disassembling the module can cause permanent damage and should be strictly avoided.
- 4) Please be careful since image retention may occur when a fixed pattern is displayed for a long time.
- 5) If any problem occurs in relation to the description of this specification , it shall be resolved through discussion with spirit of cooperation.



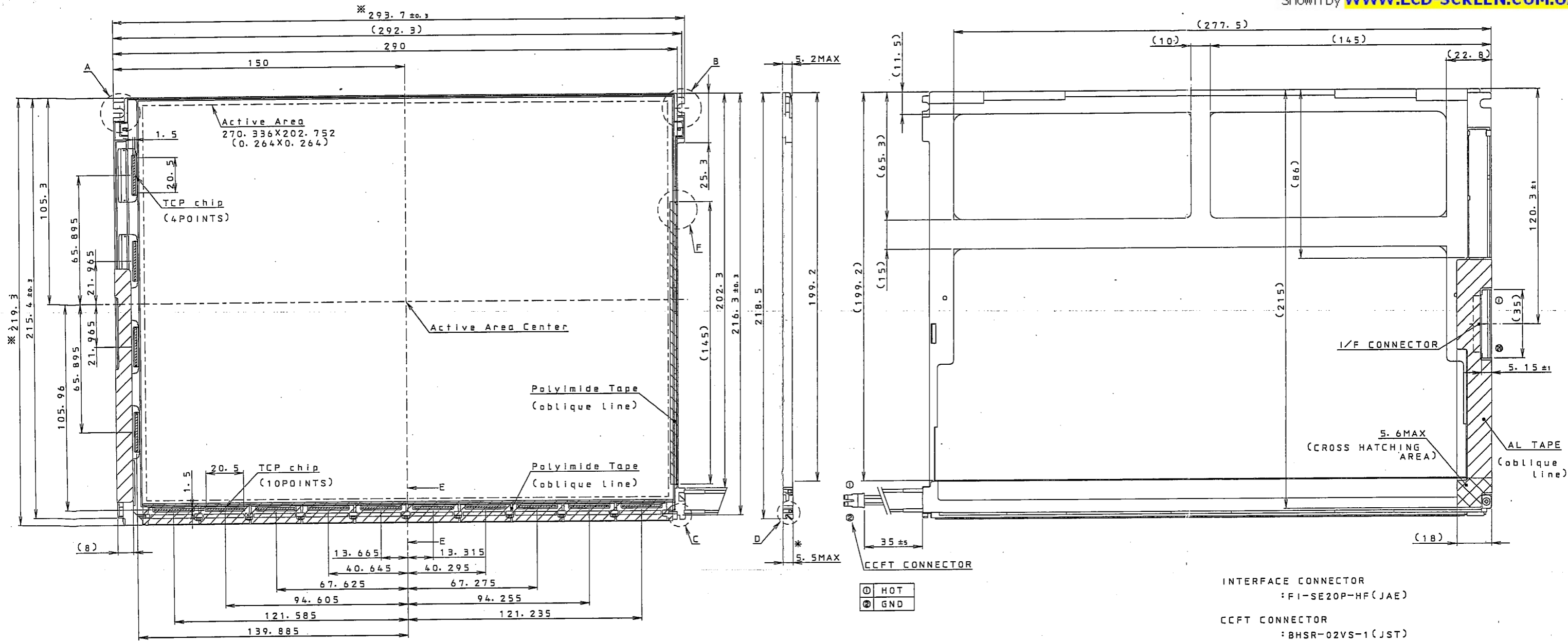
-Packing barcode label-

社内品番: (4S)LQ133X1LS20	← ①
LotNO. : (1T)1998.12.15 1	← ②
Quantity: (Q) 10 pcs	← ③
ユーザー品番 :	← ④
シャープ物用ラベルです.	← ⑤

- ① Model No.
- ② Lot No. (Date)
- ③ Quantity
- ④ User model No.
- ⑤ Sharp model No.

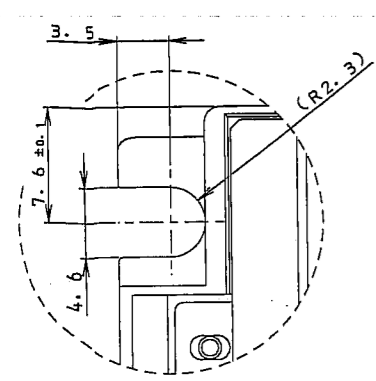
TYPE	
QUANTITY	
LOT(DATE)	

Fig.4 PACKING FORM

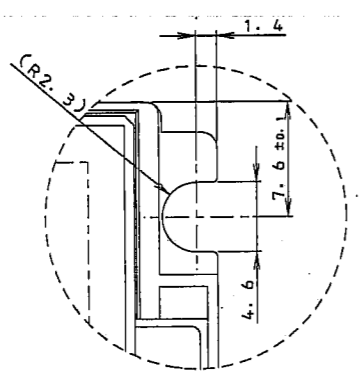


① HOT
② GND

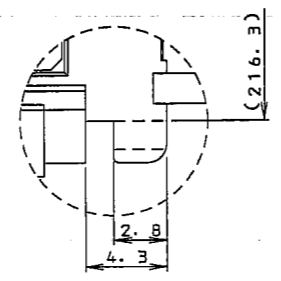
INTERFACE CONNECTOR
:FI-SE20P-HF(JAE)
CCFT CONNECTOR
:BHSR-02VS-1(JST)



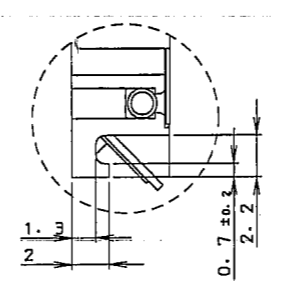
SEE VIEW A (S=4/1)



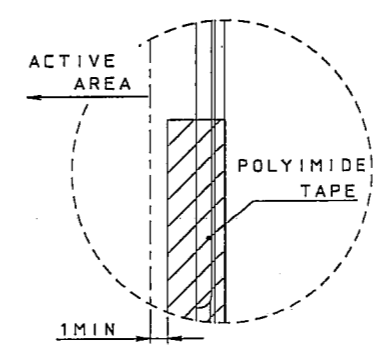
SEE VIEW B (S=4/1)



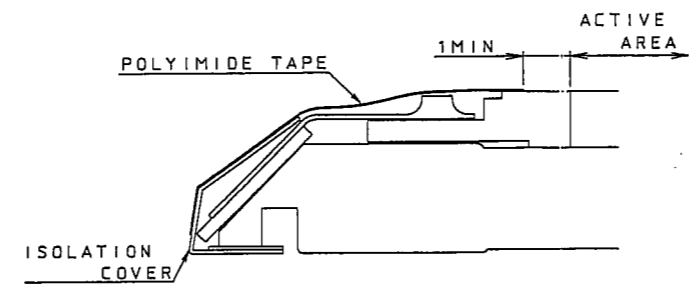
SEE VIEW C (S=5/1)



SEE VIEW D (S=5/1)



SEE VIEW F (S=4/1)



SECT E-E

UNSPECIFIED TOLERANCE	
RANGE	TOLERANCE
0 ~ 1	±0.1
1 ~ 10	±0.2
10 ~ 50	±0.3
50 ~ 100	±0.4
100 ~	±0.5

WARP AND FLATING FOR MECHANICAL PARTS ARE EXCLUDED FROM THICKNESS AND DIMENSION OF THE UNIT.
ONLY THE CROSS HATCHING AREA SHOWN IN THE LCD BACKSIDE DRAWING IS 5.6mmMAX.
※: EXCLUDING THE TOLERANCE OF AL TAPE AND ISOLATION COVER(0.5mm).
*: NOT INCLUDE OF POLYIMIDE TAPE(t=0.1mm).

Fig.1 LQ133X1LS20 OUTLINE DIMENSIONS