

SHARP

No. LD-24250A

DATE 01-Feb-12 REV. -

TECHNICAL

LITERATURE

FOR

TFT - LCD module

MODEL No.

LQ150X1LG98

These parts have corresponded with the RoHS directive.

The technical literature is subject to change without notice. So, please contact SHARP or its representative before designing your product based on this literature.

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RECORDS OF REVISION

LQ150X1LG98

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1. Application

This technical literature applies to the color TFT-LCD module LQ150X1LG98.

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Do not use the device for equipment that requires an extreme level of reliability, such as aerospace applications, telecommunication equipment (trunk lines), nuclear power control equipment and medical or other equipment for life support.

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This module is a color active matrix LCD module incorporating amorphous silicon TFT (<u>Thin Film Transistor</u>). It is composed of a color TFT-LCD panel, driver ICs, control circuit, power supply circuit and a White-LED Backlight unit. Graphics and texts can be displayed on a 1024 × RGB × 768dots panel with about 16 million colors by using LVDS (Low Voltage Differential Signaling) and supplying +3.3V DC supply voltages for TFT-LCD panel driving and supply voltage for backlight.

The maximum viewing angle is in the 6o'clock direction.

The 12o'clock direction is difficult to reverse the grayscale.

The LED driver circuit and the PWM circuit to drive the backlight are built into the module.

3. Mechanical technical literatures

Parameter	technical literatures	Unit
Display size	38 (15inch) Diagonal	cm
Active area	304.1 (H) × 228.1 (V)	mm
Divid former	1024(H)×768(V)	and and I
Pixel format	(1pixel=R+G+B dot)	pixel
Aspect ratio	4:3	
Pixel pitch	0.297(H)×0.297(V)	mm
Pixel configuration	R,G,B vertical stripe	
Display mode	Normally white	
Unit outline dimensions [*1]	326.5 (W) × 253.5 (H) × 9.6(D)	mm
Mass	(1200g (Max.), 980g (typ.))	g
Surface treatment	Anti-glare and hard-coating 3H	

[*1] Excluding the protrusion of the connector cover from thickness.

Outline dimensions are shown in Fig.1.

4. Input Terminals

4-1. TFT-LCD panel driving

CN1 (Interface signals and +3.3V power supply)

Using connectors: DF14H-20P-1.25H(56) (Hirose Electric Co., Ltd.)

Corresponding connectors: DF14-20S-1.25C(conector) (Hirose Electric Co., Ltd.)

:DF14-2628SCFA(terminal) (Hirose Electric Co., Ltd.)

Using LVDS receiver: Building into cotrol IC (THC63LVDF84B (Thine electronics) or Compatible product)

 $Corresponding \ LVDS \ transmitter: THC63LVDM83D (Thine \ electronics) \ or \ Compatible \ product$

CN1

Pin	Symbol	Function	Remark
1	VCC	+3.3V Power supply	
2	VCC	+3.3V Power supply	
3	GND	GND	
4	GND	GND	
5	RxIN0-	LVDS receiver signal CH0 (-)	LVDS
6	RxIN0+	LVDS receiver signal CH0 (+)	LVDS
7	GND	GND	
8	RxIN1-	LVDS receiver signal CH1 (-)	LVDS
9	RxIN1+	LVDS receiver signal CH1 (+)	LVDS
10	GND	GND	
11	RxIN2-	LVDS receiver signal CH2 (-)	LVDS
12	RxIN2+	LVDS receiver signal CH2 (+)	LVDS
13	GND	GND	
14	CK IN-	LVDS receiver signal CK (-)	LVDS
15	CK IN+	LVDS receiver signal CK (+)	LVDS
16	GND	GND	
17	RxIN3-	LVDS receiver signal CH3 (-)	LVDS
18	RxIN3+	LVDS receiver signal CH3 (+)	LVDS
19	RL/UD	Horizontal/Vertical display mode select signal	[*1]
20	SELLVDS	LVDS SET	[*2]

[*1] RL/UD = LOW



RL/UD = HIGH



[*2] SELLVDS is shown in 4-2.



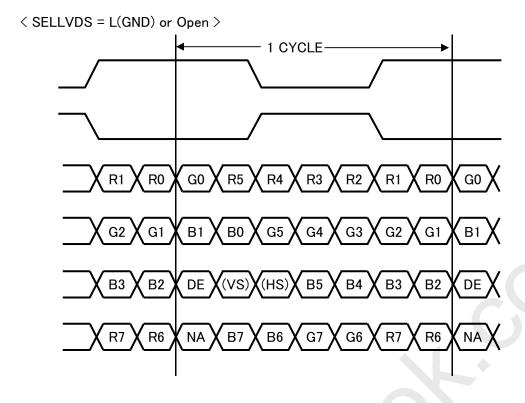
4-2. Data Mapping

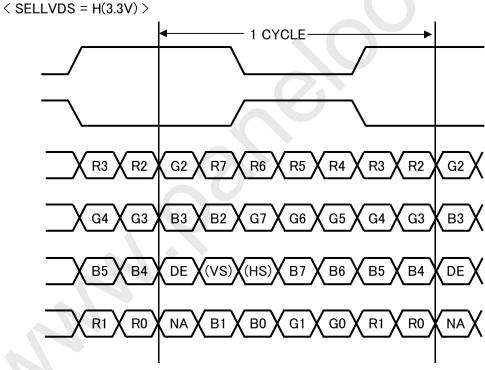
1) 8 bit input

pin assignment with SELLVDS pin $(THC63LVDM83D(Thine\ electronics)\ or\ Compatible\ product)$

Transmitter		20Pin SELLVDS			
Pin No	Data	= L(GND) or Open	= H(3.3V)		
51	TA0	R0 (LSB)	R2		
52	TA1	R1	R3		
54	TA2	R2	R4		
55	TA3	R3	R5		
56	TA4	R4	R6		
3	TA5	R5	R7 (MSB)		
4	TA6	G0 (LSB)	G2		
6	TB0	G1	G3		
7	TB1	G2	G4		
11	TB2	G3	G5		
12	TB3	G4	G6		
14	TB4	G5	G7 (MSB)		
15	TB5	B0 (LSB)	B2		
19	TB6	B1	В3		
20	TC0	B2	B4		
22	TC1	В3	B5		
23	TC2	B4	В6		
24	TC3	B5	B7 (MSB)		
27	TC4	(HS)	(HS)		
28	TC5	(VS)	(VS)		
30	TC6	DE	DE		
50	TD0	R6	R0 (LSB)		
2	TD1	R7 (MSB)	R1		
8	TD2	G6	G0 (LSB)		
10	TD3	G7 (MSB)	G1		
16	TD4	В6	B0 (LSB)		
18	TD5	B7 (MSB)	B1		
25	TD6	(NA)	(NA)		







DE: DATA ENABLE

HS:Hsync VS:Vsync

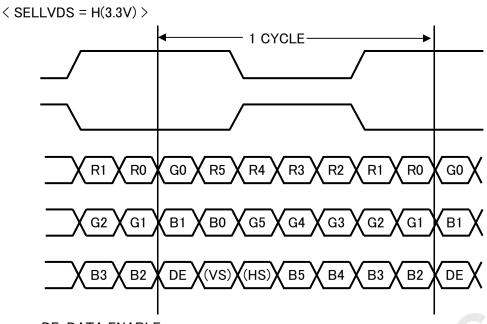
NA: Non Available



1) 6bit input

pin assignment with SELLVDS (THC63LVDM83D(Thine electronics) or Compatible product)

Transmitter		20Pin SELLVDS		
Pin No	Data	= L(GND) or Open	= H(3.3V)	
51	TA0	-	R0 (LSB)	
52	TA1	_	R1	
54	TA2	-	R2	
55	TA3	-	R3	
56	TA4	-	R4	
3	TA5	-	R5 (MSB)	
4	TA6	-	G0 (LSB)	
6	ТВ0	-	G1	
7	TB1	-	G2	
11	TB2	-	G3	
12	ТВ3	-	G4	
14	TB4	-	G5 (MSB)	
15	TB5	-	B0 (LSB)	
19	TB6	-	B1	
20	TC0	-//	B2	
22	TC1	-	В3	
23	TC2	-	B4	
24	TC3	-	B5 (MSB)	
27	TC4	-	(HS)	
28	TC5	-	(VS)	
30	TC6	-	DE	
50	TD0	-	GND	
2	TD1	-	GND	
8	TD2	-	GND	
10	TD3	-	GND	
16	TD4	_	GND	
18	TD5	-	GND	
25	TD6	-	(NA)	

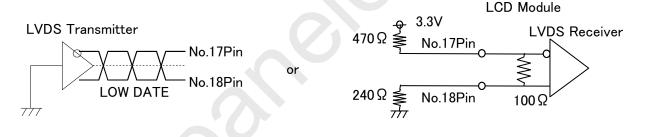


DE: DATA ENABLE

HS:Hsync VS:Vsync

NA:Non Available

Recommended input (17pin, 18pin at 6bit)



4-3. LED backlight

LED backlight connector

CN2 Used connector : SM06B-SHLS-TF (J.S.T. Mfg. Co. Ltd)

Corresponding connector : SHLP-06V-S-B (J.S.T. Mfg. Co. Ltd)

Connector No.	Pin No.	symbol	function
	1	VDD	+12V power supply
ou o	2	VDD	+12V power supply
	3	GND	GND
CN2	4	GND	GND
	5	XSTABY	Backlight ON/OFF signal
	6	VBR	PWM signal

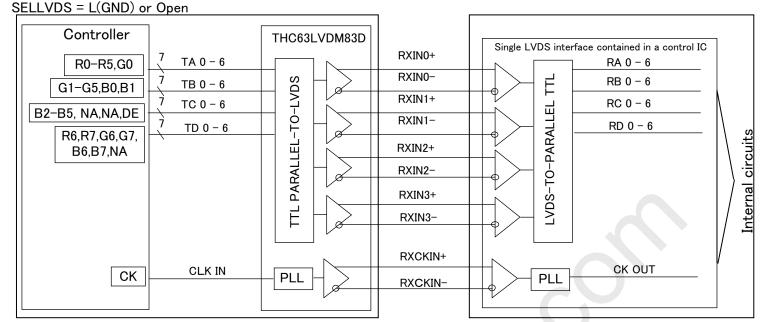
PLL



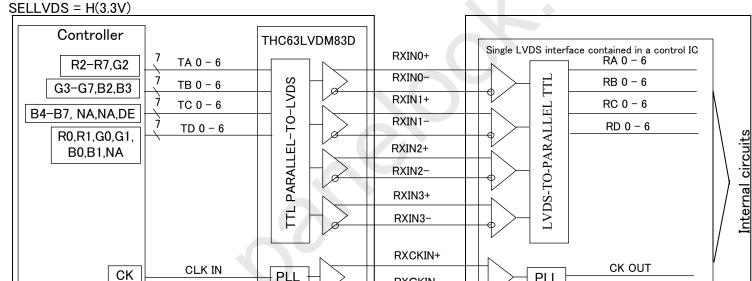
(Computer Side) 1)8 bit input

4-4. Interface block diagram

(TFT-LCD side)

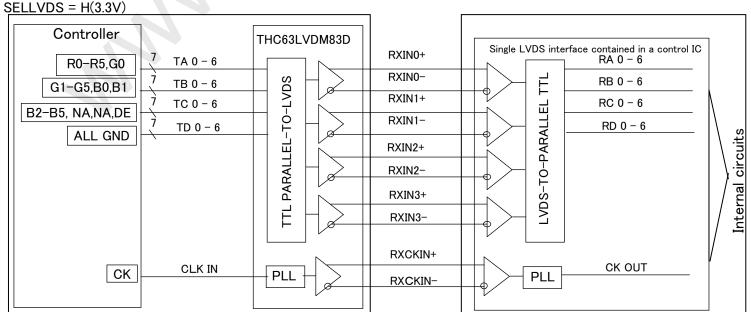


28 bit input



RXCKIN-







5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Pin	Ratings	Unit	Remark
Supply voltage	Vcc Ta=25°C VCC		VCC	−0.3 ~ +4.0	٧	【*1,2】
	V _{DD} Ta=25°C VDD −0.3 ~ +15.0		٧	【*1,2】		
)/ T 0/		RxINi-/+	0.25.1/5.510.2	.,	:-0100
Townshire the me	V _{I 1}	Ta=25°C	CK IN-/+	-0.3 ~ Vcc+0.3	V	i=0,1,2,3
Input voltage	V _{I 2}	Ta=25°C	RL/UD,SELLVDS	-0.3~Vcc+0.3	٧	
	V _{I 4}	Ta=25°C	XSTABY, VBR	-0.3∼V _{DD}	٧	
Storage temperature	T _{STG}	_	-	−30 ~ +80	°C	[*1]
Operating temperature	T_OPA	1	-	(-20) ~ +70	°C	【*1,3,4】

[*1] Humidity: 95%RH Max.($Ta \le 40^{\circ}C$) Note static electricity.

Maximum wet-bulb temperature at 39°C or less. (Ta>40°C) No condensation.

- [*2] The Vcc power supply capacity must use the one of 2A or more. The VDD power supply capacity must use the one of 3A or more.
- [*3] There is a possibility of causing deterioration in the irregularity and others of the screen and the display fineness though the liquid crystal module doesn't arrive at destruction when using it at $65 \sim 70^{\circ}$ C or $-20 \sim 0^{\circ}$ C. There is a possibility of causing the fineness deterioration bythe prolonged use in the(high temperature) humidity environment(60% or more).
- [*4] In the operating temperature item, the low temperature side is the ambient temperature regulations. The high temperature side is the panel surface temperature regulations.



6. Electrical Characteristics

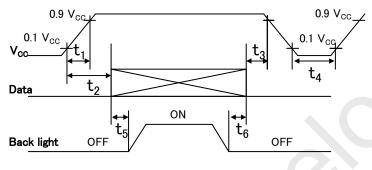
6-1. TFT-LCD panel driving

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$$T_a = +25^{\circ}C$$

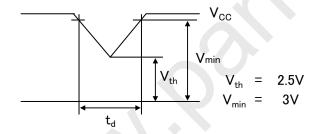
Parameter		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Supply voltag	е	V_{CC}		3.0	3.3	3.6	V	[*1]
Current dissipat	tion	I_{CC}	Vcc=3.3V	-	(300)	(400)	mA	【*2】
Input voltage width for LVDS	receiver	V_L		0	_	2.4	٧	
Permissive input ripple	voltage	V_{RP}		_	_	200	mV_{P-P}	Vcc = 3.3V
Differential input	High	V_{TH}		_	_	V _{CM} +100	mV	V _{CM} =+1.2V
Threshold voltage	Low	V_{TL}		V _{CM} -100	_	_	mV	[*3]
Innut valtana		V_{IH}		2.1	_	_	V	[*4]
Input voltage	•	V_{IL}		_	_	0.8	٧	
Input reak current		I_{OH}		_	_	400	μΑ	$V_{12} = +3.3V[*4]$
		I _{OL}		-10	-	+10	μΑ	V ₁₂ =0V [*4]
Terminal resist	or	R_T		_	100	_	Ω	Differential input

[*1] On-off conditions for supply voltage



$$20 \,\mu\,\mathrm{s} < t_1 \leq 10 \mathrm{ms}$$
 $0 < t_2 \leq 20 \mathrm{ms}$
 $0 < t_3 \leq 1 \mathrm{s}$
 $1 \mathrm{s} \leq t_4$
 $300 \mathrm{ms} \leq t_5$

Vcc-dip conditions



- V_{th} $< V_{CC} \le V_{min}$ $t_d \leq 10 ms$
- $V_{CC} < V_{th}$

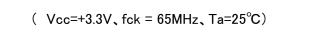
 $200 \text{ms} \leq t_6$

Vcc-dip conditions should also follow the On-off conditions for supply voltage

- Hsync/Vsync need not be input so that this model may drive only by the ENAB signal. Even if Hsync/Vsync is input, it doesn't become a malfunction.
- The relation between the data input and the backlight lighting will recommend the above-mentioned input sequence. When the backlight is turned on before the panel operates, there is a possibility of abnormally displaying. The liquid crystal module is not damaged.

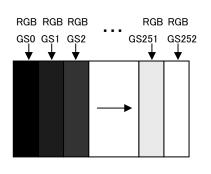
[*2] Current dissipation

Typical current situation: 253-gray-bar pattern (Vcc=+3.3V, fck = 65MHz, Ta=25°C)



[*3] V_{CM}: LVDS common mode voltage

[*4] RL/UD, SELLVDS



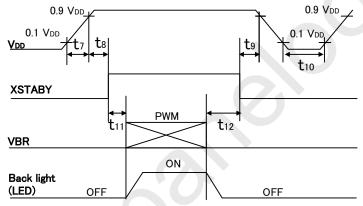
6-2. LED backlight

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 $Ta=+25^{\circ}C$

Parameter		Symbol	Min.	Тур.	Max.	Unit	Remark
Supply	voltage	V _{DD}	10.2	12.0	13.8	V	【*1】
C	dia a in a 4 i a	I DD1	-	(480)	(700)	mA	【*2】
Gurrent	dissipation	I DD2	ı	-	10.0	μΑ	
Permissive inp	ut ripple voltage	VRP_BL	-	-	200.0	mV _{P-P}	VDD=+12.0V
High volt	High voltage	VIH_BL1	2.4	-	VDD	٧	【*3】
XSTABY	Low voltage	VIL_BL1	-	-	0.4	٧	【*3】
VBR	High voltage	VIH_BL2	2.1	_	VDD	V	【*4】
VDK	Low voltage	VIL_BL2	-	-	0.4	٧	【*4】
PWM frequency		fрwм	200.0	-	1k	Hz	【*4,5】
PWM duty		Dрwм	10.0	-	100.0	%	【*4,5】
Life time		L	-	(50,000) (Module)	-	h	【Reference】 【*6】

[*1] On-off conditions for supply voltage



$$20 \, \mu \, \text{s} \leq t7 \leq 200 \, \text{ms}$$
 $0 \, \text{ms} \leq t8$
 $0 \, \text{ms} \leq t9$
 $200 \, \text{ms} \leq t10$
 $10 \, \text{ms} \leq t11$
 $0 \, \text{ms} \leq t12$

[*2] Current dissipation

Typ. value: VDD= +12V, Duty=100% Max. value: VDD= +10.2V, Duty=100%

[*3] XSTABY is connected by the pull-down resistor of 33k Ω .

[*4] VBR is connected by the pull-down resistor of $33k\Omega$.

[*5] PWM

 $fPWM = 1/t_{14}$

Duty 10%: Min. Luminance

Duty 100%: Max. Luminance

Luminance changes in proportion to the duty ratio. (t₁₃ \ge 10 μ s)

When the frequency slows, the display fineness might decrease.

[*6] Luminance becomes 50% of an initial value. (Ta=25°C, PWM=100%)

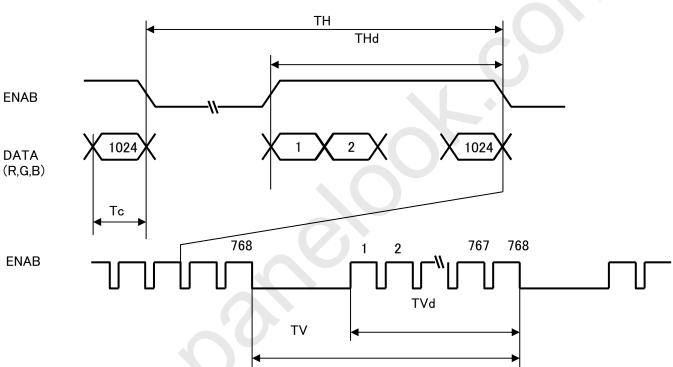
VBR

7. Timing characteristics of input signals

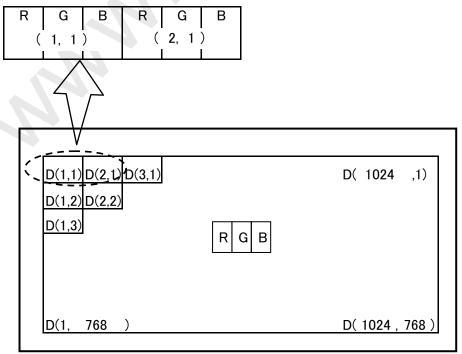
7-1. Timing characteristics

Parameter		Symbol	Min.	Тур.	Max.	Unit	Remark
Clock	Frequency	1/Tc	50.0	65	80.0	MHz	
ENAB	Havisantal naviad	TH	1056	1344	1720	clock	
	Horizontal period	IΠ	16.0	20.7	23.4	μs	
	Horizontal period (High)	THd	1024	1024	1024	clock	
	Vertical Frequency	TV	772	806	990	line	[*1]
			13.3	16.7	18.0	ms	[↑1]
	Vertical period (High)	TVd	768	768	768	line	

[*1] In case of using the long vertical period, the deterioration of display quality, flicker etc. may occur.



7-2. Input Data Signals and Display Position on the screen





8. Input Signals, Basic Display Colors and Gray Scale of Each Color

8-1. 8 bit input

		Data signal																								
	Colors & Gray scale	Gray Scale	R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	В0	B1	B2	В3	В4	B5	В6	В7
	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1
or	Green	_	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Color	Cyan	_	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1
Basic	Red	_	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
В	Magenta	_	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1
	Yellow	_	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	-	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1	Х	X	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
g	1	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
of Red	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ile o	1	1	1								1 1															
Scale	\downarrow	\downarrow	ļ								1															
Gray	Brighter	GS250	0	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	GS251	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS252	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
eu	1	GS1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Green	Darker	GS2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
of	1	1	1 1								<u> </u>															
scale	\downarrow	\downarrow									↓								↓							
Gray S	Brighter	GS250	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0
ģ	1	GS251	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0
	Green	GS252	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Φ	1	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Gray Scale of Blue	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	1	1	↑								t t															
Scal		1				,	l				Ţ															
ray	Brighter	GS250	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1
ŋ	\downarrow	GS251	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1
	Blue	GS252	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1

0 :Low level voltage 1 :High level voltage X :Don't care

Each basic color can be displayed in 253 gray scales from 8 bit data signals. According to the combination of, total 24 bit data signals, the 16-million-color display can be achieved on the screen.

8-2. 6 bit input

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	Colors &	Data signal																			
	Gray scale	GrayScale	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	В0	В1	B2	ВЗ	В4	B5	
	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Blue	_	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	
٥	Green	_	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	
Basic Color	Cyan	_	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	
asic	Red	_	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	
В	Magenta	_	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1	
	Yellow	_	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	
	White	_	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
٥	1	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Gray Scale of Red	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
o ale	1	\downarrow			,	Į			1												
Sca	\downarrow	\downarrow		1									1								
згау	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	
	\downarrow	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
eu	1	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	
Scale of Green	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	
e of	1	\downarrow	1						1							↓					
Scal	1	\downarrow				l					,	l					,	l			
Gray	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0	
g	1	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Gray Scale of Blue	1	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
	1	\downarrow							<u> </u>												
	\	\downarrow			,	ļ					1						,	l			
згау	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	
	1	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	

Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

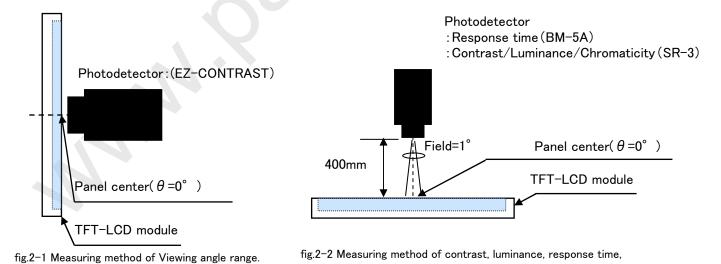
9. Optical Characteristics

Ta=+25°C, Vcc=+3.3V

Para	meter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark		
Viewing	Horizontal	θ 21, θ 22		(70)	(80)	-	Deg.			
angle	Vertical	θ 11	CR>10	(70)	(80)	-	Deg.	【*1,2,4】		
range	verticai	θ 12		(60)	(80)	-	Deg.			
Contra	st ratio	CR	optimized angle	(450)	(800)	-		[*2,4]		
Response Time	Response Time White Black			-	(30)	-	ms	[*3,4]		
Chroma	Chromaticity of White		ticity of Wx			(0.255)	(0.305)	(0.355)		
Wł				(0.275)	(0.325)	(0.375)				
Chromaticity of		Rx		_	(0.589)	-				
	Red			-	(0.337)	-		Fut 43		
Chroma	Chromaticity of		$\theta = 0^{\circ}$	_	(0.329)	-		【*4】		
Gr	een	Gy	0-0	-	(0.596)	_				
Chromaticity of Blue		Bx		_	(0.150)	\ -	·			
		Ву		-	(0.107)					
Luminance of white		Y _{L1}		(280)	(350))	cd/m²	[*4]		
White Uniformity			1	-		1.33		【*5】		

XThe measurement shall be executed 30 minutes after lighting at rating.

The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig.2 below.



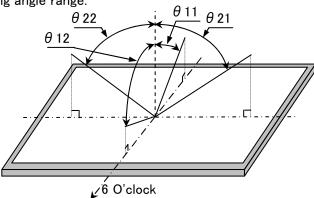
and Chromaticity.

Fig.2 Optical characteristics measurement method

Normal line



[*1]Definitions of viewing angle range:



[*2]Definition of contrast ratio:

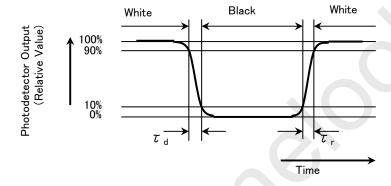
The contrast ratio is defined as the following. Contrast (CR) =

Luminance with all pixels white

Luminance with all pixels black

[*3] Definition of response time:

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

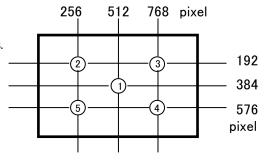


[*4] This shall be measured at center of the screen.

[*5] Definition of white uniformity:

White uniformity is defined as the following with five measurements. (1) \sim (5)

$$\delta_{\rm w} = \frac{{\sf Maximum\ luminance\ of\ 5\ points(1) \sim (5)}}{{\sf Minimum\ luminance\ of\ 5\ points(1) \sim (5)}}.$$



10. Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Since the front polarizer is easily damaged, pay attention not to scratch it.
- c) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- d) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- e) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
 Handle with care.
- f) Since CMOS LSI is used in this module, take care of static electricity and injure the human earth when handling. Observe all other precautionary requirements in handling components.
- g) Since there is a circuit board in the module back, stress is not added at the time of a design assembly.

 Please make it like. If stress is added, there is a possibility that circuit parts may be damaged.
- h) It causes an irregular display and the defective indication, etc., when always put constant pressure on the back of the module.
 - Please do not make the structure to press the back of the module.
- i) Do not expose the LCD panel to direct sunlight. Lightproof shade etc. should be attached when LCD panel is used under such environment.
- j) Connect GND to stabilize against EMI and external noise.
- k) When handling LCD modules and assembling them into cabinets, please avoid that long-terms storage in the environment of oxidization or deoxidization gas and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the modules. Do not use the LCD module under such environment.
- I) Liquid crystal contained in the panel may leak if the LCD is broken. Rinse it as soon as possible if it gets inside your eye or mouth by mistake.
- m) Be careful when using it for long time with fixed pattern display as it may cause accidential image.
- n) Adjusting volume have been set optimally before shipment, so do not change any adjusted value. If adjusted value is changed, the specification may not be satisfied.
- o) If a minute particle enters in the module and adheres to an optical material, it may cause display non-uniformity issue, etc. Therefore, fine-pitch filters have to be installed to cooling and inhalation hole if you intend to install a fan.
- p) The polarizer surface on the panel is treated with Anti-Glare for low reflection. In case of attaching protective board over the LCD, be careful about the optical interface fringe etc. which degrades display quality.
- q) Notice: Never take to pieces the module, because it will cause failure. Please do not peel off the Black tape pasted to the product.
- r) An abnormal display by changing in quality of the polarizing plate might occur regardless of contact or no contact to the polarizing plate, because of epoxy resin (amine system curing agent) that comes out from the material and the packaging material used for the set side, the silicon adhesive (dealcoholization system and oxime system), and the tray blowing agents (azo-compound), etc. Please confirm adaptability with your employed material.





T.B.D.

12. Reliability test items

No.	Test item	Conditions	Remark
1	High temperature storage test	Ambient temperature 80°C 240H	【Note1】
2	Low temperature strage test	Ambient temperature -30°C 240H	【Note1】
3	High temperature & high humidity operation test	Ambient temperature 40°C, Humidity 95% RH 240H (No condensation.)	【Note1】
4	High temperature operation test	Panel surface 70°C 240H	【Note1】
5	Low temperature operation test	Ambient temperature −20°C 240H	[Note1]
6	Vibration test (non-operating)	<pre><sin wave=""> Frequency :10~57Hz/Vibration width (one side) :0.076mm :57~500Hz/Gravity:9.8m/s2 Sweep time:11minutes Test period :3H(X, Y, Z direction 1H)</sin></pre>	【Note1】
7	Shock test (non-operating)	Max. gravity:490m/s2 Pulse width:11ms Direction: ±X,±Y,±Z Test period:1time ✓1direction	【Note1】
8	Thermal shock test (non-operating)	−30°C[0.5h]~80°C[0.5h]∕50cycles	【Note1】

[Note1] Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function. (normal operation state: Temperature:15~35°C, Humidity:45~75%, Atmospheric pressure:86~106kpa)

13. Others

13-1. Lot No Label:

A) Module serial label

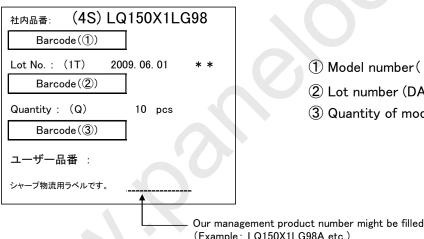
T.B.D.

B) Backlight serial label

T.B.D.

13-2. Packing box Label:

The label that displays ①Model number(LQ150X1LG98) ②Lot number ③Quantity of module is stuck on the packing box. Moreover, the display of bar code also applies to this.



- 1 Model number (LQ150X1LG98)
- 2 Lot number (DATA)
- 3 Quantity of module

(Example: LQ150X1LG98A etc.)

* R.C.(RoHs Compliance) means these parts have corresponded with the RoHs directive. This module corresponds from the first sample to RoHS Directive.

A right picture is written to the packing box of module for the RoHS restriction.

R.C.

- 13-3. The ozone-depleting substances is not used.
- 13-4. If any problem occurs in relation to the description of this technical literature, it shall be resolved through discussion with spirit of cooperation.



14. Storage conditions

Environmental condition range of storage temperature and humidity

Temperature 0 to 40 degrees Celsius

Relative humidity 95% and below

[Note] Please refer below as a mean value of the environmental conditions.

Summer time temperature 20 to 35 degrees Celsius humidity , 85% and below

Winter time temperature 5 to 15 degrees Celsius humidity, 85% and below

Please maintain within 240 hours of accumulated length of storage time, with conditions of 40 degrees

Celsius and room humidity of 95%.

Direct sun light

Please keep the product in a dark room or cover the product to protect from direct sun light.

Atmospheric condition

Please refrain from keeping the product with possible corrosive gas or volatile flux.

Prevention of dew

Please store the product carton either on a wooden pallet or a stand / rack to prevent dew.

Do not place directly on the floor. In addition, to obtain moderate ventilation in between the pallet's top and bottom surfaces, pile the cartons up in a single direction and in order.

Please place the product cartons away from the storage wall.

Storage period

Within above mentioned conditions, maximum storage period should be one year.

Global LCD Panel Exchange Center

屏庫:全球液晶屏交易中心

2-5.5±0.2

326.5±0.5

163.25

2-55±0.2

BEZEL OPENNING AREA 307.4×231.3

304.128×228.096

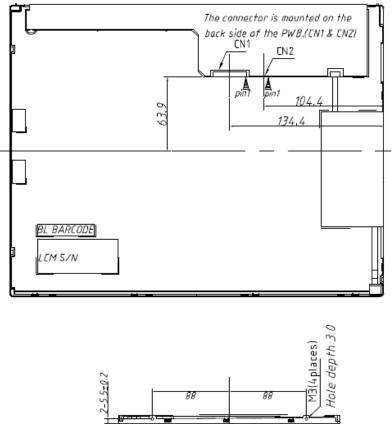
ACTIVE AREA CENTER

ZII

5'075'E5Z

52 921

211





Please confirm whether there is the problem such 3.Never take to pieces the module,because do not peel off the pwb cover.screw,tape pasted to the as loosening of the screw in a real machine



3.0BLIQUITY OF DISPLAY AREA /C-D/-0.5

2. TOLERANCE Y-DIRECTION B: 1.6±0.5 1. TOL ERANCE X-DIRECTION A:1.6±0.5

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