No.	LD -16753	
DATE	Aug 07 . 2004	

TECHNICAL LITERATURE

FOR

TFT - LCD module

MODEL No. LQ197V3DZ69

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DEVELOPMENT ENGINEERING DEPT. TAKI DEVELOPMENT CENTER AVC LIQUID CRYSTAL DISPLAY GROUP SHARP CORPORATION

RECORDS OF REVISION

LQ197V3DZ69

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1. Application

This technical literature applies to the color 19.7 "VGA TFT-LCD module LQ197V3DZ69.

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2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (<u>Thin Film Transistor</u>). It is composed of a color TFT-LCD panel, driver ICs, control circuit, power supply circuit, inverter circuit, back light system and etc. Graphics and texts can be displayed on a 640 × RGB × 480 dots panel with about 16 million colors by supplying date signal of 24 bit(8 bit x RGB), 3 kind of timing signal, +5V of DC supply voltages and supply voltage for back light.

Also, this module includes the DC/AC inverter to drive the CCFT lamps.

Parameter	Specifications	Unit
Display size	50 (Diagonal)	cm
	19.7 (Diagonal)	inch
Active area	401.28 (H) x 298.8 (V)	mm
Pixel Format	640 (H) × 480 (V)	pixel
	(1 pixel = R + G + B dot)	
Pixel pitch	0.627 (H) × 0.6225 (V)	mm
Pixel configuration	B, G, R vertical stripe	
Display mode	Normally black	
Unit Outline Dimensions *1	462.6(W) × 338.45(H) × 45.2(D)	mm
Mass	3150 ± 50	g
Surface treatment	Anti Glare, low reflection coating	
	Hard Coating: 2H	
	Haze :23 +/ - 5%	

3. Mechanical Specifications

(*1)Outline dimensions are shown in Fig.1

4. Input Terminals

4-1. Control circuit driving

CN1	Using connector	: 50FLZX-RSM1-A-TB (JST)
-----	-----------------	--------------------------

Pin No.	Symbol	Function	Remarks
1	GND		
2	СК	Clock signal(sampling each data)	
3	GND		
4	Hsync	Horizontal synchronized signal	Note 1
5	Vsync	Vertical synchronized signal	Note 1
6	GND		
7	GND		
8	R0	Red data signal (LSB)	
9	R1	Red data signal	
10	R2	Red data signal	
11	R3	Red data signal	
12	GND		
13	R4	Red data signal	
14	R5	Red data signal	
15	R6	Red data signal	
16	R7	Red data signal (MSB)	
17	GND		
18	G0	Green data signal (LSB)	
19	G1	Green data signal	
20	G2	Green data signal	
20	G3	Green data signal	
22	GND	Green data signal	
23	GIND G4	Green data signal	
23	G5	Green data signal	
24	G6 G6		
		Green data signal	
26	G7	Green data signal (MSB)	
27	GND		
28	B0	Blue data signal (LSB)	
29	B1	Blue data signal	
30	B2	Blue data signal	
31	B3	Blue data signal	
32	GND		
33	B4	Blue data signal	
34	B5	Blue data signal	
35	B6	Blue data signal	
36	B7	Blue data signal (MSB)	
37	GND		
38	GND		
39	NC	Reserved	
40	NC	Reserved	
41	NC	Reserved	
42	NC	Reserved	
43	NC	Reserved	
44	VDD	+5V Power Supply	
45	VDD	+5V Power Supply	
46	VDD	+5V Power Supply	
47	VDD	+5V Power Supply	
48	L/R	Reverse terminal of Right and Left	Note 2
49	U/D	Reverse terminal of Up and Down	Note 2
<u>49</u> 50	GND		

Shield case contacts GND(Grand) of LCD module. Recommended dimensions of FPC/FFC are shown in Fig.2.



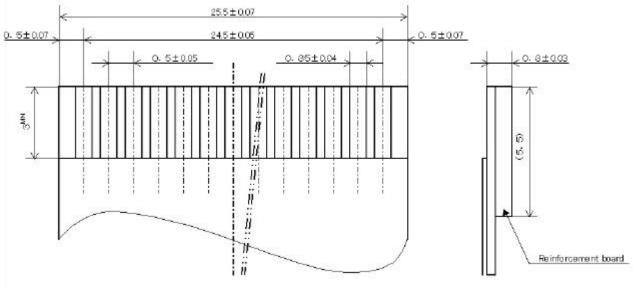
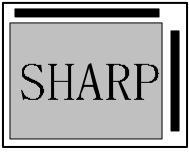


Fig.2 Recommended FPC/FFC dimensional diagram

Note 1 The polarity combination of the Hsync., Vsync.

Hsync	Negative
Vsync	Negative





R/L : L

U/D : L



R/L : L U/D : H



R/L : H U/D : L





4-2. Inverter driving for back light

$CN3 \qquad Suppling \ for \ Inverter \ Power \qquad Using \ connector: S12B-PH-SM3-TB(JST)$

Matching connector : PHR-12(JST)

Terminal #	Signal	Functions	Remarks
1	V _{ON}	Back light ON / OFF	Note 1
2	V _{BRT} 0	Setting of PWM light adjustment	Note 2
3	V _{BRT} 1	PWM light adjustment pulse input	Note 3
4	V _{BRT} 2	PWM light adjustment analogue input	Note 4
5	V _{BRT} 3	Current light adjustment analogue input	Note 5
6	GND	GND	
7	MNV		
8	MNV	+12V	
9	MNV		
10	GND		
11	GND	GND	
12	GND		

* GND(Ground) of Inverter doesn 't contact GND(Ground) of LCD module.

Note 1 Inverter ON / OFF

Input Voltage	Functions
3V	Inverter in action
0V	Inverter at still

Note 2 Setting of PWM light adjustment

To control PWM, adjust outer input pulse signal according to a range of analogue input voltage shown below.

Input Voltage	Functions
5V	V _{BRT} 2 select Note 4
0V	V _{BRT} 1 select Note 3

Note 3 PWM light adjustment pulse input

By 100 Hz 350 Hz input pulse signal, PWM DUTY is ontrolled.

Input signal			Functions	Remarks
MIN	TYP	MAX	DUTY (20%) :Dark :100% Bright (100Hz)	V _{BRT} 0=0V
100Hz	-	350Hz		
High Vo				



Ton > 2ms

Note 4 PWM light adjustment analogue input

By 0 5 V analogue input voltage, brightness controls adjusted.

Т

Input Voltage	Functions	Remarks
5V	Brightness Control (20%) Dark	V _{BRT} 0=5V
0V	Brightness Control (100%): Bright	

Note) 0 0.3 V: Duty is 100%.

Do not adjust the voltage between 0.3 0.7 V, as the range cannot be detected

Note 5 Current light adjustment analogue input

By 0 (5.0) V analogue input voltage, current light is adjusted.

	MIN	TYP	MAX	Functions
Input Voltage [V]	0	-	5.0	Light adjustment
Lamp Current [mArms]	4.0mA	-	6.0mA	4.0mA: Dark - 6.0mA: Bright

4-3. Back light driving

The back light system is under-lighting type with 6 CCFTs(Cold Cathode Fluorescent Tube).

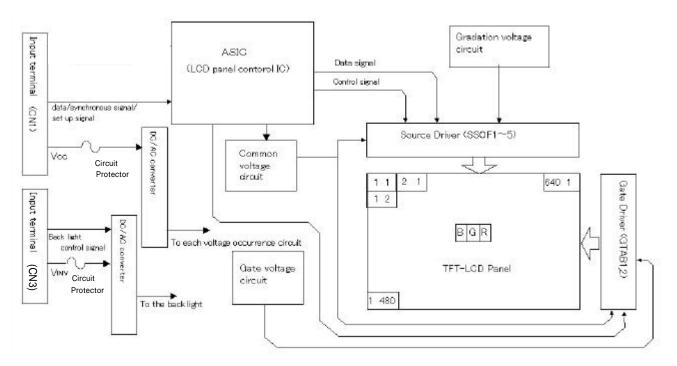
The characteristics of the lamp are shown in the following table. The value mentioned below is at the case of one CCFT.

Item	Symbol	Min.	Тур.	Max.	Unit	Remarks
Life time	ΤL	50000	-	-	Hour	Note 1

Note 1]Lamp life time is defined as below in the continuous operation under the condition of Ta=25 and Brightness Control (100%)

• Brightness becomes 50% of the original value under standard condition.

4-4 LCD Module Block Diagram



5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Input voltage (for Control)	V _{IC}	Ta=25	-0.3 3.6	V	Note 1
5V supply voltage (for Control)	Vcc	Ta=25	0 + 6	V	
Input voltage	V _{I1}	Ta=25	0 + 6	V	Note 2
(for Inverter)	V _{I2}	Ta=25	0 + 6	V	Note 3
12V supply voltage (for Inverter)	V _{INV}	Ta=25	0 +14	V	
Storage temperature	T _{stq}	-	-25 +60	Degree	
Operation temperature (Ambient)	T_{opa}	-	0 +50	Degree	Note 4 】

Note 1] CK, R0 ~ R7, G0 ~ G7, B0 ~ B7, Hsync, Vsync, R/L, U/D

[Note 2] V_{ON} , PWM_{SEL} , V_{BRT2,3}

Note 3 V_{BRT1}

Note 4] Humidity 95%RH Max.(Ta 40 degree)

Maximum wet-bulb temperature at 39 or less.(Ta>40) No condensation.

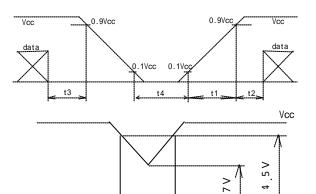
- 6. Electrical Characteristics
 - 6-1. Control circuit driving

1. Control circuit driving Ta=25 de											
Para	ameter	Symbol	Min.	Тур.	Max.	Uniit	Remark				
+5V supply	Supply voltage	Vcc	+4.5	+5.0	+5.5	V	Note 1				
voltage	Current dissipation	lcc	-	460	800	mA	Note 2				
Permissive inp	out ripple voltage	V _{RP}	-	-	100	mVP-P	Vcc=+5.0V				
Input Lo	ow voltage	V _{IL}	GND	-	0.9	V	Note 3]				
Input Hi	gh voltage	V _{IH}	3.0	-	3.6	V	Note 51				
Input leak	I _{OL1}	-	-	1.0	μA	V ₁ =0V Note 3 】					
Input leak o	current (High)	I _{OH1}	-	-	1.0	μA	V ₁ =Vcc Note 3 】				

Note 1]

1) Input voltage sequences

- 0 < t1 10ms, 0 < t2 10ms
- 0 < t3 t4 1s, 1s



td

2) Dip conditions for supply voltage

Vcc < 4.5Va) 2.7V

> 10ms td

b) Vcc < 2.7V

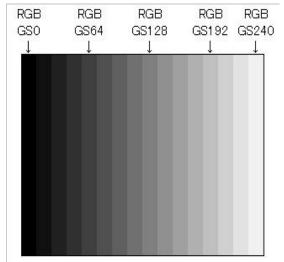
Condition of Dip conditions for supply voltage

is based on input voltage sequence.

Note 2 Typical current situation : 16 gray-bar pattern(Vcc=+5.0V)

The explanation of RGB gray scale see section

16.



2

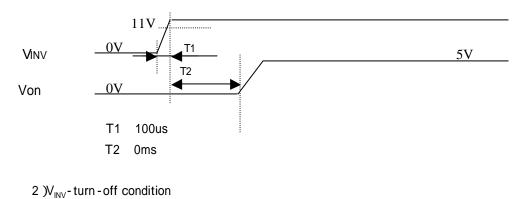
Note 3 JCK,R0 ~ R7,G0 ~ G7,B0 ~ B7,Hsync,Vsync, L/R, U/D

6-2. Inver driving for back light

The back light system is under-lighting type with 6 CCFTs (Cold Cathode Fluorescent Tube)

								Ta=25
	Par	ameter	Symbol	Min.	Тур.	Max.	Unit	Remark
	9	Supply voltage	V _{INV}	11.0	12.0	13.0	V	Note 1
+ 12V	Cu	rrent dissipation	I _{INV}	-	4.6	5.5	А	Vinv=+12V Brightness =MAX V _{ON} =+5V
	Lamp	Current	I	-	6.0	6.5	mArms	Vinv=+12V
								Brightness =MAX
								V _{ON} =+5V
Pern	missive in	out ripple voltage	VINVRP	-	-	200	mVP-P	V _{INV} =+12V
Brightnes	s control	Input voltage (Low)	V _{ONL1}	0	-	0.5	V	Note 2 】
analogu	e input	Input voltage (High)	V _{ONH1}	3.3	-	5.0	V	impedance =110k
Brightnes	s control	Input voltage (Low)	V _{ONL2}	0	-	0.5	V	
PWM puls	se inout	Input voltage (High)	V _{ONH2}	(3.3)	-	5.0	V	
(V _{BF}	_{RT2})		1/B _I	100	-	350	Hz	
Brightnes	rightness control Max duty(100%)			0		0.3	V	Note 3
(V _{BF}	_{RT3})	Changeable Voltage		0.7		5.0	V	Impedance =335k

Note $1 1 V_{INV}$ -turn-on condition



VINV 5V Von T3 0V

T3 0ms

Note 2 Jimpedance V_{ON}:100k ,PWM_{SEL}:1.1M

Note 3]Refrain from using the device under the condition $V_{BRT3}=0.3$ 0.7 (V) because of the possibility of flicker on display. In case of $V_{BRT} > 5.0$ V, the protective circuit may stop driving the inverter.

7. Timing characteristics of input signals

Timing diagrams of input signal rare shown in Fig.3

7-1. Timing ch aracteristics

Param	eter	Symbol	Min.	Тур.	Max.	Unit	Remark
	Frequency	1/Tc	-	25.17	30.0	MHz	Note 1
Clock	High time	Tch	5	-	-	ns	
	Low time	Tc1	5	-	-	ns	
Data	Set up time	Tds	5	-	-	ns	
Data	Hold time	Tdh	5	-	-	ns	
	Cycle	TH	-	31.78	-	μs	Note 2
Horizontal synchronized	Cycle		650	800	1620	Clock	
signal	Pulse width	THp	2	-	10	Clock	
	First data timing	THe	10	10	10	Clock	
Vertical synchronized	Cycle	ΤV	492	525	1000	Line	Note 2]
signal	Pulse width	TVp	2	-	12	Line	Note 21
Horizontal di	isplay area	THd	640	640	640	Clock	
Vertical dis	play area	TVd	480	480	480	Line	
Hsync-Clock ph	ase difference	THc	10	-	Tc-10	ns	
Hsync-Vsync ph	ase difference	TVh	0	-	TH-THp	Clock	

Note 1] In case of lower frequency, the deterioration of display quality, flicker, and etc, may occur.

Note 2] Horizontal cycle need not be stable during vertical blanking period. In addition, it is acceptable, if horizontal cycle is within the specifications against other factors, e.g. clock frequecies, 1H clock time (min., max.) and etc.. But, please note that the image quality may deteriorate at certain frequencies.

7-2. Vertical display position

The vertictal display position is determined by the falling edge of Vsync signal .

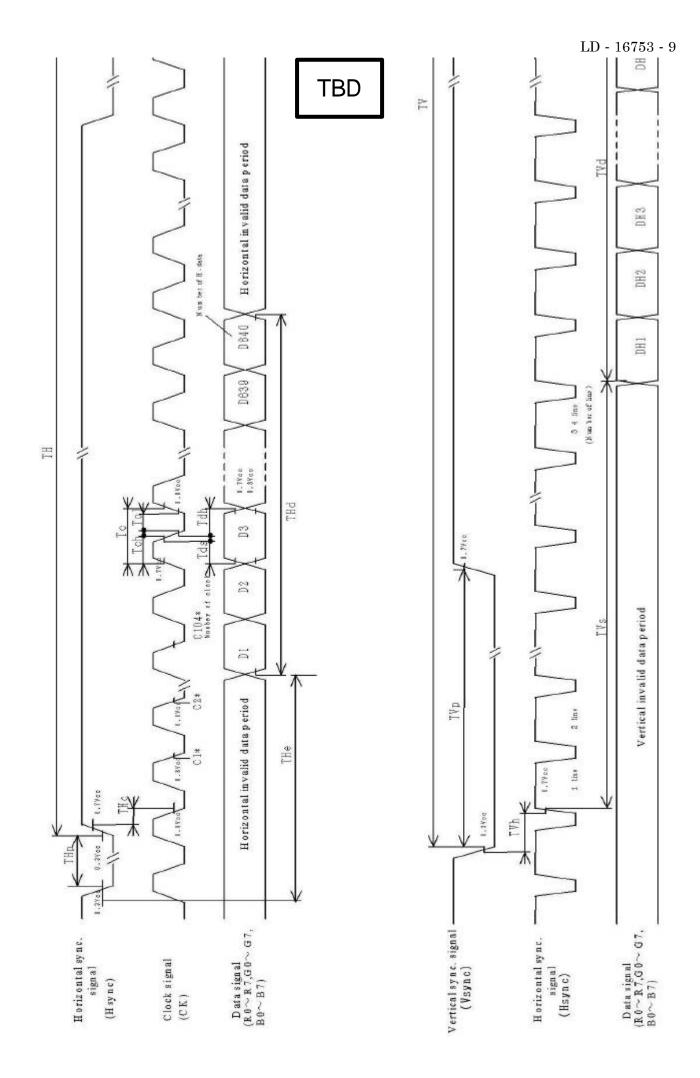
Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Vertical data start position	TVs	12	12	12	Line	

7-3. Input data signal and display position on the screen

\ UP

Display position of input data(H, V)

D1,DH1	D2,DH1	D3,DH1		D640,DH1
D1,DH2	D2,DH2			
D1,DH3				
			B G R	
D1,DH480				D640,DH48



8. Input Signal, Basic Display Colors and Gray Scale of Each Color

	Color &	-						Olay				Da	ata s	ignal												
	Gray scale	Gray Scale	R0	R1	R2	R3	R4	R5	R6	R7	G0			-	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7
	Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1
ធ	Green	-	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic Color	Cyan	-	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1
Colo	Red	-	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
or	Magenta	-	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1
	Yellow	-	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	-	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1
Gra	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Red	仓	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
cale	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
of F	仓	\checkmark					\mathbf{b}							``	r							``	r			
Red	Û	\checkmark												``	V							``	r			
	Brighter	GS250	0	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Û	GS251	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS252	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gr	仓	GS1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ay S	Darker	GS2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
cale	仓	\checkmark					arepsilon							``	r							`	r			
Gray Scale of Green	Û	\checkmark												``	r							``	r			
Gree	Brighter	GS250	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0
'n	Û	GS251	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0
	Green	GS252	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
G	仓	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
ray	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
Gray Scale of Blue	Û	\checkmark					V							`	r							``	V			
e of	Û	\checkmark					\mathbf{b}							``	V							``	V			
Blu	Brighter	GS250	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1
æ	Û	GS251	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1
	Blue	GS252	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1

0 :Low level voltage, 1 :High level voltage,

Each basic color can be displayed in 253 gray scales with 8 bit data signals. According to the combination of total 24 bit data signals, the 16-million-color display can be achieved on the screen. (X: don 't care)

9. Optical characteristics

Ta=25 degree, Vcc=+5V , V_{INV} = + 12V

Para	meter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Viewing angle	Horizontal	21, 22	CR 10	80	85	-	Deg.	Note1,4 】
range	Vertical	11 12		80	85	-	Deg.	Brightness=MAX
Contra	Contrast ratio		Best Viewing Angle	450	550	-		Note2,4 】 Brightness=MAX
Respor	nse time	r+ d	=0 deg.	-	25	55	ms	Note3,4,5 T Brightness=MAX
Chromatic	ity of white	Х		0.242	0.272	0.302	-	Note 4
		Y		0.248	0.278	0.308	-	Brightness=MAX
Luminance of white		YL1		400	500	-	cd/m ²	Note 4 Brightness=MAX
Luminance	Luminance uniformity			-	-	1.25		Note 6 Brightness=MAX

*The measurement shall be executed 30 minutes after lighting at rating.

The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig.4 below.

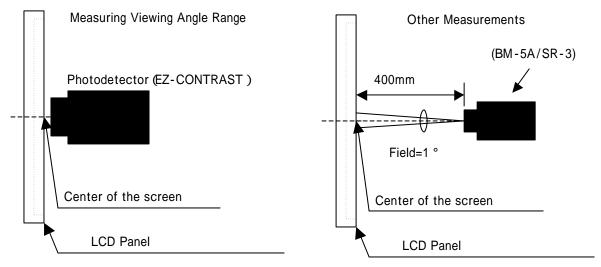
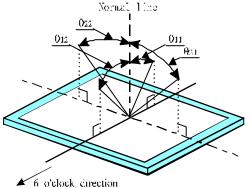


Fig.4 Optical characteristics measurement method

Note 1 Definitions of viewing angle range :



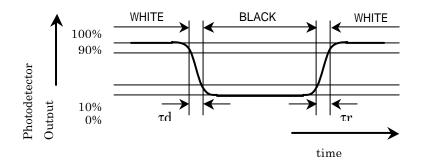
Note 2 Definition of contrast ratio :

The contrast ratio is defined as the following.

Contrast Ratio (CR) = Luminace(brightness) with all pixels white Luminace(brightness) with all pixels black

Note 3 Definition of response time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



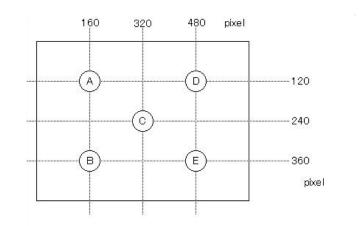
Note 4 This shall be measured at center of the screen.

Note 5 Typical Value(15ms) is measured under default condition.(60Hz)

Note 6 Definition of white uniformity ;

White uniformity is defined as the following with five measurements.(A ~ E)

W = <u>maximum Luminance of five points(brightness)</u> minimum Luminace of five points(brightness)



10. Display Quantity

The display quality of the color TFT-LCD module shall be in compliance with the incoming inspection Standard.

11.Warning

The module includes the inverter circuit, which generates high voltage. Do not touch the inverter cover DSand CCFT lamp terminals when inverter is turning on. Please alert 'Don 't touch it "if someone may touch.

12 Handling Precautions of the module

- a) Be sure to turn off the power supply when inserting disconnecting the cable.
- b) This product is using the parts(inverter, CCFT etc) which generate the high voltage. Therefore, during operating, please don't touch these parts.
- c) Brightness control voltage is switched for "ON" and "OFF", as shown in Fig.5. Voltage difference generated by this switching, VINV, may affect a sound output, etc. when the power supply is shared between the inverter and its surrounding circuit. So, separate the power supply of the inverter circuit with the one of its surrounding circuit.

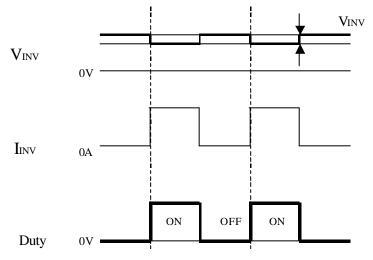


Fig.5 Brightness control voltage.

- d) Be sure to fix the module in the same plane so that the module can be installed without any extra stress such as warp or twist.
- e) Since the front polarizer is easily damaged, pay attention to treat it.
- f) Since long contact with water may cause discoloration or spots, wipe off water drop immediately.
- g) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- h) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
- i) Since CMOS LSI is used in this module, take care of static electricity and consider wearing the earth personnel when handling.
- j) Ground attachment to the LCD module should be considered, so that influences from EMI and outer noise is minimized.
- k) The module has some printed circuit boards (PCBs) on the back side, take care to keep them form any Stress or pressure when handling or installing the module; otherwise some of electronic parts on the PCBs may be damaged.
- I) Observe all other precautionary requirements in handling components.
- m) When some pressure is added onto the module from rear side constantly, it causes display non-uniformity issue, functional defect, etc.. So, please avoid such design

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- n) When handling LCD modules and assembling them into cabinets, please be noted that long-term storage in the environment of oxidization or deoxidization gas and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the LCD modules.
- Blow off dust with N2 blower for which static electricity preventive measure has been taken. Ionized air gun is recommended.
- p) Please connect from the product side to the inverter's power source ground line, as the PWB's ground for inverter is not connected to module's bezel.

12. Packing form

- a) Piling number of cartons: 3(maximum)
- b) Packing quantity in one carton : 5
- c) Carton size : 537mm(W) × 400mm(D) × 528mm(H)
- d) Total mass of one carton filled with full modules : 18.6Kg

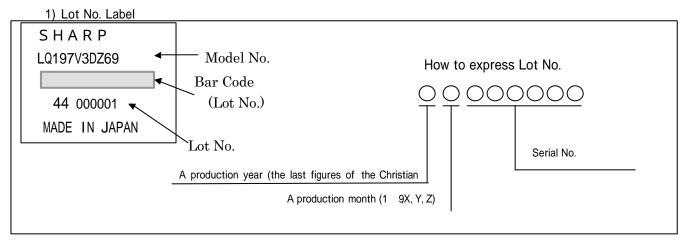
No	Test item	Conditions
1	High temperature storage test	Ta = 60 240h
2	Low temperature storage test	Ta = -25 240h
3	High temperature	Ta = 40 ; 95%RH 240h
	& high humidity operation test	(No condensation)
4	High temperature operation test	Ta = 50 240h
		(The panel temp. must be less than 60)
5	Low temperature operation test	Ta = 0 240H
6	Vibration test	Waveform : Sine wave
	(non- operating)	Frequency : $10 \sim 57 Hz/Vibration width$ (one side) : 0.075mm
		: 58 ~ 500Hz/Gravity : 9.8m/s ²
		Sweep time : 11minutes
		Test period : 3 hours
		(1 hour for each direction of X,Y,Z)
7	Shock test	Max. gravity : 490m/s ²
	(non- operating)	Pulse width : 11ms, sine wave
		Direction : $\pm X$, $\pm Y$, $\pm Z$,
		once for each direction.
8	Thermal shock test	Ta=-25 ~ 60 ; 5 cycles
	(Storage)	Test period : 10 hours (1 hour for each temperature)
9	Altitude	Ta=50 ,70kPa,3,048m(10,000ft), t=24h (Operating)
		Ta=70 ,12kPa,15,240m(50,000ft), t=24h (Storage)

Result evaluation criteria

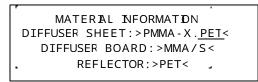
Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

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14. Others

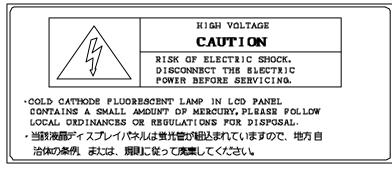


- 2) Adjusting volume have been set optimally before shipment, so do not change any adjusted value. If adjusted value is changed, the specification may not be satisfied.
- 3) Disassembling the module can cause permanent damage and should be strictly avoided.
- 4) Please be careful since image retention may occur when a fixed pattern is displayed for a long time.
- 5) Turn off the inverter circuit for back light before turning off the power source for the controller.
- 6) Rust is out of considerations.
- 7) Regulation on usage of destructible chemical substances for the Ozone layer
 - Regulated substances : CFCS, Quadru Carbon Chloride, 1,1,1-Tri chloro-ethylene (MethylChloroform)
 - a) above mentioned substances are not used in the product, and/or assembled unit and parts of this product
 - b) above mentioned substances are not used in the process of manufacturing the product and/or assembled unit and parts of this product.
- 8) Label of using material information
 - It is displaying the material of the optical parts with the label in the module back.



9) Cold cathode fluorescent lamp in LCD PANEL contains a small amount of mercury.

Please follow local ordinances or regulations for disposal.



10) When any question or issue occurs, it shall be solved by mutual discussion.