PROGRAMMABLE PRECISION REFERENCE

DESCRIPTION

The LR431ALT1G is a three-terminal adjustable regulator with a guaranteed thermal stability over applicable temperature ranges. The output voltage may be set to any value between Vref(approximately 2.5V) and 36V with two external resistors. It provides very wide applications, including shunt regulator, series regulator, switching regulator, voltage reference and others.

LR431ALT1G

SOT-23

SOT-23 1: Ref; 2: Cathode; 3: Anode

FEATURES

Programmable output Voltage to 36V.

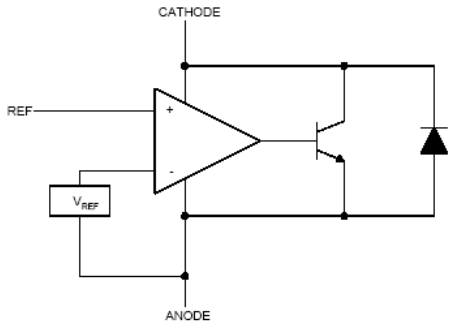
Low dynamic output impedance 0.2Ω

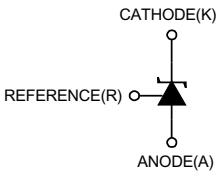
Sink current capability of 1 to 100mA.

Equivalent full-range temperature coefficient of 50ppm/°C typical for operation over full rated operating temperature range.

Pb-Free package is available

BLOCK DIAGRAM





LR431ALT1G

ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

PARAMETER	SYMBOL	VALUE	UNIT
Cathode Voltage	V_{KA}	36	V
Cathode Current Range(Continuous)	I_{KA}	-100 ~ +150	mA
Reference Input Current Range	Iref	-0.05 ~ +10	mA
Operating Junction Temperature	Tj	150	°C
Operating Ambient Temperature	Topr	-40 ~ + 85	°C
Storage Temperature Range	Tstg	-65 ~ +150	°C

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Cathode Voltage	V_{KA}	V_{REF}		36	V
Cathode Current	I_{KA}	1		100	mA

ELECTRICAL CHARACTERISTICS (Ta=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Reference Input Voltage *	Vref	$V_{KA}=V_{REF}, I_{KA}=10mA$			2.50 2.495		V
Deviation of Reference Input Voltage Over Ambient Temperature Range	ΔVref	$V_{KA}=V_{REF}, I_{KA}=10mA$			4.5	17	mV
Ratio of Change in Reference Input Voltage to the Change in Cathode Voltage	$\Delta V_{ref}/\Delta V_{KA}$	$I_{KA}=10mA$	$\Delta V_{KA}=10V \sim V_{REF}$ $\Delta V_{KA}=36V \sim 10V$		-1.0 -0.5	-2.7 -2.0	mV/V
Reference Input Current	Iref	$I_{KA}=10mA, R1=10k\Omega, R2=\infty$			1.5	4	μA
Deviation of Reference Input Current Over Ambient Temperature Range	ΔIref	$_{KA}=10mA, R1=10k\Omega, R2=\infty$			0.4	1.2	μA
Minimum Cathode Current for Regulation	I_{KA}(min)	$V_{KA}=V_{REF}$			0.45	1.0	mA
Off-State Cathode Current	I_{KA}(OFF)	$V_{KA}=36V, V_{REF}=0$			0.05	1.0	μA
Dynamic Impedance	Z_{KA}	$V_{KA}=V_{REF}$, I_{KA}=1 to 100mA f≤1.0kHz			0.15	0.5	Ω

* CLASSIFICATION OF V_{ref} AND PACKAGE

Type	Rank	Range(V)	Marking	Package	Topr
LR431ALT1G	0.5%	2.487~2.512	RA	SOT-23	-40 ~ + 85 °C

LR431ALT1G

TYPICAL PERFORMANCE CHARACTERISTICS

Fig 1 Cathode Current Vs Cathode Voltage

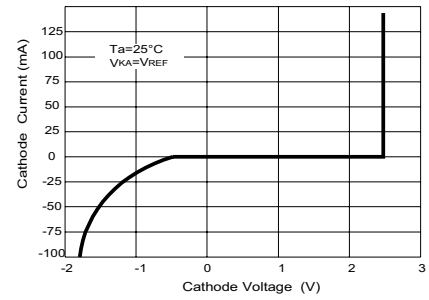


Fig 2 Cathode Current Vs Cathode Voltage

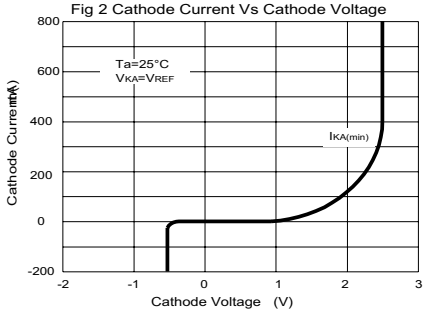


Fig 3 Change in Reference Input Voltage Vs Cathode voltage

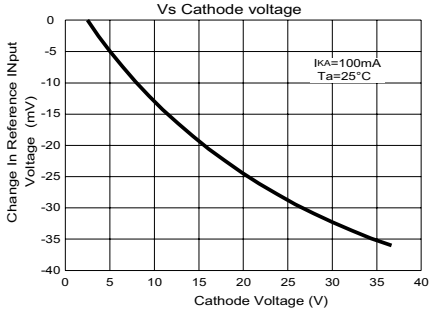


Fig 4 Pulse Response

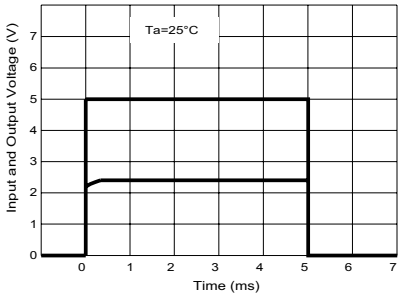


Fig 5 Dynamic Impedance Vs Frequency

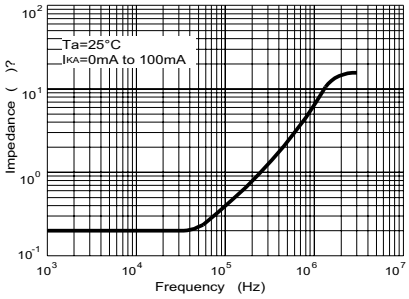
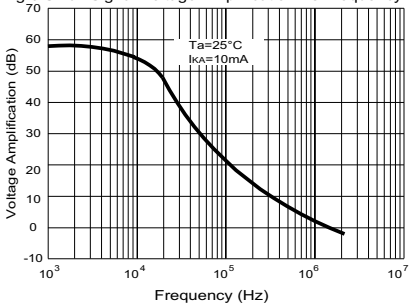
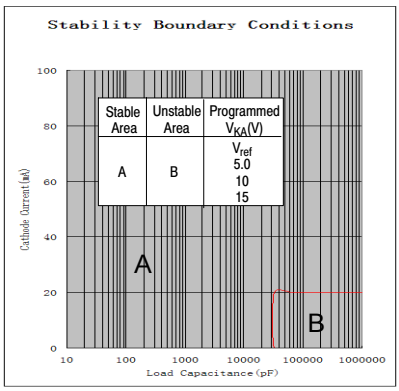
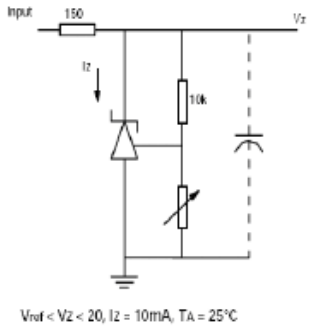


Fig 6 Small Signal Voltage Amplification Vs Frequency



Stability Boundary Conditions





Vref < Vz < 20, Iz = 10mA, TA = 25°C

Test Circuit for Stability Boundary Conditions

LR431ALT1G

TEST CIRCUIT

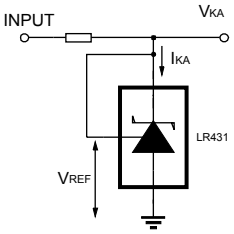


Fig 7 Test Circuit For $V_{KA}=V_{REF}$

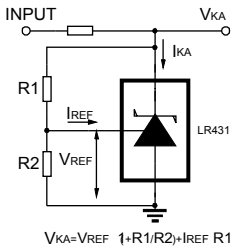


Fig 8 Test Circuit for $V_{KA} >= V_{REF}$

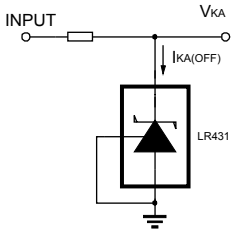


Fig 9 Test Circuit For $I_{KA(OFF)}$

APPLICATION CIRCUIT

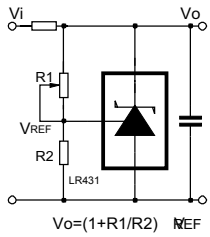


Fig 10 Shutdown Regulator

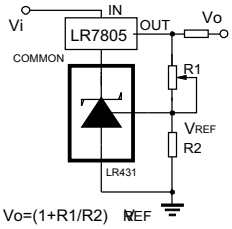


Fig 11 Output Control of a Three -Terminal Fixed Regulator

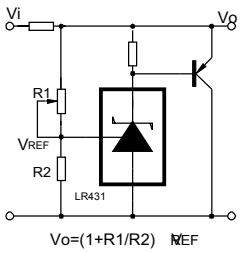


Fig 12 Higher-current Shunt Regulator

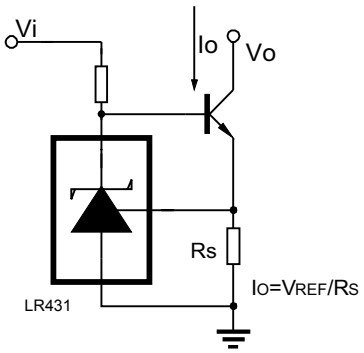


Fig 13 Constant-current Sink

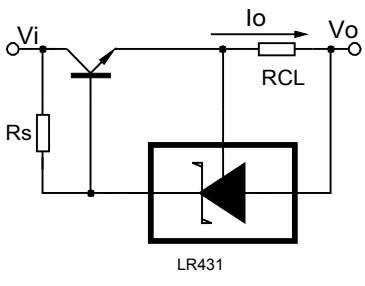
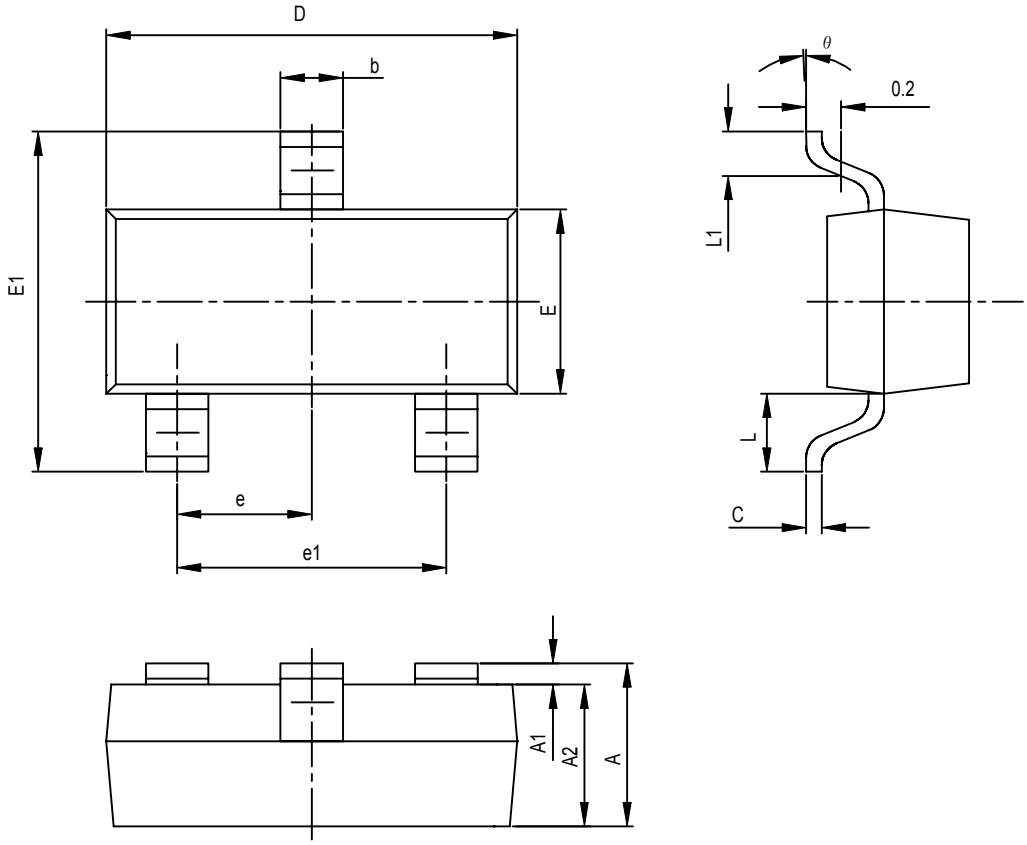


Fig 14 Current Limiting or Current Source

LR431ALT1G

SOT-23 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.100	0.110	0.118
E	1.200	1.610	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950TPY		0.037TPY	
e1	1.800	2.000	0.071	0.079
L	0.550REF		0.022REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°