**Technical Document** 

## **LCD Specification**

**LCD Group** 

## LS013B4DN01 LCD Module

#### Preliminary Product Specification April 2009

96 × 96 Silver Reflective PNLC Memory LCD Module featuring Zebra (elastomeric) connector, 50% reflectivity with 10:1 contrast.





| No.  | LCY-12T09302A                |
|------|------------------------------|
| Date | April. 24 <sup>th</sup> 2009 |

# TECHNICAL LITERATURE For TFT-LCD Module

# Model No. LS013B4DN01

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Mobile Liquid Crystal Display Group SHARP CORPORATION



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#### [For handling and system design]

(1) It is recommended to handle LCD panel with carefully, since this LCD panel is made of thin glass, dropping the module or banging it against hard objects may cause cracks or fragmentation.

(2) Do not scratch the surface of the UV protection film as it is easily damaged.

(3) Water droplets on the UV protection film must be wiped off immediately as they may cause color changes, or other defects if remained for a long time.

(4) If the cleaning of the surface of the LCD panel is necessary, wipe it swiftly with cotton or other soft cloth.

Do not use organic solvents as it damages the UV protection film.

(5) Do not hold or touch the part of single glass of LCD panel which has electric connection area, as electric circuits in LCD panel may be damaged.

(6) Hold LCD with very carefully when placing LCD module into the system housing.

(7) LCD panel is susceptible to mechanical stress and even the slightest stress will cause a color change in background. So make sure the LCD panel is placed on flat plane without any continuous twisting, bending or pushing stress.

(8) When carrying the LCD module, place it on the tray to protect from mechanical damage. It is recommended to use the conductive trays to protect LCD panel from electrostatic discharge. Certain materials such as epoxy resin (amine's hardener) or silicone adhesive agent (de-alcohol or de-oxym) emits gas to which the UV protection film reacts (color change).

Check carefully that gas from materials used in system housing or packaging do not hart the UV protection film.

(9)Do not apply excessive stress or pressure to LCD module. Do not to use chloroprene rubber as it may affect on the reliability of the electrical interconnection.

(10) As this LCD module contains components sensitive to electrostatic discharge, more than 200V, be sure to follow the instructions in below.

① Operators

Operators must wear anti-static wears to prevent electrostatic charge up to and discharge from human body.

2 Equipment and containers

Process equipment such as conveyer, soldering iron, working bench and containers may possibly generate electrostatic charge up and discharge. Equipment must be grounded through 100Mohms resistance. Use ion blower.

3 Floor

Floor is an important part to leak static electricity which is generated from human body or equipment.

There is a possibility that the static electricity is charged to them without leakage in case of insulating floor, so the electrostatic earth should be lower than the  $1 \times 10^8 \Omega$ .

(4)Humidity

Proper humidity of working room may reduce the risk of electrostatic charge up and discharge. Humidity should be kept over 50% all the time.

(5)Transportation/storage

Storage materials must be anti-static to prevent causing electrostatic discharge.

<sup>6</sup>Others

(11) Protective film is attached on the surface of the UV protection film on LCD panel to prevent scratches or other damages. When removing this protective film, remove it slowly under proper anti-ESD control such as ion blower.



### [For operating LCD module]

(1) Do not operate the LCD panel under outside of electrical specification. Otherwise LCD panel may be damaged.

(2) Do not operate the LCD panel under outside of specified driving timing chart. Otherwise LCD panel may not have proper picture quality.

(3) A still image should be displayed less than two hours, if it is necessary to display still image longer than two hour, display image data must be refreshed in order to avoid sticking image on LCD panel.

### [Precautions for Storage]

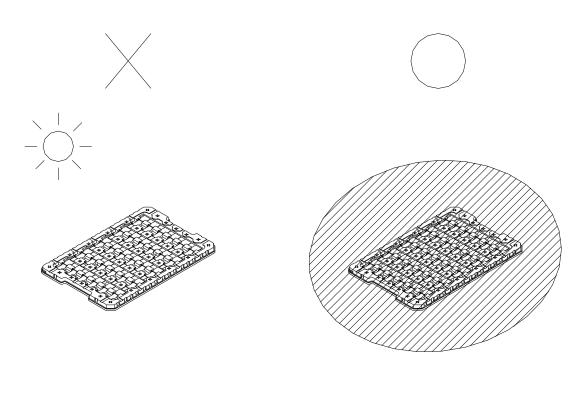
(1) Do not expose the LCD module to direct sunlight or strong ultraviolet light for long periods. Store in a dark place.

(2) The liquid crystal material will solidify if stored below the rated storage temperature and will become an isotropic liquid if stored above the rated storage temperature, and may not retain its original properties. Only store the module at normal temperature and humidity  $(25\pm5^{\circ}C,60\pm10\%RH)$  in order to avoid exposing the UV protection film to chronic humidity.

#### (3) Keeping Method

a. Don't keeping under the direct sunlight.

b. Keeping in the tray under the dark place.





## [Other Notice]

(1) Do not operate or store the LCD module under outside of specified environmental conditions.

(2) As electrical impedance of power supply lines (VDD-VSS) are low when LCD module is working, place the de-coupling capacitor near by LCD module as close as possible.

(3) Although the LCD panel has the UV protection film on the LCD panel surface, just in case, do not expose the LCD module to direct sunlight or strong ultraviolet light for long periods.

(4) Do not disassemble the LCD module as it may cause permanent damage.

(5) Since the liquid crystal is injurious, do not put it into the eyes

 $or mouth. \quad When \ liquid\ crystal\ sticks\ to\ hands,\ feet\ or\ clothes,\ wash\ it\ out\ immediately\ with\ soap.$ 

(6) This LCD module does not contain nor use any ODS (1,1,1-Trichloroethane, CCL4) in all materials used, in all production processes.

(7) Observe all other precautionary requirements in handling general electronic components.

## [Precautions for Discarding Liquid Crystal Modules]

LCD panel: Dispose of as glass waste. This LCD module contains no harmful substances. The liquid crystal panel contains no dangerous or harmful substances. The liquid crystal panel only contains an extremely small amount of liquid crystal (approx.100mg) and therefore it will not leak even if the panel should break.

-Its median lethal dose (LD50) is greater than 2,000 mg/kg and a mutagenetic (Aims test: negative) material is employed.



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#### 1.Outline

This TFT-LCD module is a reflective active matrix memory liquid crystal display module

with CG silicone thin film transistor.

Module outline is indicated in Table 8-1.

#### 2.Characteristics

- Reflective panel of white and black with aspect ratio of 1:1
- 1.35" screen has 96x 96 resolution.( 9216 pixels stripe array)
- Display control by serial data signal communication.
- Arbitrary line data renewable.
- 1bit internal memory for data storage within the panel.
- Thin, light-weight and compact module with monolithic technology.
- Super low power consumption TFT panel.
- ${\boldsymbol{\cdot}}$  Zebra connecter

#### 3. Mechanical Specification

#### Table 3-1

| Item              | Specification                  | unit            |
|-------------------|--------------------------------|-----------------|
| Screen size       | 1.35                           | Inch            |
| Viewing Area      | 24 (H) × 24 (V)                | mm              |
| Dot configuration | 96 (H) × 96 (V)                | Dot             |
| Dot pitch         | $0.252 (H) \times 0.252 (V)$   | mm              |
| Pixel Array       | Stripe Array                   | -               |
| Outline Dimension | 28.2 (W) × 32.34 (H) × 1.4 (D) | mm              |
| Mass              | 3.6 (TYP)                      | g               |
| Surface Hardness  | ЗН                             | Pencil hardness |

(Note) Detail dimension and tolerance are shown in fig. 8-1



| Table4-1 |          |       |   |            |
|----------|----------|-------|---|------------|
| Terminal | Symbol   | I/0   | Function  | Remark     |
| 1        | TEST1    | -     | Test terminal                                   | [Remark4-1 |
| 2        | TEST2    | -     | Test terminal                                   | [Remark4-1 |
| 3        | SCLK     | INPUT | Serial clock signal                             |            |
| 4        | SI       | INPUT | Serial Data input signal                        |            |
| 5        | SCS      | INPUT | Chip select signal                              |            |
| 6        | EXTCOMIN | INPUT | External COM inversion signal input (H: enable) |            |
| 7        | DISP     | INPUT | Display ON/OFF signal                           | [Remark4-3 |
| 8        | VDDA     | POWER | Power supply (Analog)                           |            |
| 9        | VDD      | POWER | Power supply (Digital)                          |            |
| 10       | EXTMODE  | INPUT | COM inversion select terminal                   | [Remark4-2 |
| 11       | VSS      | POWER | GND(Digital)                                    |            |
| 12       | VSSA     | POWER | GND(Analog)                                     |            |
| 13       | TEST3    | -     | Test terminal                                   | Remark4-   |
| 14       | TEST4    | -     | Test terminal                                   | [Remark4-1 |

[Remark4-1] TEST terminal: OPEN these test terminals.

[Remark4-2] When it's "H", EXTCOMIN signal is enable. And when it's "L", serial input flag is enable.

"H"mode; connect the EXTMODE toVDD, "L" mode; connect the EXTMODE to VSS.

[Remark4-3] The display ON/OFF signal is only for display. Data in the memory will be saved at the time of ON/OFF. When it's "H", data in the memory will display, when it's "L", white color will diaplay and data in the memory will be saved.

4-2) Recommended circuit;

#### COM Signal Serial Input EXTMODE=L

| 0    | 4   | TROTA    |
|------|-----|----------|
| Open | 1   | TEST1    |
| Open | 2   | TEST2    |
|      | 3   | SCLK     |
|      | 4   | SI       |
|      | 5   | SCS      |
|      | 6   | EXTCOMIN |
|      | 7   | DISP     |
|      | - 8 | VDDA     |
|      | 9   | VDD      |
|      | 10  | EXTMODE  |
|      | 11  | VSS      |
|      | 12  | VSSA     |
| Open | 13  | TEST3    |
| Open | 14  | TEST4    |

#### External COM Signal Input EXTMODE=H

| Open | 1  | TEST1    |
|------|----|----------|
| Open | 2  | TEST2    |
|      | 3  | SCLK     |
|      | 4  | SI       |
|      | 5  | SCS      |
|      | 6  | EXTCOMIN |
|      | 7  | DISP     |
|      | 8  | VDDA     |
| -    | 9  | VDD      |
|      | 10 | EXTMODE  |
|      | 11 | VSS      |
|      | 12 | VSSA     |
| Open | 13 | TEST3    |
| Open | 14 | TEST4    |
| -    |    |          |



## 5. Absolute maximum rating

| Table5-1                 | (GND=0V)         |        |      |      |      |               |
|--------------------------|------------------|--------|------|------|------|---------------|
|                          | Item             | Symbol | MIN. | MAX. | Unit | Remark        |
| Power                    | Analog           | VDDA   | -0.3 | +5.8 | V    |               |
| supply<br>voltage        | Logic            | VDD    | -0.3 | +5.8 | V    | [Remark5-1]   |
| Input sign               | al voltage(high) |        |      | VDD  | V    | [Remark5-2]   |
| Input sign               | al voltage(low)  |        | -0.3 |      | V    |               |
| Strage Ter               | mperature        | Tstg   | -30  | +80  | °C   | [Remark5-3,4] |
| Operation<br>panel surfa | Temperature (at  | Topr1  | -20  | +70  | °C   | [Remark5-5]   |

[Remark5-1] Applies to EXTMODE.

[Remark5-2] Applies to SCLK, SI, SCS, DISP, EXTCOMIN.

[Remark5-3] Do not exceed this temperature in any parts of module.

[Remark5-4] Maximum wet bulb temperature is 57°C or lower. No condensation is allowed.

Cndensation will cause electeical leak and may cause the module to not meet this specification.

[Remark5-5] Operating temperature is the temperature that guarantees only the operation.

For contrast, response time, and other display quality determination, use  $Ta = +25^{\circ}C$ .

#### 6. Electrical characteristics

6-1) TFT LCD panel Drive

Table6-1 Recommended operating Condition

#### $VSS(GND)=0V, Ta=+25^{\circ}C$

|                      | 1      | 0      |       |       | •        |      |                      |
|----------------------|--------|--------|-------|-------|----------|------|----------------------|
| Item                 |        | symbol | MIN.  | TYP.  | MAX.     | Unit | Remark               |
| Power supply         | Analog | VDDA   | +4.8  | +5.0  | +5.5     | V    |                      |
|                      | Logic  | VDD    | +4.8  | +5.0  | +5.5     | V    | [Remark6-1]          |
| Input signal voltage | Hi     | VIH    | +2.70 | +3.00 | ₩VDD     | V    | [Remark6-2]          |
|                      | Lo     | VIL    | VSS   | VSS   | VSS+0.15 | V    |                      |
| Input leak current   | Hi     | IOH    | -     | -     | TBD      | uA   | [Remark6-2]<br>VI=3V |
|                      | Lo     | IOL    | -     | -     | TBD      | uA   | [Remark6-2]<br>VI=0V |

X Lower 3V is recommended, and it's lower than the VDD voltage.

[Remark6-1] Applies to EXTMODE="H"

[Remark6-2] Applies to SCLK, SI, SCS, DISP, EXTCOMIN.

|  | _           |          |         |         |             | LCY        | Z-12T093         | 602A    | LS        | 013B       | 4DN0  | 1   | 9 |
|--|-------------|----------|---------|---------|-------------|------------|------------------|---------|-----------|------------|-------|-----|---|
| 6-2) Power supply                        | sequence    | e        |         |         |             | 1          |                  |         |           |            |       |     |   |
| [On sequence]                            | * All o     | n sta    | rt up   | Syste   | m           |            |                  |         |           |            | 7     |     |   |
| ①5V start up                             |             |          | •       | ·       |             |            |                  |         |           |            |       |     |   |
| 2 Initializes men                        | nory data   | withi    | n pixe  | 1       |             |            |                  | T2      | more tha  | n 1V       |       |     |   |
| Initialize with I                        | D2 "all cl  | ear fla  | ag" or  | displa  | y all w     | hite.      |                  |         |           |            |       |     |   |
| 3 To cancel late                         | h initializ | ation f  | for TC  | COM.    |             |            |                  | T3      | more tha  | n 30us     |       |     |   |
| The period of                            | canceling   | g CON    | M late  | h circ  | uit for     | initializa | ation by us      | ing DIS | P signal. |            |       |     |   |
| (4) TCOM polarit                         |             |          |         |         |             |            |                  |         | more tha  | n 30us     |       |     |   |
| The period of                            | initia liza | tion fo  | r TCO   | OM po   | olarity l   | by EXT     | COMIN s          | ignal.  |           |            |       |     |   |
|  | 1           |          |         |         |             |            |                  |         |           |            |       |     |   |
| [Normal operation                        |             | _        |         |         |             |            |                  |         |           |            |       |     |   |
| Duration of norm                         | al driving  | g.       |         |         |             |            |                  |         |           |            |       |     |   |
| [Off sequence]                           |             |          |         |         |             |            |                  |         |           |            |       |     |   |
| 5 Initializes men                        | orv data    | withi    | n nixe  | 1       |             |            |                  | Т5      | more tha  | n 1V       |       |     |   |
| 6 The period of                          | -           |          | -       |         | and VC      | COM        |                  |         | more that |            |       |     |   |
| 75V shut down                            |             |          | ,       | , .     |             |            |                  |         |           |            |       |     |   |
|  |             | 1        |         |         | 1           |            |                  |         | 1         |            |       |     |   |
|  |             |          | On Se   | equence |             |            | Normal operation | on      | (         | Off Sequer | nce   |     |   |
|  |             |          |         |         |             |            |                  |         |           |            |       |     |   |
|  |             | -        |         |         |             |            |                  | 1 1     |           |            |       |     |   |
|  |             | 1        | 2       |         | <b>4</b> ×1 |            |                  |         | 5         | 6          | 1     |     |   |
|  |             | 1)<br>T1 | 2<br>T2 | ③※1<br> | ④※1<br>     |            |                  |         | (5)<br>T5 | ⑥<br>      | ⑦<br> |     |   |
| VDD/VDDA (5V)                            | GND         | 1        |         |         |             |            |                  |         |           | I I        |       | GND |   |
|  | GND         | 1        |         |         |             |            |                  |         |           | I I        |       |     |   |
|  |             | 1        |         |         |             |            |                  |         |           | I I        |       | GND |   |
| DISP                                     |             | 1        |         |         |             |            | Normal oper      | ation   |           | I I        |       |     |   |
| DISP<br>EXTCOMIN                         | GND         | 1        |         |         |             |            | Normal oper      |         |           | I I        |       | GND |   |
| VDD/VDDA (5V)<br>DISP<br>EXTCOMIN<br>SCS | GND<br>GND  | 1        |         |         |             |            |                  | ation   |           | I I        |       | GND |   |

\*Detail description for signal timing is shown the digital timing chart and the AC timing chart.

 $\times 1$  The order of 3 and 4 can be set as being reversed, in this case, TCOM polarity inversion timing which can be controlled by EXTCOMIN doesn't work during DISP="L".

\*2 Setup value for initialization of data within pixel memory To refer the timing chart of all clear mode section 6-5-4.

[Remark] Precaustions at ehte time of power on and power off.

Remark 1)When power on , VDDand VDDA are same timem or VDD should be faster than the VDDA. Remark 2) When power off, VDD and VDDA are same time or VDDA shoud be faster than the VDD.

| Table6-3-1        | VDDA=+5.0V, $V$ | DD=+5.0V | / GND=0V | ∕. Ta=25°C |      |        |
|-------------------|-----------------|----------|----------|------------|------|--------|
| Item              | Symbol          | MIN      | ТҮР      | MAX        | Unit | Remark |
| Frame frequency   | fSCS            | 1        | -        | 60         | Hz   |        |
| Clock frequency   | fSCLK           |          | 1        | 2          | MHz  |        |
| Vertical Interval | tV              | 16.66    | -        | 1000       | ms   |        |
| COM Frequency     | fCOM            | 0.5      | -        | 30         | Hz   |        |

Table 6-3-2

VDDA=+5.0V, VDD=+5.0V, GND=0V, Ta=25°C

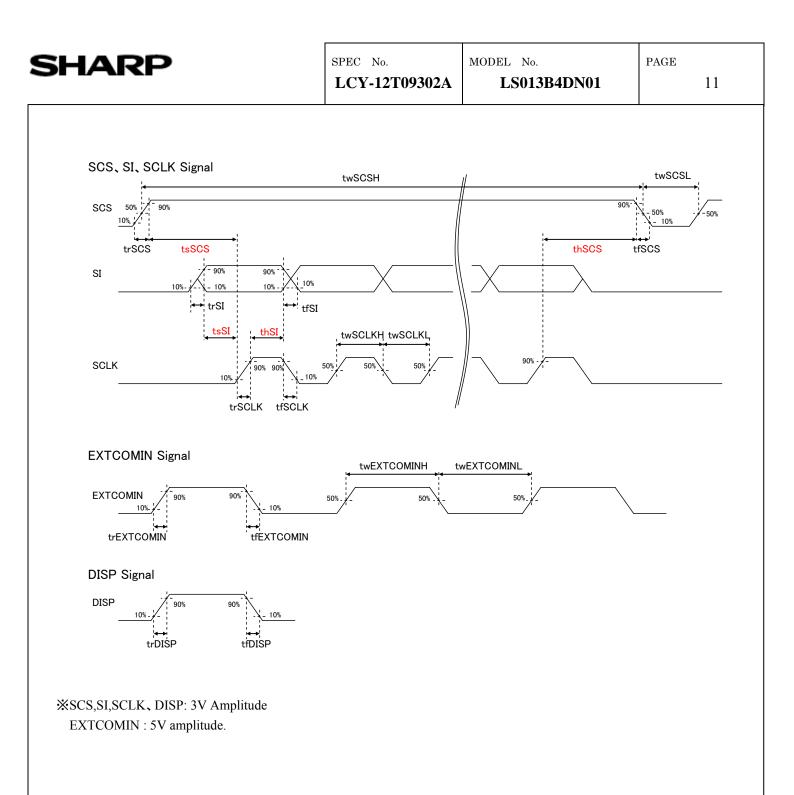
| Item                         | Symbol           | MIN   | ТҮР | MAX | Unit | Remark       |
|------------------------------|------------------|-------|-----|-----|------|--------------|
| SCS Rising time              | trSCS            | -     | -   | 50  | ns   |              |
| SCS Falling Time             | tfSCS            | -     | -   | 50  | ns   |              |
| SCS High duration            | twSCSH           | 68    | -   | -   | us   | [Remark 6-4] |
|                              |                  | 12    | -   | -   | us   | [Remark 6-5] |
| SCS Low duration             | twSCSL           | (1)   | -   | -   | us   |              |
| SCS set up time              | tsSCS            | (3)   | -   | -   | us   |              |
| SCS hold time                | thSCS            | (1)   | -   | -   | us   |              |
| SI frequency                 | fSI              | -     | 0.5 | 1   | MHz  |              |
| SI Rising time               | trSI             | -     | -   | 50  | ns   |              |
| SI Folling time              | tfSI             | -     | -   | 50  | ns   |              |
| SI set up time               | tsSI             | (120) | -   | -   | ns   |              |
| SI hold time                 | thSI             | (125) | -   | -   | ns   |              |
| SCLK Rising time             | trSCLK           | -     | -   | 50  | ns   |              |
| SCLK Folling time            | tfSCLK           | -     | -   | 50  | ns   |              |
| SCLK High duration           | twSCLKH          | 200   | 450 | -   | ns   |              |
| SCLK Low duration            | twSCLKL          | 200   | 450 | -   | ns   |              |
| EXTCOMIN signal freequency   | <b>fEXTCOMIN</b> |       | 1   | 60  | Hz   | [Remark6-6]  |
| EXTCOMIN signal rising time  | trEXTCOMIN       | -     | _   | 50  | ns   |              |
| EXTCOMIN signal folling time | tfEXTCOMIN       | -     | -   | 50  | ns   |              |
| EXTCOMIN signalHigh duration | thIEXTCOMIN      | 1     |     |     | us   |              |
| DISP Rising time             | trDISP           | -     | -   | 50  | ns   |              |
| DISP Folling time            | tfDISP           | _     | -   | 50  | ns   |              |

( ) is tentative valure.

[Remark6-4] Data renew mode.

[Remark6-5] Display mode.

[Remark6-6] Adjust EXTCOMIN frequency to be lower than the frame frequency.





#### 6-4) Power consumption (Average)

| Table6-4   |        |     |     |     | Ta=2 | 5°C         |
|------------|--------|-----|-----|-----|------|-------------|
| Item       | Symbol | MIN | ТҮР | MAX | unit | Remark      |
| Condition1 |        |     | 15  |     | uW   | [Remark6-6] |
| Condition2 |        |     | 30  |     | uW   | [Remark6-6] |

\* Condition1

Display mode (No display data update)

Common AC Driving with VDD=5V, VDDA=5V, fSCLK=1MHz, fSCS=1 Hz, Display pattern: Vertical stripe display \* Condition 2

Data Renew mode (With display data update: 1Hz)

Common AC Driving with VDD=5V, VDDA=5V, fSCLK=1MHz, fSCS=1 Hz, Display pattern: Vertical stripe display

[Remark6-6] This is the average power in normal condition, not the peak power for COM driving.

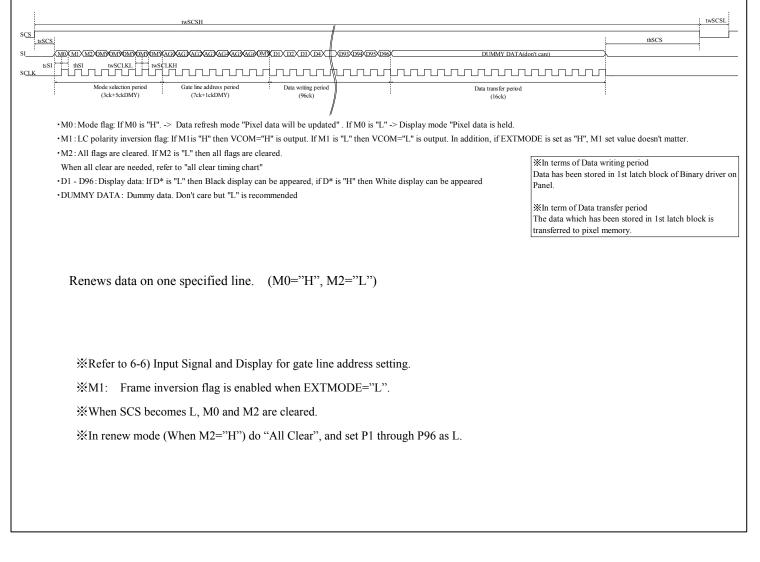
Allow margin in power supply. Use of capacitor is recommended for VDD+VDDA.

(If VDD and VDDA are separate, use of capacitor on each is recommended.)

#### 6-5) Input Signal Timing Chart

6-5-1 Data Renew Mode (1 line)

Renews data on one specified line. (M0="H", M2="L")



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|---|---|---|--------------------|
| 6-5-2 Data Renew Mode<br>Updates multiple line data. (M0="H", M2=   | -"L")   |   |                    |
| SCS   |   | Sfer period Data transfer (8ck(Dummy)+7ck(address)+ |                    |
| Image: Description of the sector of the s | A D94 X D95 X D96 X   | DUMMY NTA(don't care)                               |                    |
| <ul> <li>• M0 : Mode flag: If M0 is "H" then Data refresh m</li> <li>• M1 : LC polarity inversion flag: If M1 is "H" then<br/>In addition, if EXTMODE is set as "H", M1 set</li> <li>• M2 : All flags are cleared. If M2 is L then all flag<br/>When all clear are needed, refer to "all clear ti</li> <li>• D1 - D96 : Display data: If D* is "L" then Black of<br/>DUMMY DATA : Dummy data. Don't care but '</li> </ul>   | VCOM="H" is output. If M1 is<br>t value doesn't matter.<br>gs are cleared.<br>ming chart"<br>display can be appeared, if D* | s "L" then VCOM="L" is output.                      |                    |
| <ul> <li>※In terms of Data writing period</li> <li>Data has been stored in 1st latch block of Binary d</li> <li>※In term of Data transfer period</li> <li>As an example, the 1st latch block in binary driver for 2nd gateline"GL2" at same time.</li> </ul>  |   | ate line"GL1"output the data to GL1 a               | and store the data |
| <ul> <li>※Refer to 6-6) Input Signal and Display for gate 1</li> <li>※Input data continuously.</li> <li>※M1: Frame inversion flag is enabled when EX</li> <li>※When SCS becomes L, M0 and M2 are cleared.</li> </ul>  | XTMODE="L".   |   |                    |
| ₩In renew mode (When M2="H") do "All Clear"   | ', and set P1 through P96 as I  |   |                    |
|   |   |   |                    |

| SHARP   | SPEC No.   | MODEL No.                | PAGE |
|---|--|--------------------------|------|
|   | LCY-12T09302A  | LS013B4DN01              | 14   |
| 6-5-3 Display mode  |  |                          |      |
| Maintains data in the memory(Maitain the c                            | current display)(M0="L",                                       | M2="L")                  |      |
| twSCSH  |  | twSCSL                   | /    |
| SCS   | thSCS  |                          |      |
| SI  |  | M0 M1 M2 X DEMATY #TAdat |      |
| SCLK Mode selection Data transfer period                              |  |                          |      |
| period (More than 13ck)<br>(3ck)<br>• M0 : Mode flag: If M0 is "L" tl | hen display mode "Pixel data                                   | is held".                |      |
| If M0 is "H" t  | hen Data refresh mode "Pixel                                   | data will be updated.    |      |
| •M1:LC polarity inversion flag  | If M1 is "L" then VCOM="I                                      | L" is output.            |      |
| In addition, if E.<br>• M2 : All flags are cleared. If M              | XTMODE is set as "H", M1 s<br>12 is L then all flags are clear |                          |      |
| When all clear are needed, ref<br>•DUMMY DATA : Dummy da              | er to "all clear timing chart"                                 |                          |      |
|   | ita. Don't care but L is reco                                  | minended                 |      |
|   |  |                          |      |
|   |  |                          |      |
| <b>**</b> M1: Frame inversion flag is enabled when EXT                | MODE="L".  |                          |      |
| When SCS becomes L, M0 and M2 are cleared.                            |  |                          |      |
|   |  |                          |      |
|   |  |                          |      |
|   |  |                          |      |
|   |  |                          |      |
|   |  |                          |      |
|   |  |                          |      |
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|   |  |                          |      |
|   |  |                          |      |
|   |  |                          |      |
|   |  |                          |      |

| SHARP   | SPEC No.<br>LCY-12T09302A  | MODEL No.<br>LS013B4DN01                                     | PAGE 15 |
|---|--|--|---------|
| 6-5-4 All Clear mode<br>Clear data in the pixel and the memory data | and displays white color. (  | M0="L"、M2="H")   |         |
| SCS tsSCS<br>SI tsSCS tsSL tsSL tsSL tsSL tsSL tsSL tsSL tsS        |  |  |         |
| -   | "L" then display mode "Pixel<br>"H" then Data refresh mode "   |  |         |
| In addition<br>• M2 : All flags are cleared                         | n flag: If M1 is "H" then VCO<br>If M1 is "L" then VCO<br>, if EXTMODE is set as "H", 1<br>l. If M2 is L then all flags are<br>d, refer to "all clear timing cha | M="L" is output.<br>M1 set value doesn't matter.<br>cleared. |         |

**%**M1: Frame inversion flag is enabled when EXTMODE="L".

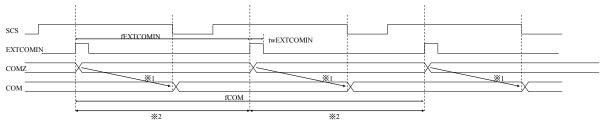
 $\ensuremath{\ens$ 

| SHARP  | SPEC No.<br>LCY-12T09302A                      | MODEL No.<br>LS013B4DN01     | PAGE 16       |
|--|--|------------------------------|---------------|
| 6-5-5 COM Inversion                                  |  |                              |               |
| There are two signals, one is COM signal with serial | <b>-</b> · · · · · · · · · · · · · · · · · · · |                              |               |
| and other is external COM signal input (EXTMODE=     | ="Н").   |                              |               |
| EXTMODE="L"  |  |                              |               |
|  | 1  | ;                            |               |
|  |  |                              | -             |
|  | fCOM   | *1                           | -             |
| •M1:LC polarity inversion flag. If M1 is "           | *2   | tout If M1 is "I" then VCOM= | "I" is output |

•M1:LC polarity inversion flag: If M1is "H" then VCOM="H" is output. If M1 is "L" then VCOM="L" is output. X1:LC inversion has been changed by M1 flag statement.

\*2: The periods of plus polarity and minus polarity should be same length as much as possible.

#### EXTMODE="H"



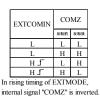
\*1: LC inversion polarity has been set by the rising timing of EXTCOMIN in internal circuit block as COMZ signal, and when SCS become falling timing, LC inversion has been activated as COM signal.
 \*2: The period of EXTCOMIN should be constant.

#### Truth table of COM

<EXTMODE="L">

| EXTCOMIN          | СОМ                       |
|-------------------|---------------------------|
| L<br>L<br>HJ<br>H | Depend on<br>status of M1 |

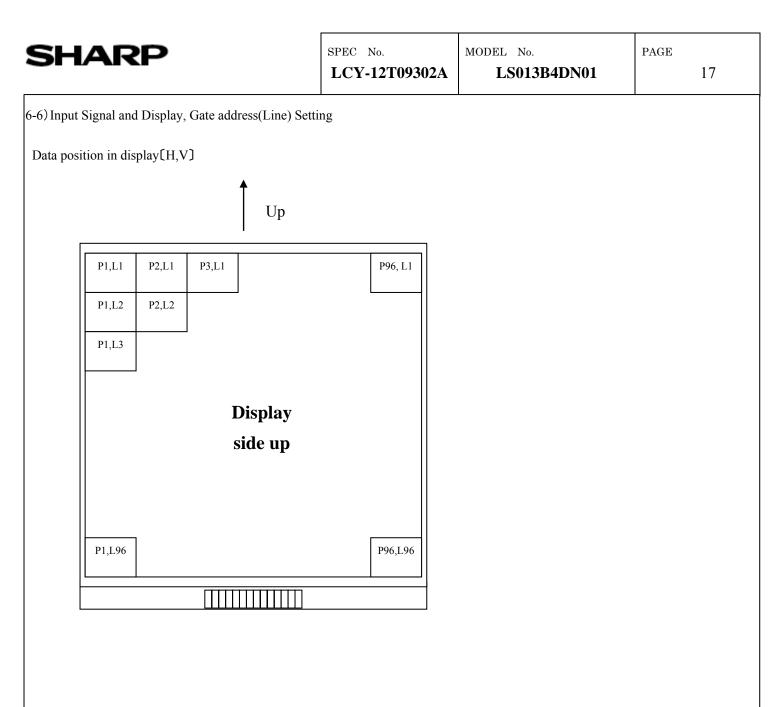
<extmode="h">



| COMZ | COM     |         |  |  |  |
|------|---------|---------|--|--|--|
|      | SCS="L" | SCS="H" |  |  |  |
| L    | L       | Qn-1    |  |  |  |
| Н    | Н       | Qn-1    |  |  |  |
|      |         |         |  |  |  |

\*Qn-1: COM has been changed polarity when SCS meet falling edge.

. (COMZ status has been fixed by signal SCS)



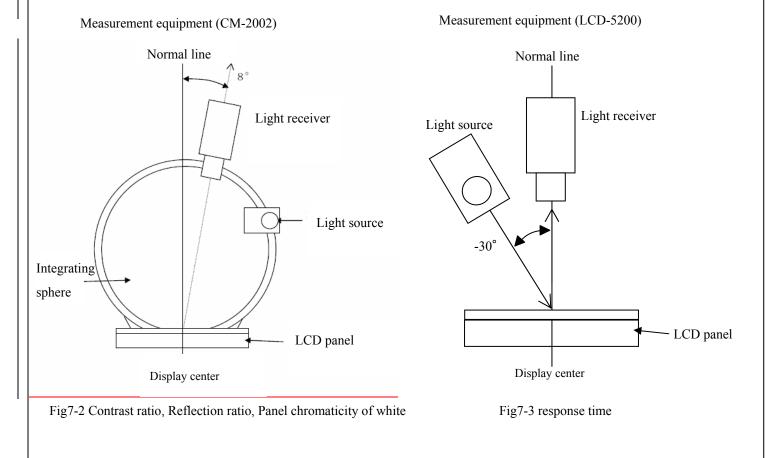
| <b>.</b> . | Gate line address setting |     |     |     |     |     |     |
|------------|---------------------------|-----|-----|-----|-----|-----|-----|
| Line       | AG0                       | AG1 | AG2 | AG3 | AG4 | AG5 | AG6 |
| L1         | Н                         | L   | L   | L   | L   | L   | L   |
| L2         | L                         | H   | L   | L   | L   | ∟   | L   |
| L3         | Н                         | H   | L   | L   | L   | L   | L   |
| •          | -                         | -   | -   | •   | -   | -   | •   |
| •          | -                         | -   | •   | •   |     | •   | •   |
| •          | -                         | •   | •   | •   | •   |     | •   |
| L94        | L                         | Н   | Н   | Н   | Н   | L   | Н   |
| L95        | Н                         | Н   | Н   | Н   | Н   | L   | Н   |
| L96        | L                         | L   | L   | L   | L   | Н   | Н   |

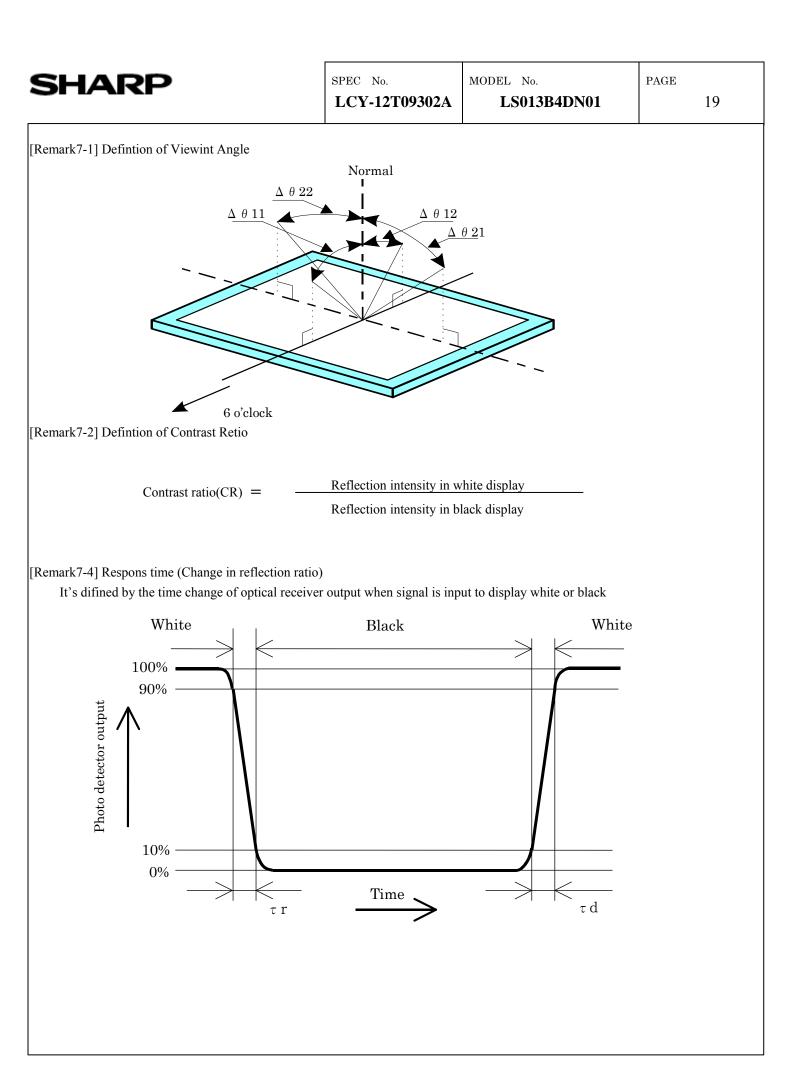
| SHARP | SPEC No.      | MODEL No.   | PAGE |
|-------|---------------|-------------|------|
|       | LCY-12T09302A | LS013B4DN01 | 18   |

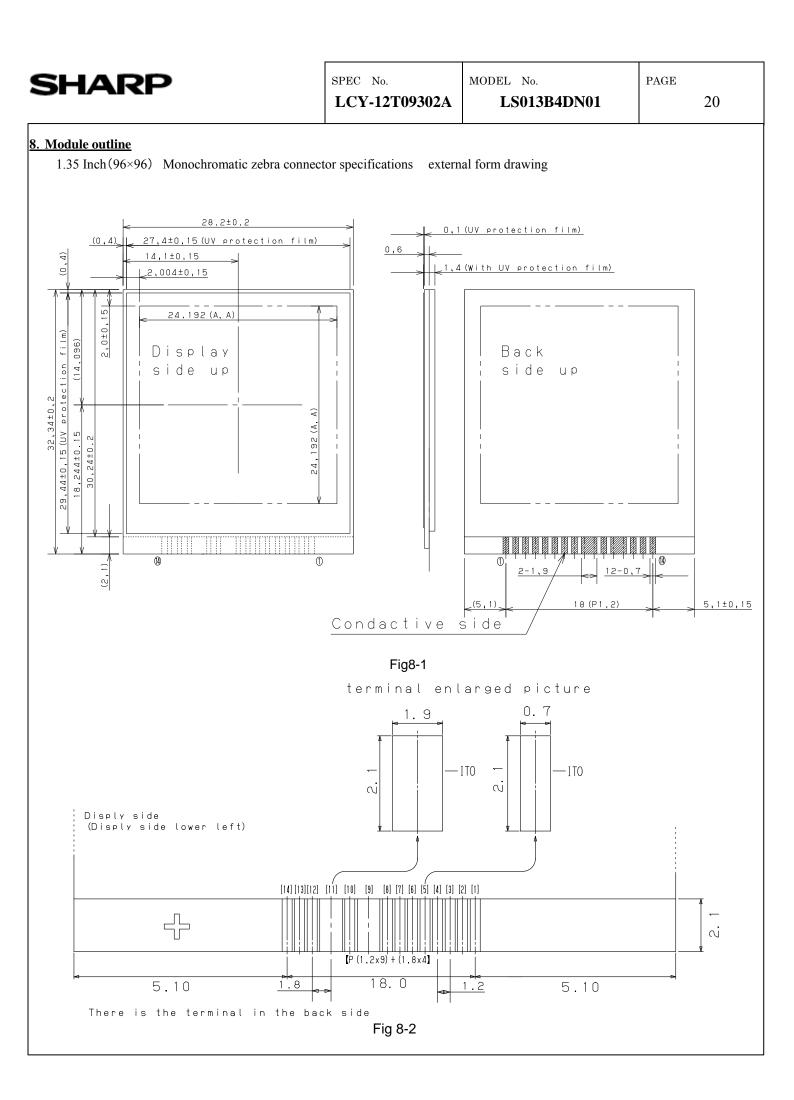
| 7. Optical character<br>Table 7-1 | <u>ristics</u> |         |      |       |      |           | Ta=25          |
|-----------------------------------|----------------|---------|------|-------|------|-----------|----------------|
| Item                              |                | Symbol  | MIN. | TYP.  | MAX. | unit      | Remark         |
| Viewing angle                     | Н              | θ21,θ22 |      | TBD   |      | °(degree) | [Remark7-1]    |
| $CR \ge 5$                        | V              | θ11     |      | TBD   |      | °(degree) |                |
|                                   |                | θ12     |      | TBD   |      | °(degree) |                |
| Contrast ratio                    |                | CR.     | 5    | 10    |      |           | [Remark7-2,3]  |
| Reflecivity ratio                 |                | R       |      | 50    |      | %         | [remark7-3]    |
| Response time                     | Rise           | τr      |      | 50    |      | ms        | [Remark 7-3,4] |
|                                   | Fall           | τd      |      | 50    |      | ms        |                |
| Panel                             | White          | v       |      | 0.313 |      |           | [Remark7-3]    |
| Chromaticity                      | winte          | x<br>y  |      | 0.313 |      |           |                |

[Remark7-3] Optical characteristics measurement equipment.

Figure 7-2 is for contrast ratio, reflectivity ratio, and panel chromaticity measurement, and figure 7-3 is for response time measurement. Both are to be conducted in a dark or room equipment to a dark room

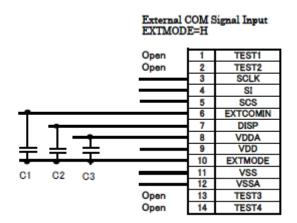






| SHARPSPEC No.MODEL No.PALCY-12T09302ALS013B4DN01 | 21 |
|--|----|

#### 9. External capacitor



#### Fig.9-1 External capacitor recommendation capacity value

<Recommended capacity value>

C1:DISP-VSS: rank B 0.1uF Ceramic capacitor

C2: VDDA- VSS: rank B 1uF Ceramic capacitor

C3: VDD- VSS: rank B 1uF Ceramic capacitor

[Remark] The recommedation capacity of an external capacitor is shown. Finally, determine a capacity value after performing evaluation sufficient by your system. (More bigger capacity value is selectable.)



#### <u> 10. External Power Supply Circuit</u>

10-1An external power supply circuit is necessary to drive the Pixel Memory LCD with the 3V battery.

Table 1-1 shows power supply IC of the recommendation.

#### Table 1-1

| Maker                  | Model Number | Note             |  |
|------------------------|--------------|------------------|--|
| SII                    | S-8821       | Charge pump type |  |
| National Semiconductor | LM2750       | Charge pump type |  |

Refer to specifications of each power supply IC for a detailed specification.

Set the constant value after it evaluating it sufficiently by an actual application when you use it.

#### 10-2. Electrical specification

#### Table 2-1

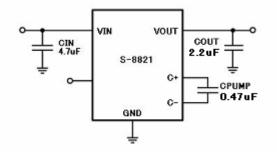
| Model Number  | Vin [V] |     | Vout |     |     | Iout |
|---------------|---------|-----|------|-----|-----|------|
| Model Inumber | min     | max | min  | typ | max | [A]  |
| S-8821        | 2.8     | 5.0 | 4.9  | 5.0 | 5.1 | 0.04 |
| LM2750        | 2.7     | 5.6 | 4.8  | 5.0 | 5.2 | 0.04 |

Refer to specifications of each power supply IC for a detailed specification.

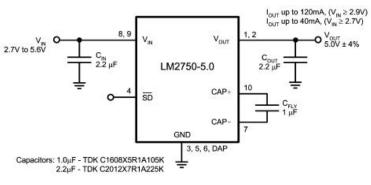
Set the constant value after it evaluating it sufficiently by an actual application when you use it.

#### 10-3. Reference circuit

The reference circuit is shown below.

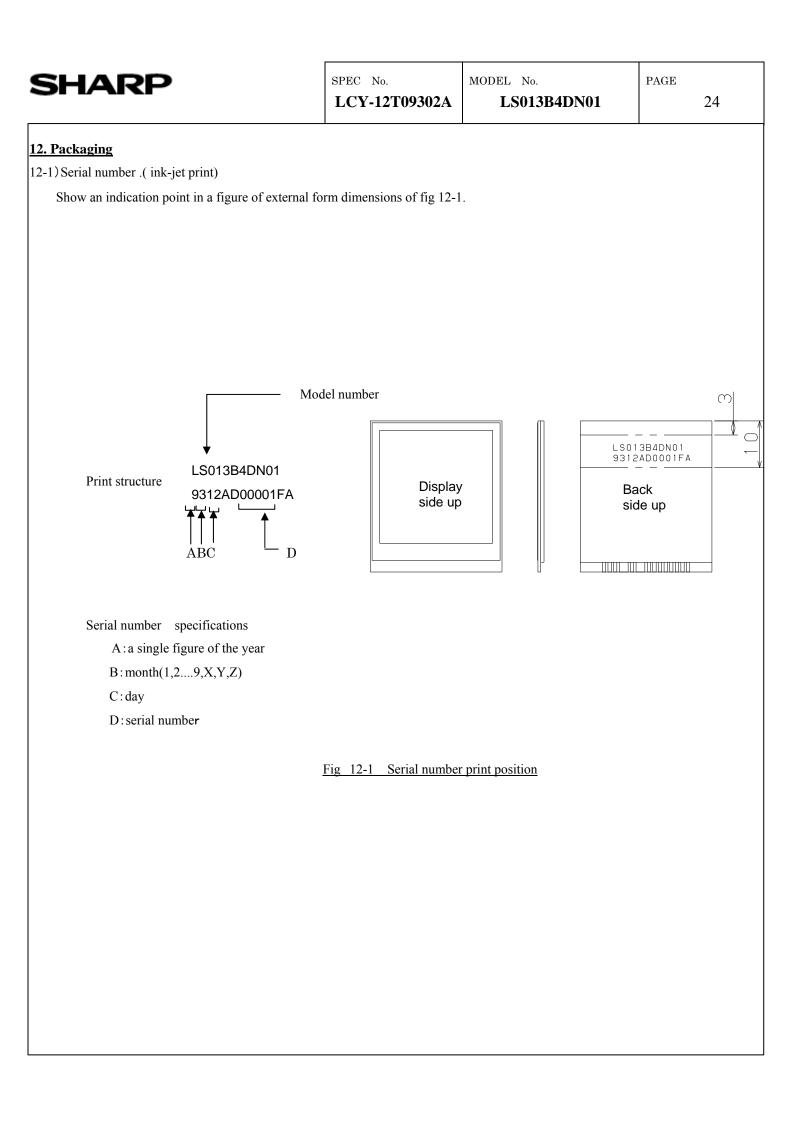


S-8821





|   | SPEC No.<br>LCY-12T09302A | MODEL No.<br>LS013B4DN01      | PAGE 23  |
|---|---------------------------|-------------------------------|--|
| bra connector   |                           |                               |  |
| Contact Resistance : lower than $20\Omega$                                    |                           |                               |  |
| [Recommended zebra connector]   |                           |                               |  |
| • Please select one from these two types.                                     |                           |                               |  |
| 1 Shin-Etsu Polymer: MS-G type  |                           |                               |  |
| Recommended size L:27.7±0.25mm  | W:1.8±0.1mm H:2.3         | ±0.1mm P:0.1mm                |  |
| ② Shin-Etsu Polymer: GB-U type  |                           |                               |  |
| Recommended size L:27.3±0.6mm V   | V:1.7±0.2mm H:2.3±        | 0.2mm P:0.1mm                 |  |
|   |                           |                               |  |
|   |                           |                               |  |
|   |                           |                               |  |
|   |                           |                               |  |
| e zebra connector placement position  |                           |                               |  |
| e zebra connector placement position<br>1. MS-G Specifications(A figure of re | ference) 2. GB            | -U Specifications (A figure o | f reference)                                     |
| 1. MS-G Specifications (A figure of re  | ference) 2. GB            | U Specifications (A figure o  | f reference)                                     |
|   | ference) 2. GB            |                               | f reference)                                     |
|   | ference) 2. GB            |                               | f reference)                                     |
| 1. MS-G Specifications (A figure of re  | ference) 2. GB            | Back Side                     |  |
| 1. MS-G Specifications (A figure of re  |                           | Back Side                     | up au  |
| 1. MS-G Specifications (A figure of re  |                           | Back Side                     | up au  |
| 1. MS-G Specifications (A figure of re  | eference) 2. GB           | Back Side                     | up qu  |
| 1. MS-G Specifications (A figure of re  |                           | Back Side                     | 32.34±0.2  |
| 1. MS-G Specifications (A figure of re  |                           | Back Side                     | a2.34±0.2  |
| 1. MS-G Specifications (A figure of re  | 32.34±0.2                 | Back Side                     | =====================================            |
| 1. MS-G Specifications (A figure of re  | 32.34±0.2                 | Back Side                     | Δ P  |
| 1. MS-G Specifications (A figure of re  | 32.34±0.2                 | Back Side<br>Back Side        | U P $(0.45)$ $(0.45)$ $(0.45)$ $(0.45)$ $(0.45)$ |
| 1. MS-G Specifications (A figure of re  | 32.34±0.2                 | Back Side<br>Back Side        | U P  |
| 1. MS-G Specifications (A figure of re  | 32.34±0.2                 | Back Side<br>Back Side        | U P  |



| SHARP                         | SPEC No.<br>LCY-12T09302A   | MODEL No.<br>LS013B4DN01                                    | PAGE 25 |
|-------------------------------|---|---|---------|
| •Atmosphere:Harmful gas, such |   | nic   |         |
|                               | about 3 months  |   |         |
|                               | In order to prevent the LCD module<br>please control the room humidity ov<br>sufficient countermeasures against e | ver 50%RH and open the package                              | taking  |
| 12-3)Packaging                |   |   | ,       |
|                               | FRAGILE<br>R & E  | ANTITY<br>JANTITY<br>JANTITY<br>T(DATE)<br>U+-7%2#3×4xcr. ( | 3B4DN01 |

| SHARP | SPEC No.      | MODEL No.   | PAGE |
|-------|---------------|-------------|------|
|       | LCY-12T09302A | LS013B4DN01 | 26   |

#### 13. Reliability Test Conditions

#### 13-1 Reliability test items

Table<u>13-1</u>

|   | Test Item   | Test condition  | Remark |
|---|---|---|--------|
| 1 | High temperature storage test                     | Ta=80°C 240h  |        |
| 2 | Low temperature storage test                      | Ta=35°C 240h  |        |
| 3 | High temperature and high humidity operating test | Tp=40°C/95%RH 240h                                    |        |
| 4 | High temperature operating test                   | Tp=70°C 240h  |        |
| 5 | Low temperature operating test                    | Tp=-20°C 240h   |        |
| 6 | Shock test  | $Ta=-30^{\circ}C(1h) \sim +80^{\circ}C(1h) / 5$ cycle |        |
| 7 | Electro static discharge test                     | $\pm 200$ V, 200pF(0 $\Omega$ ) each terminai: 1 time |        |

[Remark] Ta = Ambient temperature, Tp = Panel temperature

[Check items] In the standard condition, there shall be no practical problems that may affect the

display function.

#### 13-2 Panel surface stress spesification

Panel surface stress spesification has been defined as a parameter "Force of stress [N]" without no picture failer. Load testing: Minimum 120[N]

Test condition :

| Test stracture | : LCD panel with UV protection film |
|----------------|-------------------------------------|
| Press point    | : Center of LCD panel               |
| Press Jig      | :φ10mm column                       |
| Press speed    | :1mm/minute                         |
| Support        | : fixed on stage                    |

Press time : 5 seconds after achievement of the press strength, then reless it.

## **LCD** Specification

**LCD Group** 

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