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SAMSUNG TFT-LCD

MODEL NO. : LTA260W2-L07

Note : _____

Any Modification of Spec is not allowed without SEC's permission.

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* Revision History

Specification

No	Date	Page	Before change	After change	Remark
0.0	2005.2.16	All	First issued		

Version

Spec	Revsion No	Date	Control PCB	Inverter PCB	Remark
	0.0	2005.2.16	1.1	0.0	

General Description

* Description

LTA260W2-L02 is a color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFTs as a switching devices. This model is composed of a TFT LCD panel, a driver circuit and a back-light system. The resolution of a 26.0" contains 1366 x 768 pixels and can display up to 16.7 million colors with wide viewing angle of 85° or higher in all directions.

* Features

- High contrast ratio, high aperture structure
- PVA(Patterned Vertical Align) mode
- Wide viewing angle($\pm 170^\circ$)
- High speed response (DCC2)
- WXGA(1366 x 768 pixels) resolution (16:9)
- Low Power consumption
- 8 U-type CCFL(Cold Cathode Fluorescent Lamp)
- DE only mode
- LVDS(Low-Voltage Differential Signal) interface.(1pixel/clock)

* Applications

Home-alone Multimedia TFT-LCD TV
 Display terminals for AV application products
 High Definition TV (HD TV)

* General information

Items	Specification	Unit	Note
Display area	575.77(H) × 323.71(V)	mm	
Outline Dimension	626.0(W) × 373.0(H) × 51.0(D)	mm	Tolerance ± 1 mm
Driver element	a-Si TFT active matrix		
Display colors	16.7M(8bits-true)	colors	
Number of pixels	1,366 x 768	pixel	16:9
Pixel pitch	0.4215(H) × 0.4215(W)	mm	
Display mode	Normally Black		
Surface treatment	Haze 44% , Hard-Coating (3H)		Conductive Pol.
Weight	4700(Typ)	g	

1. Absolute Maximum Ratings

1.1 Absolute ratings of environment

Item	Symbol	Min.	Max.	Unit	Note
Storage temperature	T _{STG}	-20	65	°C	(1)
Operating temperature (Ambient temperature)	T _{OPR}	0	50	°C	(1)
	T _{sur}	0	65	°C	(2)
Shock (non - operating)	Snop	-	50	G	(3),(5)
Vibration (Non - operating)	Vnop	-	1.5	G	(4),(5)

Note (1) Temperature and relative humidity range are shown in the figure below.

90 % RH Max. (40 °C ≥ Ta)

Maximum wet-bulb temperature at 39 °C or less. (Ta > 40 °C) No condensation.

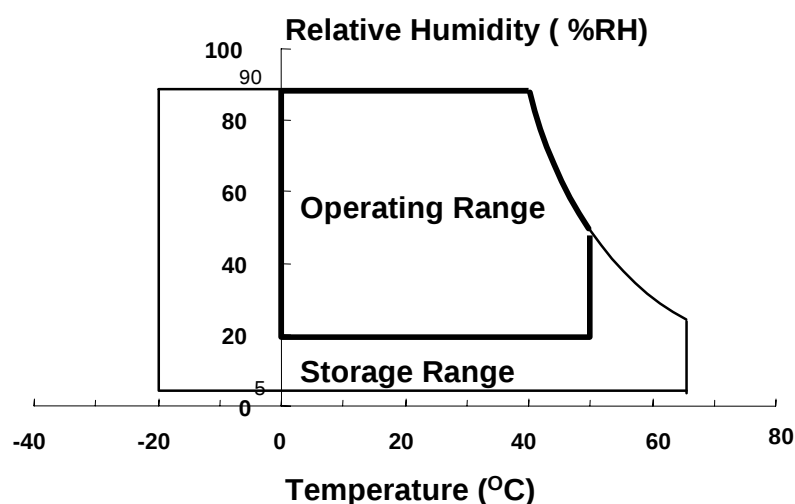
(2) Abnormal visual problems by panel surface temperature can be occurred in specific range. But materials(ex : polarizer) are not damaged permanently in this range, T_{sur}.

(3) 11ms, sine wave, 1 time for ±X, ±Y, ±Z axis

(4) 10~300Hz/1.5G

(10min/cycle, 30min for X,Y,Z axis)

(5) At testing Vibration and Shock, the fixture in holding the Module to be tested have to be hard and rigid enough so that the Module would not be twisted or bent by the fixture.



1.2 ELECTRICAL ABSOLUTE RATINGS

(1) TFT LCD Module

(V_{ss} = GND = 0 V)

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	VDD	V _{ss} -0.5	6.5	V	(1)

NOTE (1) Within Ta (25 ± 2 °C)

(2) BACK-LIGHT UNIT

(Ta = 25 ± 2°C)

ITEM	SYMBOL	MIN.	MAX.	UNIT	NOTE
Power Supply Voltage/ Inverter	V _{CC}	V _{ss} -0.5	26.4	V	(1)
Lamp Current	I _L	4.0	7.0	mArms	(2),(3)
Lamp Frequency	F _L	40	80	kHz	(2)

NOTE (1) Inverter Input power

(2) Permanent damage to the device may occur if maximum values are exceeded.
Functional operation should be restricted to the conditions described under Normal Operating Conditions.

(3) Specified values are for a single lamp.

2. Optical Characteristics

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room or equivalent state with the methods shown in Note (1).

◆ Measuring equipment : TOPCON BM-5A , BM-7, PHOTO RESEARCH PR650

* Ta = 25 ± 2°C , VDD=5.0V, fv= 60Hz, fDCLK=80 MHz, Lamp current = 5.2mA_{rms}

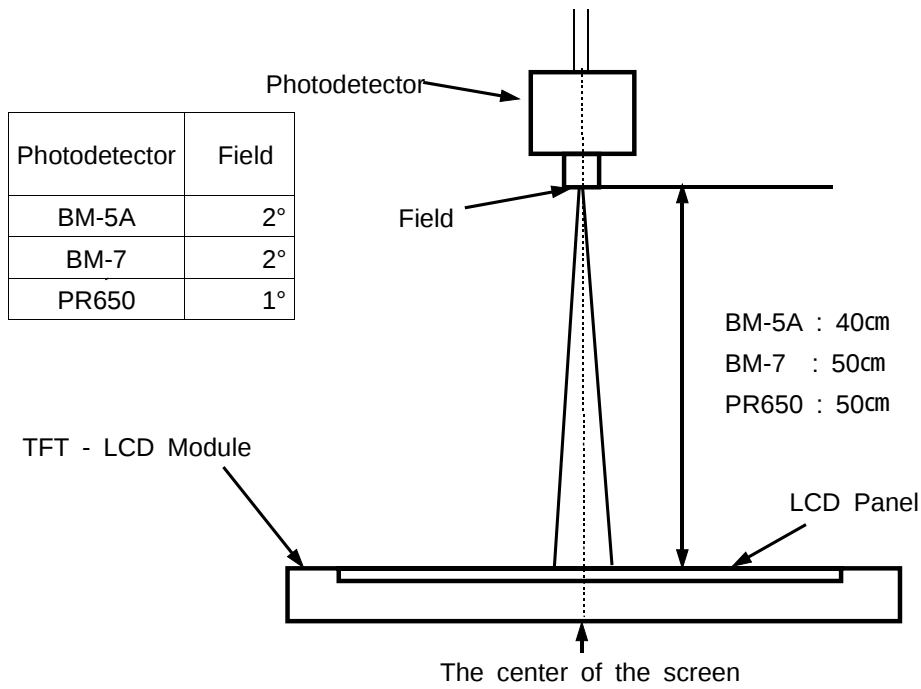
Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio (Center of screen)		C/R	Normal $\phi = 0$ $\theta = 0$	600	800	-		(3) BM-5A
Response Time (DCC On)	Rising	Tr		-	8	(12)	msec	(4),(9) BM-7
	Falling	Tf		-	6	(10)		
	G to G	Tg		-	8	(12)		
Luminance of White (Center of screen)		YL	Viewing Angle	400	500	-	cd/m2	(5) BM-5A
Color Chromaticity (CIE 1931)	Red	Rx		TYP. -0.03	(0.643)	TYP. +0.03		(6) PR650
		Ry			(0.332)			
	Green	Gx			(0.270)			
		Gy			(0.587)			
	Blue	Bx			(0.143)			
		By			(0.063)			
	White	Wx			0.272			
		Wy			0.277			
Color Temperature		k		-	12000	-		
Viewing Angle	Hor.	θ L	C/R $\geq$ 10	75	85	-	Degrees	(7) BM-5A
		θ R		75	85	-		
	Ver.	ϕ H		75	85	-		
		ϕ L		75	85	-		
Brightness Uniformity (9 points)		Buni		-	-	25	%	(2),(8) BM-5A

Note 1) Test Equipment Setup

After stabilizing and leaving the panel alone at a given temperature for 30 min ,the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. 30 min after lighting the back-light. This should be measured in the center of screen.

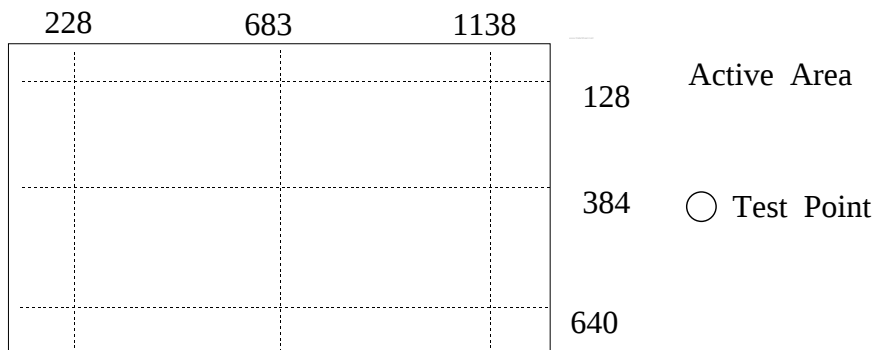
A single lamp current : 5.2mA

Environment condition : Ta = 25 ± 2 °C



Optical Measuring Equipment Setup

Note 2) Definition of test point



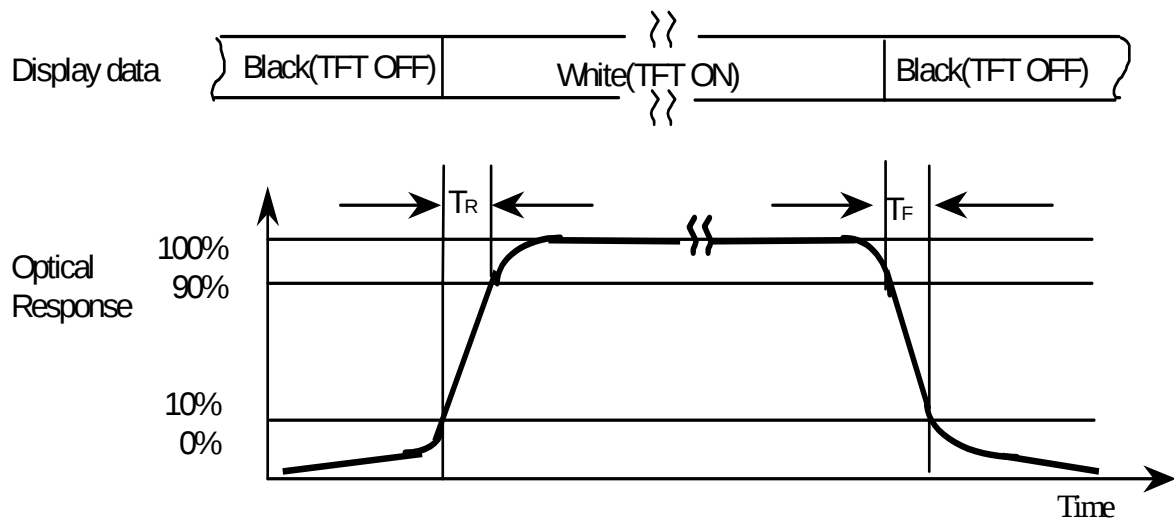
Note 3) Definition of Contrast Ratio (C/R) : Ratio of gray max (Gmax) & gray min (Gmin) at the center point(5) of the panel

$$CR = \frac{G_{\max}}{G_{\min}}$$

Gmax : Luminance with all pixels white

Gmin : Luminance with all pixels black

Note 4) Definition of Response time : Sum of T_R , T_F

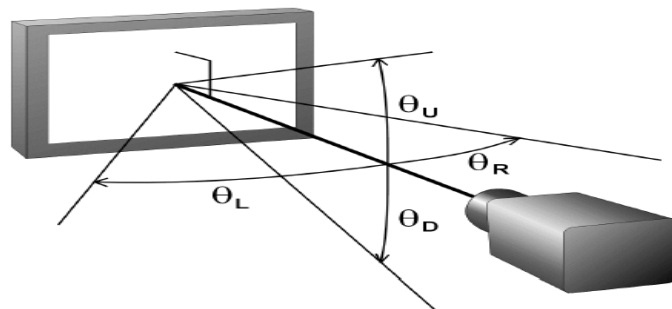


Note 5) Definition of Luminance of White : Luminance of white at center point(5).

Note 6) Definition of Color Chromaticity (CIE 1931)

Color coordinate of Red , Green , Blue & White at center point(5).

Note 7) Definition of Viewing Angle : Viewing angle range ($CR \geq 10$)



Note 8) Definition of 9 points brightness uniformity

$$B_{uni} = 100 * \frac{(B_{max} - B_{min})}{B_{max}}$$

B_{max} : Maximum brightness

B_{min} : Minimum brightness

3. Electrical Characteristics

3.1 TFT LCD MODULE

Ta = 25°C

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply	V _{DD}	4.5	5.0	5.5	V	(1)
Power Consumption	(a)Black	(800)	-	(1100)	mA	(2),(3)
	(b)White	(900)	-	(1400)	mA	
	(c)N-Pattern	(1200)	-	(1800)	mA	
Vsync Frequency	f _V	48	60	66	Hz	
Hsync Frequency	f _H	44	47	53	kHz	
Main Frequency	f _{DCLK}	65	75	82	MHz	
Rush Current	I _{RUSH}	-	-	4	A	(4)

Note (1) Main pixel clock frequency is the value which is measured at the input of LVDS transmitter.

(2) f_V=60Hz, f_{DCLK} =80MHz, V_{DD} = 5.0V, DC Current.

(3) Power dissipation check pattern(LCD Module only)

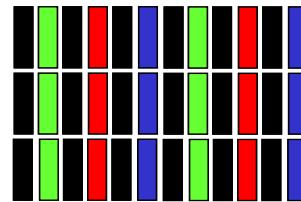
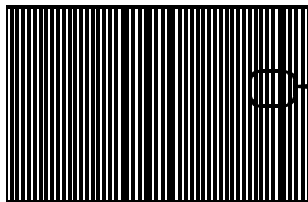
(a) Black Pattern



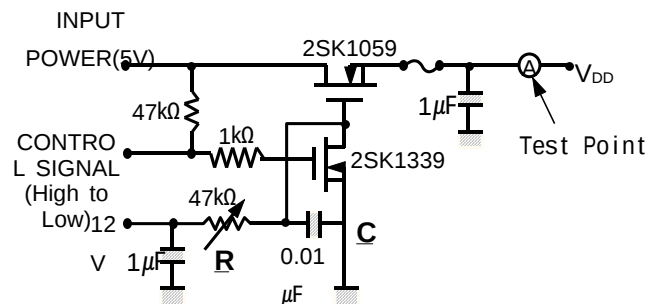
(b) White Pattern



(c) N-Pattern



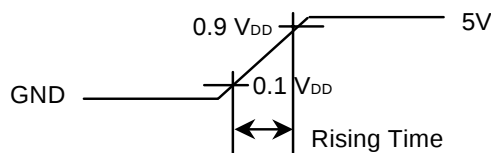
(4) Measurement Conditions (Rising time =470μs)



Note : Control Signal : High(+5V) -->Low(Ground)

All Signal lines to panel except for power 5V : Ground

The rising time of supplied voltage is controlled to 470us by R and C value.



3.2 BACK-LIGHT UNIT

The back-light system is an U-type with 8 CCFTs (Cold Cathode Fluorescent Tube).

- Life time (Hr) of a lamp, 50,000hours, is defined as the time in which it continues to operate under the condition of $T_a = 25 \pm 2^\circ\text{C}$ and Typical Luminance for a lamp until the brightness becomes 50% or lower than it's original value.

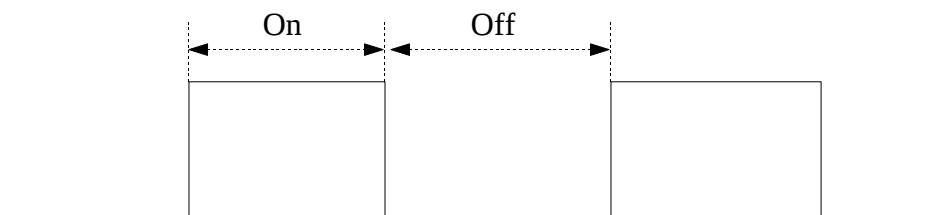
$T_a = 25 \pm 2^\circ\text{C}$

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Lamp Current	I_L	4.2	5.2	6.2	mArms	
Lamp Voltage	V_L		1005		Vrms	
Lamp Frequency	f_L	-	60		kHz	
Operating Life Time	Hr	50,000	-	-	Hour	
Start up Voltage	V_s	-	-	1560@0°C	Vrms	
				1250@25°C		

3.2.1 Inverter Input Condition & Specification

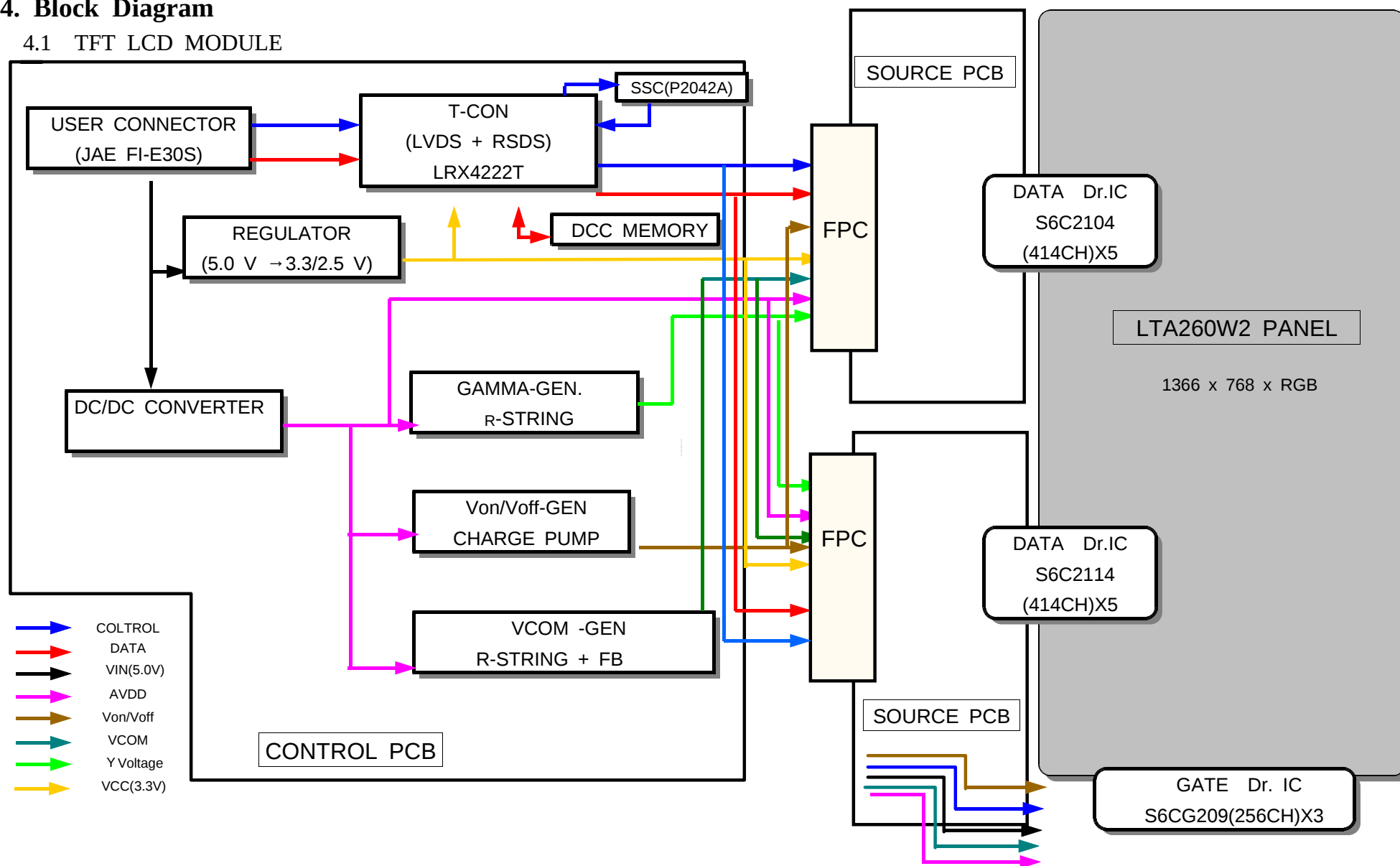
Items	Symbol	Conditions	Specifications			Unit	Note
			Min.	Typ.	Max.		
Input Voltage	Vin	-	23.0	24.0	25.0	V	Ta=25
Input Current	Pin		-	(3.5)	(5)	Adc	After 2hour Warm-up @ Vin=16.5V (1)
Output Current	Io,max	Vadim=3.3V	4.7	5.2	5.7	mArms	
	Io,min	Vadim=0V	(1.0)	(1.5)	(2.0)		
Lamp Frequency	FL	Vin=16.5,Vadim=3.3V	60±5			kHz	
Shutdown Time	Ts-d	No Load	1	-	2	sec	
Open Lamp Voltage	Vo	No Load	1560	-	-	V	
On/Off Control	On	ON/OFF=High	2.4	-	5	V	
	Off	ON/OFF=Low	0	-	0.8	V	

Note(1) RMS Lamp Current : PWM dimming Controlled by Analog Voltage



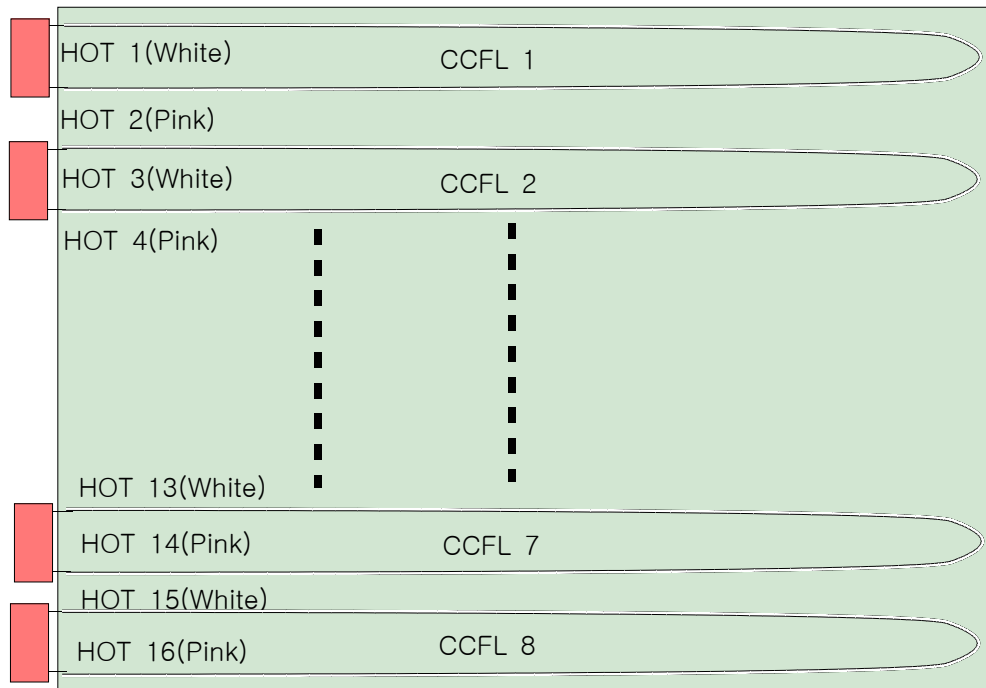
4. Block Diagram

4.1 TFT LCD MODULE



4.2 BACL-LIGHT UNIT

 HOT : HIGH VOLTAGE (Part NO. : 20015WR-07B YEONHO)



5. Input Terminal Pin Assignment

5.1. Input Signal & Power

Connector FI-E30S (JAE)

N o	S i g n a l	N o	S i g n a l
1	N . C *	1 6	G N D
2	N . C *	1 7	R x 3 -
3	N . C *	1 8	R x 3 +
4	G N D	1 9	G N D
5	R x 0 -	2 0	N . C *
6	R x 0 +	2 1	L V D S O P T I O N **
7	G N D	2 2	N . C *
8	R x 1 -	2 3	G N D
9	R x 1 +	2 4	G N D
1 0	G N D	2 5	G N D
1 1	R x 2 -	2 6	V d d (+ 5 V d c)
1 2	R x 2 +	2 7	V d d (+ 5 V d c)
1 3	G N D	2 8	V d d (+ 5 V d c)
1 4	R x C L K -	2 9	V d d (+ 5 V d c)
1 5	R x C L K +	3 0	V d d (+ 5 V d c)

* NOT CONNECTED : THIS PINS ARE ONLY USED FOR SEC INTERNAL OPERATIONS.

** LVDS OPTION : IF THIS PIN : HIGH (3.3 V) → NORMAL NS LVDS FORMAT

OTHERWISE : LOW (GND) → JEIDA LVDS FORMAT

Sequence : On = Vdd(T1) ≥ LVDS Option ≥ Interface Signal(T2)

OFF = Interface Signal(T3) ≥ LVDS Option ≥ Vdd

5.2. Inverter Input Pin Configuration

Connector : JST(S14B-PHA-SM)

PIN NO.	PIN Configuration (FUNCTION)
1	AWG24 24.0 V
2	AWG24 24.0 V
3	AWG24 24.0 V
4	AWG24 24.0 V
5	AWG24 24.0 V
6	AWG24 GND
7	AWG24 GND
8	AWG24 GND
9	AWG24 GND
10	AWG24 GND
11	N.C
12	BACKLIGHT ON ~ OFF / ON:2.4 - 5.0 V, OFF: 0 - 0.8 V
13	Internal PWM Dimming (3.3V:Max ~ 0V:Min)
14	N.C

Sub Input Pin configuration

Connector : B4B-ZR-SM3 (JST)

1	GND
2	BACKLIGHT ON ~ OFF / ON:2.4 - 5.0 V, OFF: 0 - 0.8 V
3	Internal PWM Dimming (3.3V:Max ~ 0V:Min)
4	N.C

5.3 LVDS Interface

-LVDS Receiver : Tcon merged

-JEIDA & Normal Data Format

LVDS OPTION (21pin)	:	IF THIS PIN : HIGH (3.3 V) → NORMAL NS LVDS FORMAT
		OTHERWISE : LOW (GND) → JEIDA LVDS FORMAT

	LVDS pin	JEIDA -DATA	Normal -DATA
TxOUT/RxIN0	TxIN/RxOUT0	R2	R0
	TxIN/RxOUT1	R3	R1
	TxIN/RxOUT2	R4	R2
	TxIN/RxOUT3	R5	R3
	TxIN/RxOUT4	R6	R4
	TxIN/RxOUT6	R7	R5
	TxIN/RxOUT7	G2	G0
TxOUT/RxIN1	TxIN/RxOUT8	G3	G1
	TxIN/RxOUT9	G4	G2
	TxIN/RxOUT12	G5	G3
	TxIN/RxOUT13	G6	G4
	TxIN/RxOUT14	G7	G5
	TxIN/RxOUT15	B2	B0
	TxIN/RxOUT18	B3	B1
TxOUT/RxIN2	TxIN/RxOUT19	B4	B2
	TxIN/RxOUT20	B5	B3
	TxIN/RxOUT21	B6	B4
	TxIN/RxOUT22	B7	B5
	TxIN/RxOUT24	HSYNC	HSYNC
	TxIN/RxOUT25	VSYNC	VSYNC
	TxIN/RxOUT26	DEN	DEN
TxOUT/RxIN3	TxIN/RxOUT27	R0	R6
	TxIN/RxOUT5	R1	R7
	TxIN/RxOUT10	G0	G6
	TxIN/RxOUT11	G1	G7
	TxIN/RxOUT16	B0	B6
	TxIN/RxOUT17	B1	B7
	TxIN/RxOUT23	RESERVED	RESERVED

5.4 Input Signal, Basic Display Colors and Gray Scale of Each Color

COLOR	DISPLAY	DATA SIGNAL																										GRAY SCALE LEVEL
		RED								GREEN								BLUE										
		R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7			
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-		
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-		
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-		
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-		
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-		
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-		
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-		
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-		
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0		
	DARK ↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1		
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3-R252		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:			
	↓	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R253		
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R254		
	LIGHT	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R255		
RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R255			
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0		
	DARK ↑	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1		
		0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3-G252		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:			
	↓	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G253		
		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G254		
	LIGHT	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G255		
GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G255			
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0		
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	B1		
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	B2		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3-B252		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:			
	↓	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	B253		
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	B254		
	LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	B255		
BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B255			

Note) Definition of Gray :

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

Input Signal : 0 = Low level voltage, 1 = High level voltage

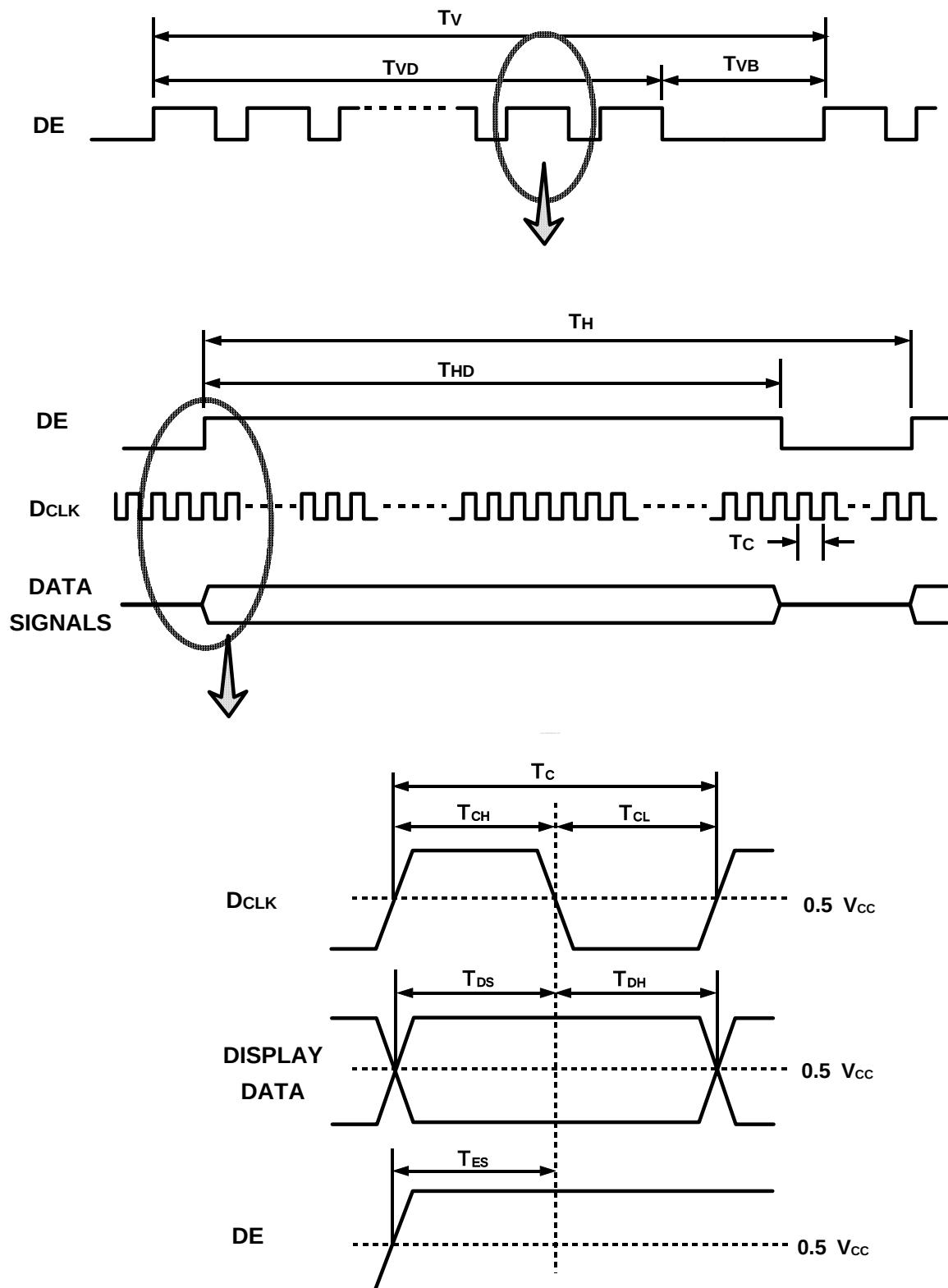
6. Interface Timing

6.1 Timing Parameters (DE only mode)

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Clock	Frequency	1/TC	65	75	82	MHz	
Hsync		Fh	44	47	53	KHz	
Vsync		Fv	48	60	66	Hz	
Vertical Active Display Term	Display Period	TVD	-	768	-	lines	
	Vertical Total	TV	773	838	1200	lines	
Horizontal Active Display Term	Display Period	THD	-	1366	-	clocks	
	Horizontal Total	TH	1460	1600	2000	clocks	

Note) This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

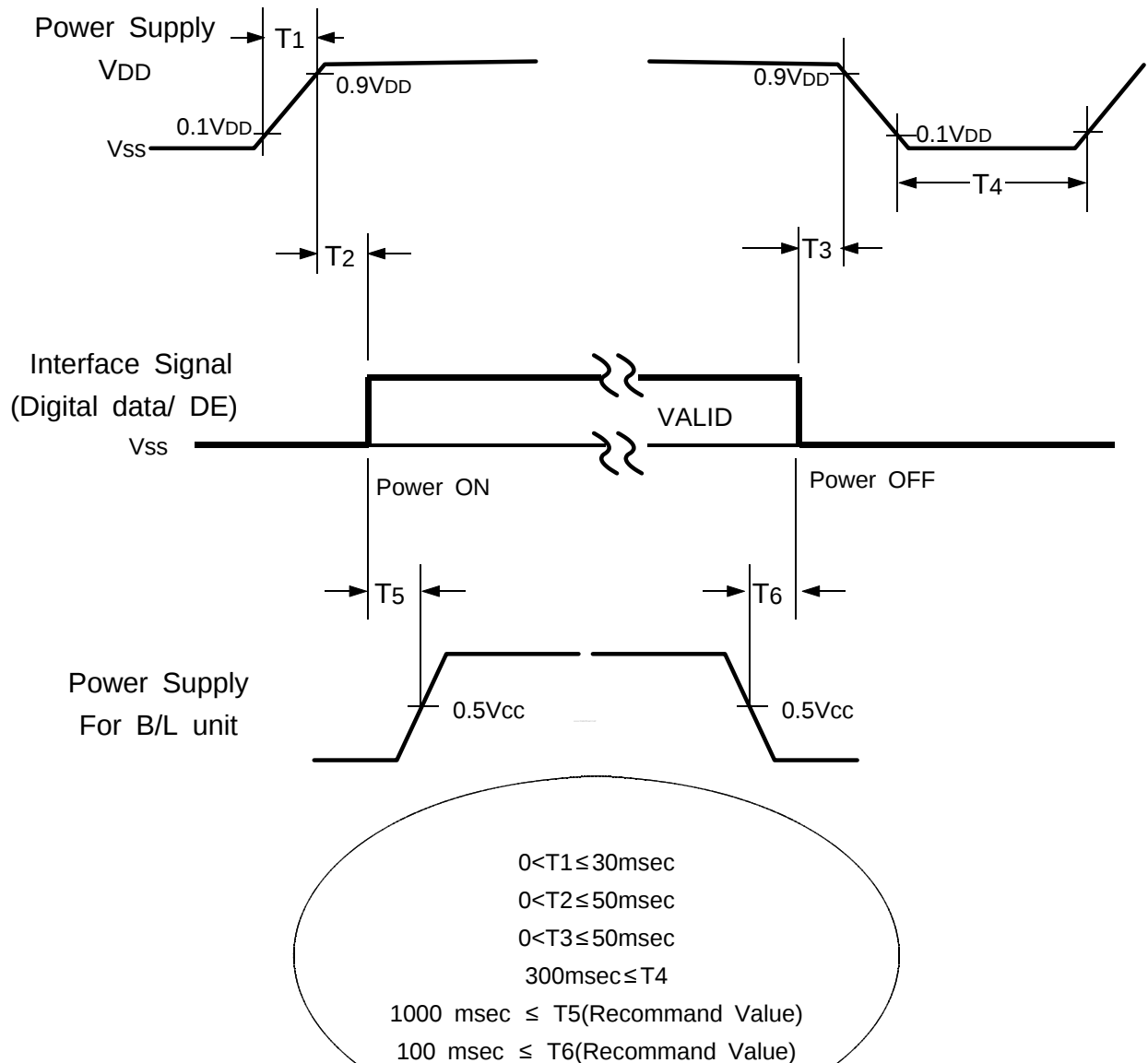
6.2 Timing diagrams of interface signal (DE only mode)



6.3 Power ON/OFF Sequence

6.3.1 Control B/D Sequence

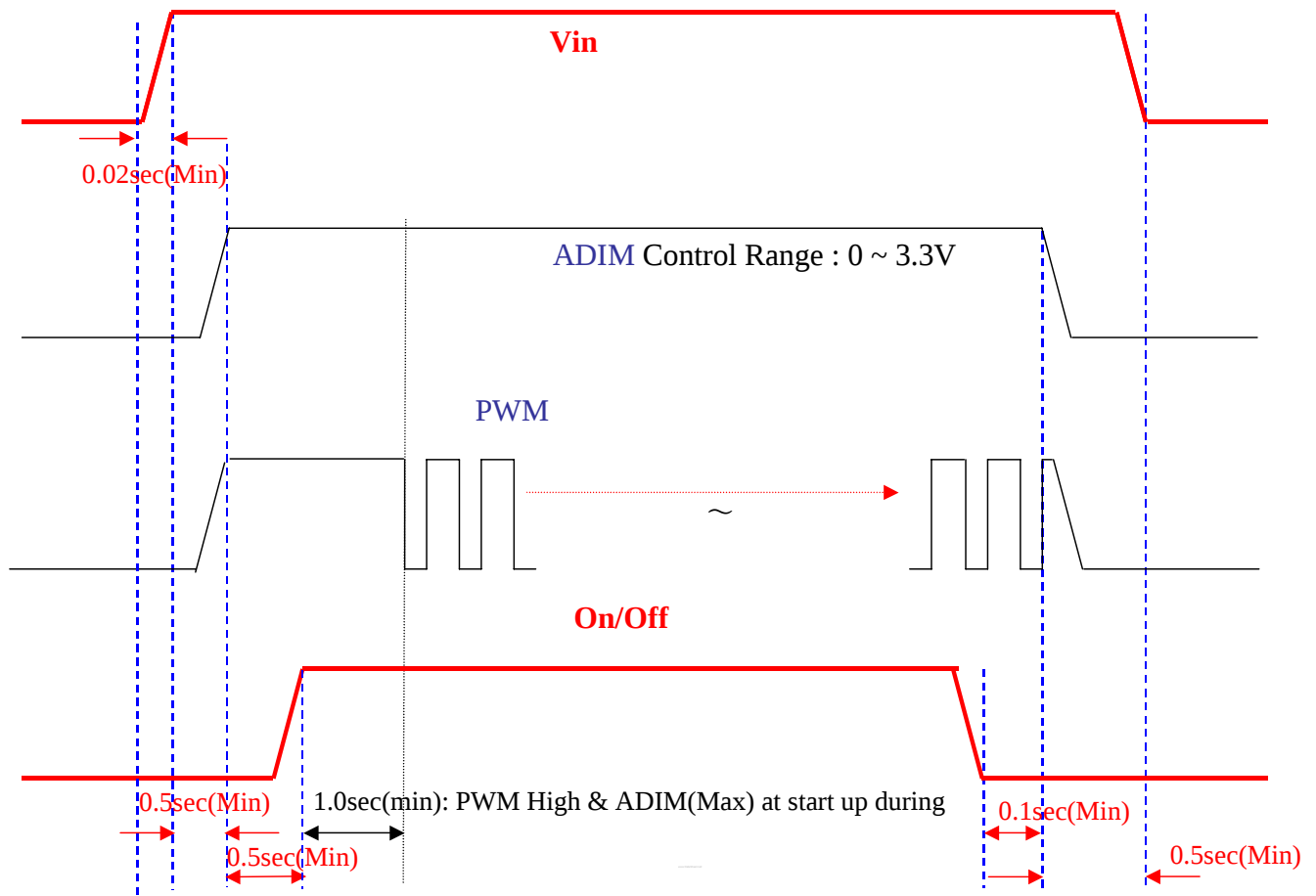
: To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.



NOTE.

- (1) The supply voltage of the external system for the module input should be the same as the definition of VDD.
- (2) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become abnormal screen.
- (3) In case of VDD = off level, please keep the level of input signals on the low or keep a high impedance.
- (4) T4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.

6.3.2 Inverter B/D Sequence



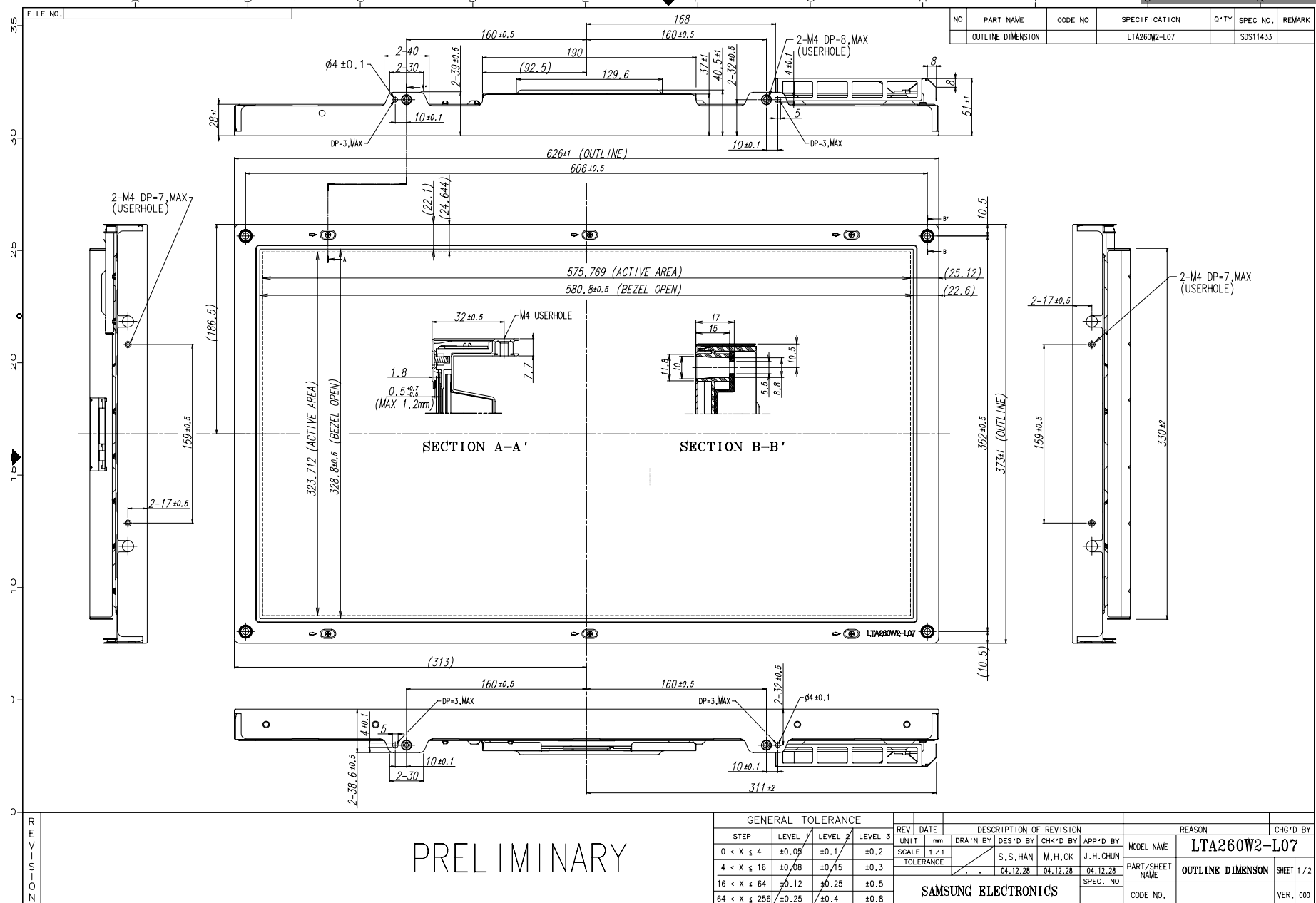
NOTE.

(1) T_r (transition time of rising) & T_f (transition time of falling)

- The range of transition time for V_{in} / A_{dim} / PWM / BL-on/off is 10% ~ 90%.

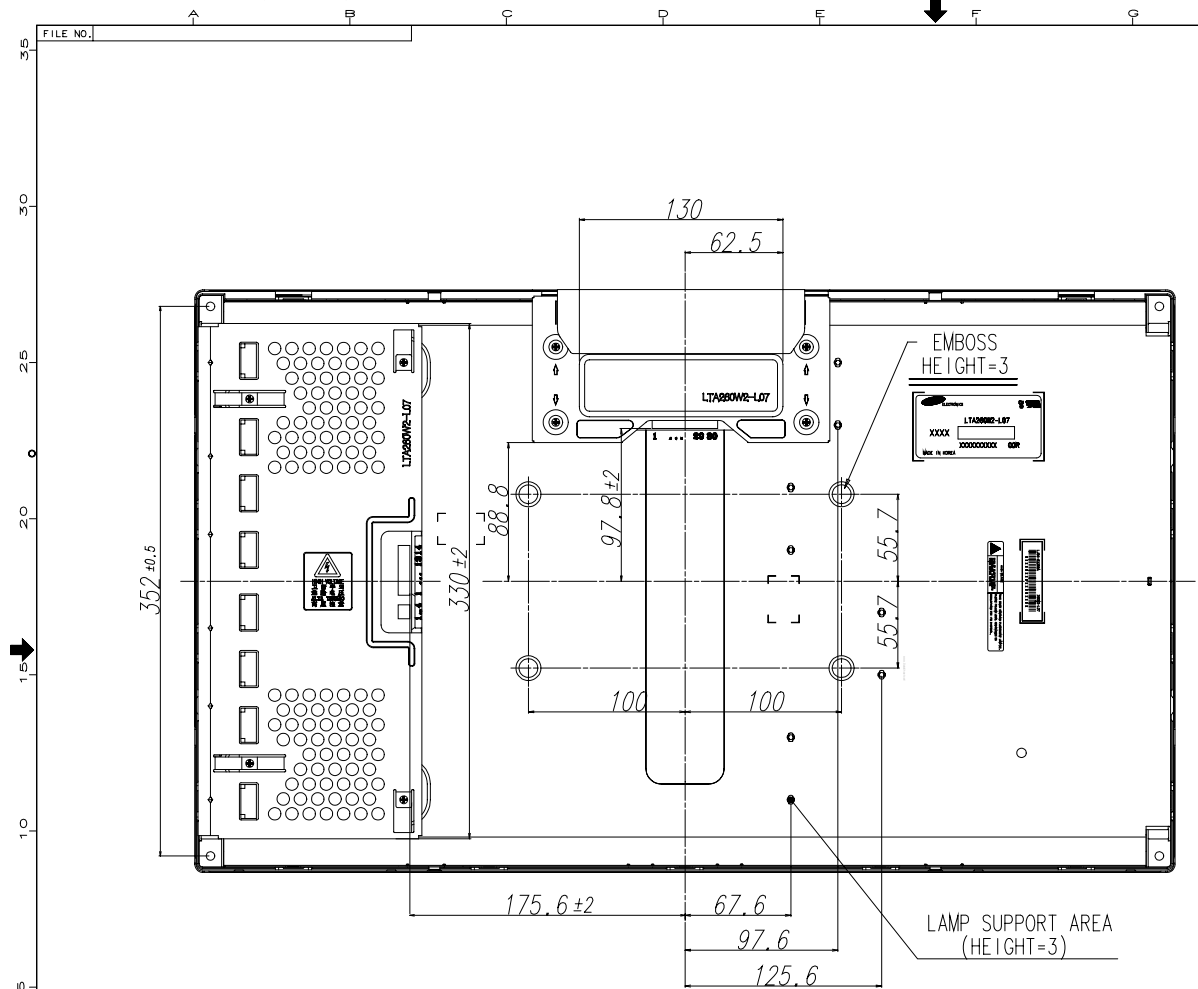
7. Outline Dimension(Front)

Preliminary



Outline Dimension(Rear)

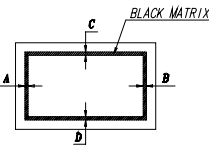
Preliminary



- * NOTE
- 1.BACKLIGHT : 8 COLD CATHODE FLUORESCENT LAMPS.(U-SHAPE)
 - 2.I/F CONNECTOR SPECIFICATION.(30PIN)
 - MAKER : JAE
 - PART NO. : FI-E30S
 - 3.INVERTER CONNECTOR SPECIFICATION.(14PIN / 4PIN)
 - MAKER : JST
 - PART NO.: S14B-PHA-SM(14PIN)
 - MAKER : JST
 - PART NO.: B4B-ZR-SM3(4PIN)
 - 4.LAMP WIRE SPEC.
 - UL3239Y, AWG24, DC6KV, ϕ 1.74
 5. BLACK MATRIX SPEC

$$|A - B| \leq 2.0 \text{ mm}$$

$$|C - D| \leq 2.0 \text{ mm}$$

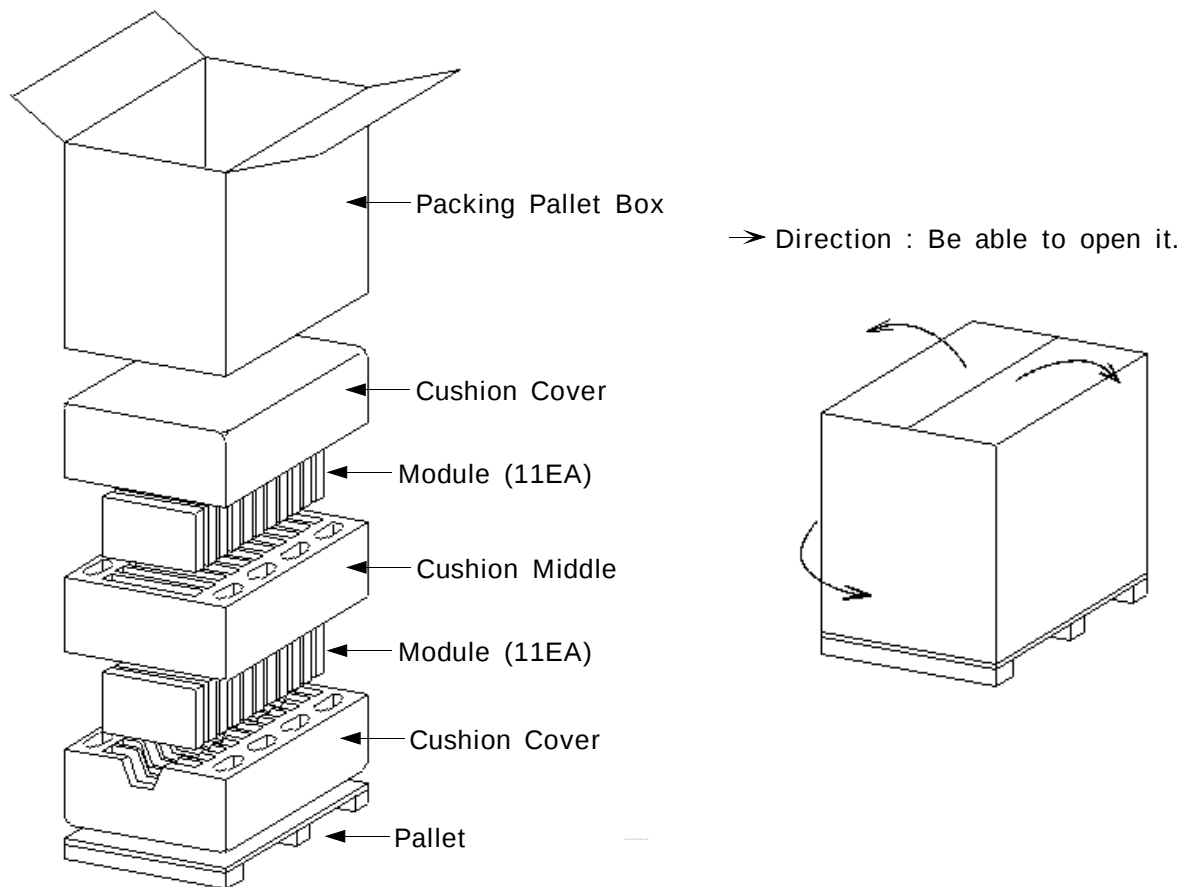


PRELIMINARY

GENERAL TOLERANCE				REV	DATE	DESCRIPTION OF REVISION				REASON		CHG'D BY
STEP	LEVEL	LEVEL 2	LEVEL 3	UNIT	mm	DRA'N BY	DES'D BY	CHK'D BY	APP'D BY	MODEL NAME	LTA260W2-L07	
0 < X ≤ 4	±0.05	±0.1	±0.2	SCALE	1/1					PART/SHEET NAME	OUTLINE DIMENSION	SHEET 2/2
4 < X ≤ 16	±0.08	±0.15	±0.3	TOLERANCE		S.S.HAN	M.H.OK	J.H.CHUN		CODE NO.	-	VER. 000
16 < X ≤ 64	±0.12	±0.25	±0.5									
64 < X ≤ 256	±0.25	±0.4	±0.8									

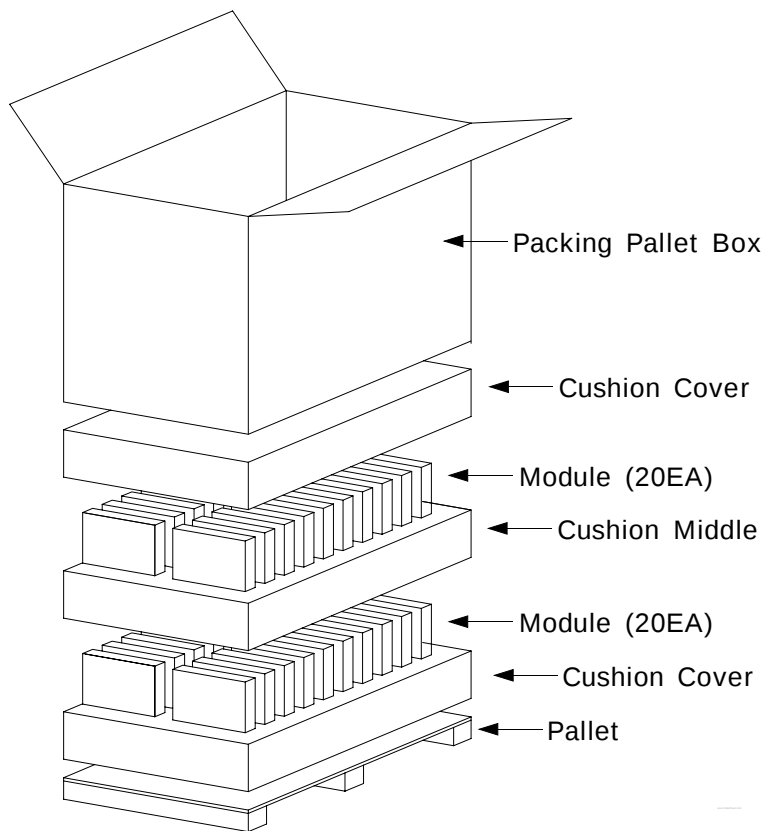
8. PACKING

8.1 Packing Flow

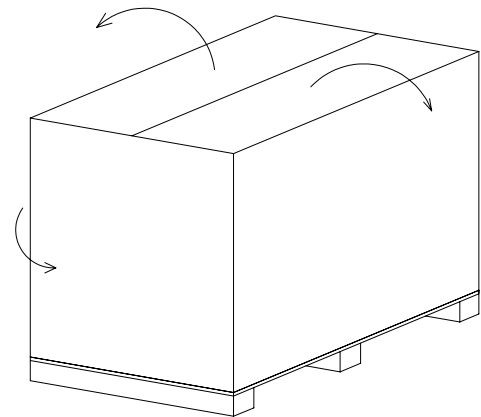


ITEM	Specification	Remark
LCD Packing	22ea / Box (Packing-Pallet Box)	1. 4.7kg/LCD 2. 5.5kg/Cushion Cover(2ea) 3. 6kg/Cushion Middle(1ea) 4. 7kg/Packing-Pallet Box(1ea) 5. Cushion Material : EPS 6. Packing Pallet Box Material : DW4
Pallet	1 Box/Pallet	1. Pallet weight : 7kg 2. 123kg/Pallet , Total : 130kg/Pallet
Packing Direction	Vertical	
Pallet Size	H x V x Height	1150mm(H) x 985mm(V) x 1230mm(Height)

8. PACKING



→ Direction : Be able to open it.



* Packing Specification

ITEM	Specification	Remark
LCD Packing	40ea / Box (Packing-Pallet Box)	1. 3.0kg/LCD(40ea) 2. 3.7kg/Cushion Cover(2ea) 3. 5.5kg/Cushion Middle(1ea) 4. 8.7kg/Packing-Pallet Box(1ea) 5. Cushion Material : EPS 6. Packing Pallet Box Material : DW4
Pallet	1 Box/Pallet	1. Pallet weight : 8.8kg 2. 141.6kg/Pallet , Total : 150.4kg/Pallet
Packing Direction	Vertical	
Pallet Size	H x V x Height	1265mm(H) x 1145mm(V) x 928mm(Height)

9. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

(1) Parts number : LTA260W2-L07-XXXX

1

2

3

4

5

6

7

8

9

(2) Lot number : 5 Y 5 H 123 01 A

1

2

3

4

5

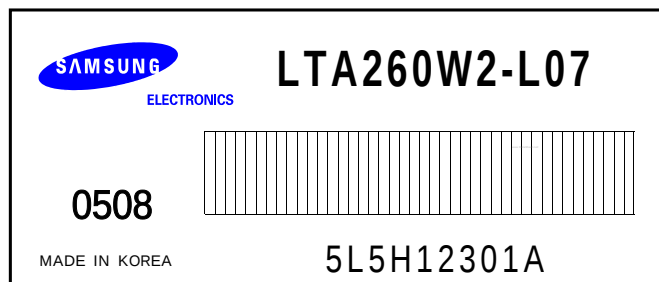
6

7

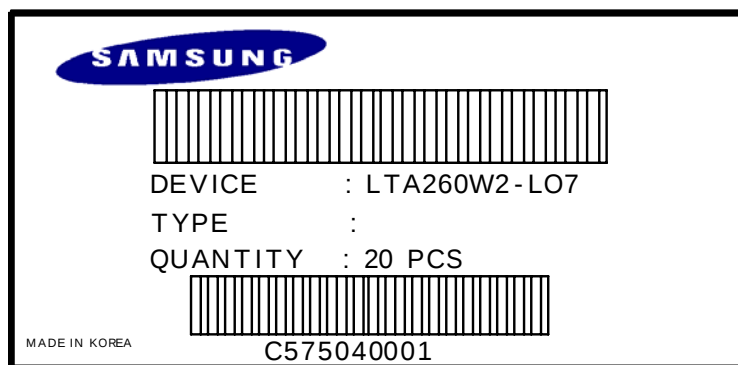
5 : Line
Y : Device
5 : Year
H : Month
123 : LOT No.
01 : GLASS No.
A : CELL No.

LTA : AV model
260 : Panel Size
W : WXGA
2 : Generation
L : LVDS
07 : Derivation No.
X : Customer Code
XX : Revision No.
X : Line

(3) Nameplate Indication



(4) Packing Pallet Label



C 5 7 5 0 4 0 0 0 1

1

2

3

4

5

6

C : PALLET
5 : LINE
7 : CITE CODE
5 : YEAR
04 : WEEK
0001 : SERIAL No.

10. General Precautions

10.1 Handling

- (a) When the module is assembled, It should be attached to the system firmly using every mounting holes. Be careful not to twist and bend the modules.
- (b) Refrain from strong mechanical shock and / or any force to the module. In addition to damage, this may cause improper operation or damage to the module and CCFT back-light.
- (c) Note that polarizers are very fragile and could be easily damaged. Do not press or scratch the surface harder than a HB pencil lead.
- (d) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, Staining and discoloration may occur.
- (e) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
- (f) The desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (g) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.
- (h) Protect the module from static , it may cause damage to the CMOS Gate Array IC.
- (i) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (j) Do not disassemble the module.
- (k) Do not pull or fold the lamp wire.
- (l) Do not adjust the variable resistor which is located on the module.
- (m) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (n) Pins of I/F connector shall not be touched directly with bare hands.

10.2 Storage

- (a) Do not leave the module in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 0 to 35C and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD module in direct sunlight.
- (c) The module shall be stored in a dark place. It is prohibited to apply sunlight or fluorescent light during the store.

10.3 Operation

- (a) Do not connect,disconnect the module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the item 6.3 "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the back-light connector and its inverter power supply shall be a minimized length and be connected directly . The longer cable between the back-light and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage(Vs).

10.4 Operation Condition Guide

- (a) LCD product supposed to be operated under circumstance of normal condition.
Normal condition is defined as below;
 - Temperature : 20±15℃
 - Humidity : 65±20%
 - Display pattern : continually changing pattern (Not stationary)
- (b) When the product is used for special application where operates LCD products in a special condition - sever then normal temperature or humidity or operation time or display pattern -that may happen at Airport, Transit Station, Stock market, Bank, and Controlling system Etc, please contact SEC and take application AMLCD engineers advice. Otherwise, it may not be guaranteed its life time and function.

10.5 Others

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. (the supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the module may be damaged.
- (d) If the module displays the same pattern continuously for a long period of time, it can be the situation when the image "Sticks" to the screen.
We recommend that you should discuss SEC when you want the module to be operated in displaying the same pattern for a long time.
- (e) This module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.