

**SAMSUNG****ELECTRONICS**

Approval

TO : IEC

DATE : April. 20. 2007

*SAMSUNG TFT-LCD***MODEL NO. : LTE400WQ-F04**

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\_\_\_\_\_  
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*Any Modification of Spec is not allowed without SEC's permission.*

Approved by : S.C. OH**LCD DIVISION****Samsung Electronics Co., Ltd.**

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**Revision History**

Date	Rev. No.	Page	Summary
Apr .20.2007	000		Rev.000 was first issues.

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## General Description

### \* Description

LTE400WQ-F04 is a TMR(Transmissive with Micro Reflective) type color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching devices. This model is composed of a TFT-LCD module, a driver circuit and a back-light unit.

The resolution of a 4.0" contains 480x272(RGB) dots and can display up to 16.7M colors.

### \* Features

- triple-gate & Dual ASG
- Transmissive with Micro Reflective type and back-light with 8 LEDs are available.
- TN(Twisted Nematic) mode.
- 24bit RGB Interface
- DE(Data Enable) & SYNC mode - DE, Vsync, Hsync, DOTCLK

### \* Applications

- Display terminals for PMP(Portable Multimedia Player) , Portable CNS(P-CNS) , MP3 application products.
- Display terminals for AV application products.

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**\* General information**

Items	Specification	Unit	Note
Display area	87.84(H) x 49.776(V) (4" diagonal)	mm	-
Driver element	a-Si TFT active matrix	-	-
Display colors	16,777,216	colors	-
Number of dots	480(H) x 272 x RGB(V)	dot	-
Pixel arrangement	RGB stripe	-	-
Pixel pitch	0.183(H) x 0.183(V)	mm	138ppi
Display mode	Normally White	-	-
Viewing direction	12	o'clock	-

**\* Mechanical information**

Item		Min.	Typ.	Max.	Unit	Note
Module size	Horizontal(H)	98.1	98.3	98.5	mm	-
	Vertical(V)	62.4	62.6	62.8	mm	(1)
	Depth(D)	2.772	2.972	3.172	mm	(1)
Weight		-	40	45	g	(2)

Note (1) Not include FPC.

Refer to the Outline Dimension in the "8. Outline Dimension" for further information.

(2) Module and Back-light unit are included.

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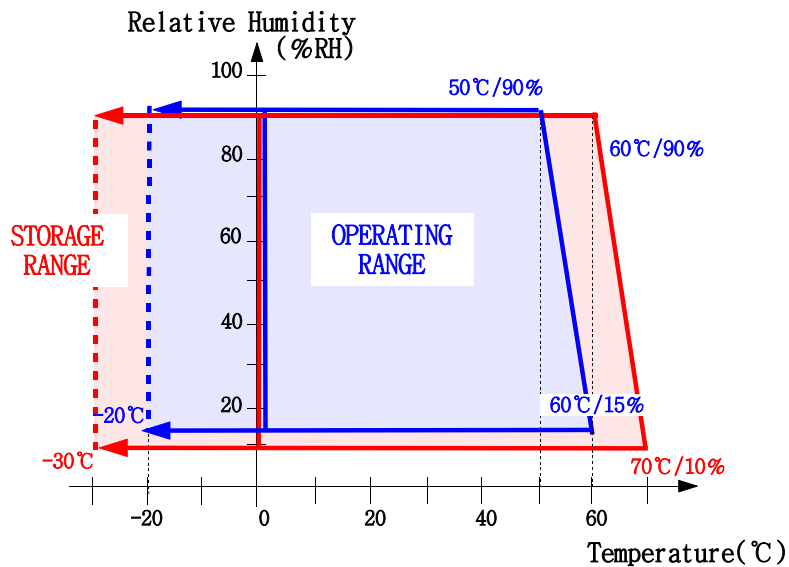
## 1. Absolute Maximum Ratings

### 1.1 Absolute Ratings Of Environment

ITEM	MIN	MAX	REMARK
Storage Temperature	-30°C	70°C	Note(1)
Operating Temperature	-20°C	60°C	Note(2)(3)

Note(1) 90%RH maximum humidity, 60°C maximum wet-bulb temperature

- (2) When operated at a temperature lower than 0°C, the LCD worked slowly and the screen appeared low-contrast images due to the characteristics of LC(Liquid Crystal).
- (3) If any fixed pattern is displayed on LCD for minutes, image-sticking phenomenon may occur.



Temperature & Humidity Graph at Absolute Environment

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**1.2 Electrical Absolute Ratings**

## (1) TFT-LCD Module

(Ta = 25 ± 2°C, VSS=GND=0)

Item	Symbol	Min.	Max.	Unit	Note
Input voltage	VDD	-0.3	4.6	V	-

## (2) Back-Light Unit

(Ta = 25 ± 2°C)

Item	Symbol	Min.	Max.	Unit.	Note
Current	I <sub>B</sub>	-	25	mA	(1)

Note (1) Permanent damage to the device may occur if maximum values are exceeded or reverse voltage is loaded.

Functional operation should be restricted to the conditions described under normal operating conditions.

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## 2. Optical Characteristics

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room or equivalent state with the methods shown in Note (1).

Measuring equipment : SR-3, BM-7, EZ-Contrast

( $T_a=25 \pm 2^\circ\text{C}$  ,  $V_{DD}=2.5\text{V}$  or  $3.3\text{V}$  ,  $f_v=60\text{Hz}$  ,  $f_{DCLK}=9.2\text{MHz}$  ,  $I_L=20\text{mA}$ )

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast ratio (Center point)		C/R		150	300	-	-	(2) SR-3
Luminance of white (Center point)		Y <sub>L</sub>	NOTE (1)	330	440	-	cd/m <sup>2</sup>	(3) SR-3
Response time	Rising	T <sub>R</sub>	$\Theta = 0$ $\Phi = 0$	-	5	15	msec	(4) BM-7
	Falling	T <sub>F</sub>		-	25	30		
Color chromaticity (CIE 1931)	White	W <sub>X</sub>	Normal Viewing Angle  B/L On	0.26	0.31	0.36	-	(5) SR-3
		W <sub>Y</sub>		0.30	0.35	0.40		
	Red	R <sub>X</sub>		0.54	0.59	0.64		
		R <sub>Y</sub>		0.28	0.33	0.38		
	Green	G <sub>X</sub>		0.29	0.34	0.39		
		G <sub>Y</sub>		0.52	0.57	0.62		
Blue	B <sub>X</sub>	0.10	0.15	0.20				
	B <sub>Y</sub>	0.10	0.15	0.20				
Viewing angle	Hor.	$\Theta_L$	C/R $\geq$ 10 B/L On	60	65	-	Degrees	(6) EZ-Contrast
		$\Theta_R$		60	65	-		
	Ver.	$\Phi_H$		45	50	-		
		$\Phi_L$		50	55	-		



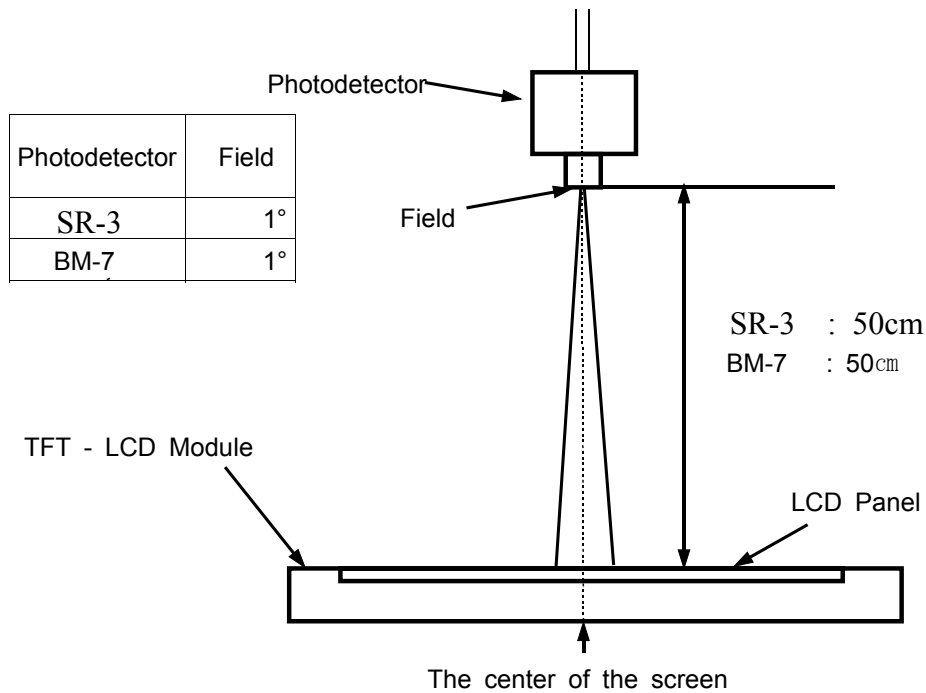
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**Note (1) Test Equipment Setup**

After stabilizing and leaving the panel alone at a given temperature for 30 min , the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. 30 min after lighting the back-light. This should be measured in the center of screen.

Environment condition :  $T_a = 25 \pm 2 \text{ }^\circ\text{C}$

Back-Light On condition



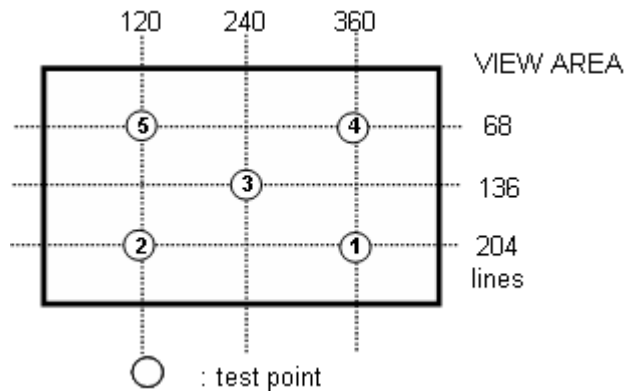
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Note (2) Definition of Contrast Ratio (C/R) : Ratio of gray max (Gmax) & gray min (Gmin) at the center point

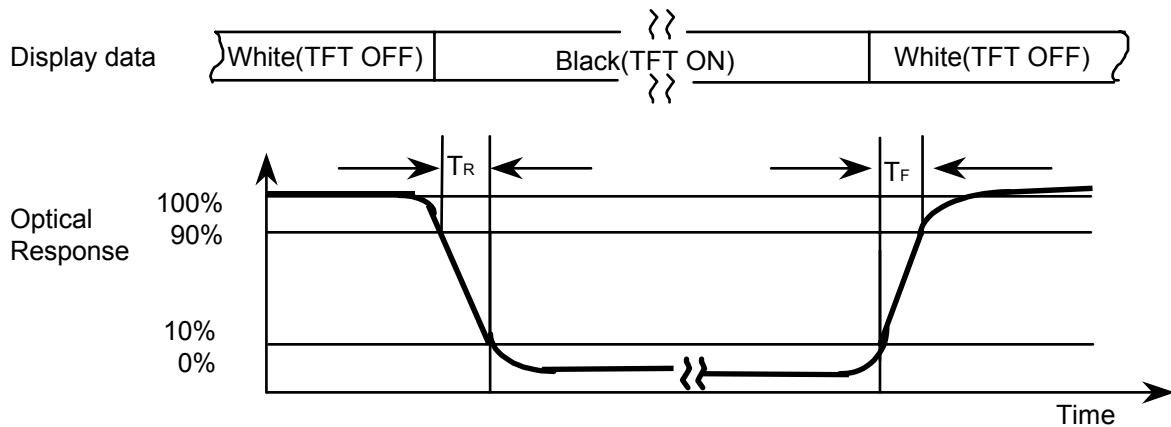
$$CR = \frac{G_{max}}{G_{min}}$$

\* Gmax : Luminance with all pixels white  
\* Gmin : Luminance with all pixels black

Note (3) Definition of Luminance of White : Luminance of white at the center point (@③)



Note (4) Definition of Response time : Sum of Tr ,Tf

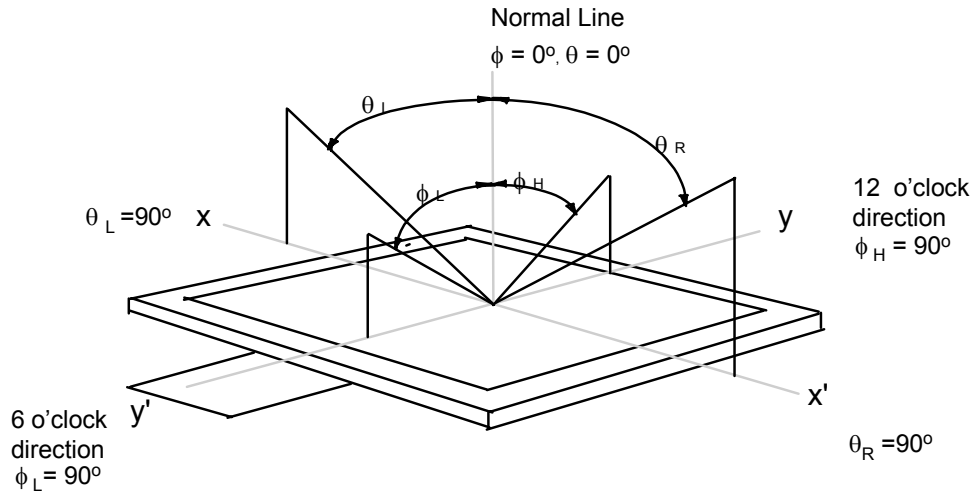


Note (5) Definition of Color Chromaticity (CIE 1931)

Color coordinate of white & red, green, blue at center point.

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Note (6) Definition of Viewing Angle : Viewing angle range ( $CR \geq 10$  )



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### 3. Electrical Characteristics

#### 3.1 TFT-LCD Module

 $T_a = 25 \pm 2^\circ\text{C}$ 

ITEM	Symbol	Min.	Typ.	Max.	Unit	Note
Logic supply voltage	VDD	2.25	2.5	2.9	V	(1)
		2.9	3.3	3.5	V	(2)
Vsync Frequency	$f_v$	-	60	70	Hz	
Hsync Frequency	$f_H$	-	17.28	-	kHz	
Main Frequency	$f_{DCLK}$	-	9.2	15.0	MHZ	
Power Dissipation	White	-	72	95	mW	(3),(4)
	Black	-	72	95	mW	

Note (1). PWRSEL = Low

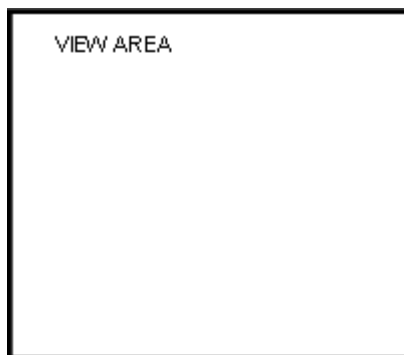
Note (2). PWRSEL = High

※ Refer to the "5. Input Terminal Pin Assignment"

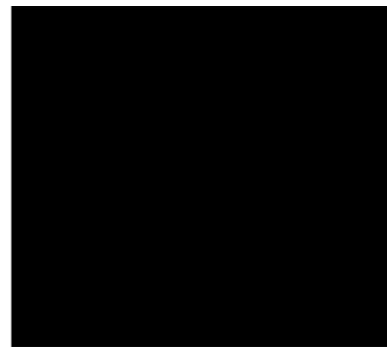
Note (3).  $f_v=60\text{Hz}$ ,  $f_{DCLK}=9.2\text{MHZ}$ ,  $VDD=2.5\text{V}$  or  $3.3\text{V}$

Note (4). Power Dissipation check pattern

a) White Pattern



b) Black Pattern



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### 3.2 Back-Light unit

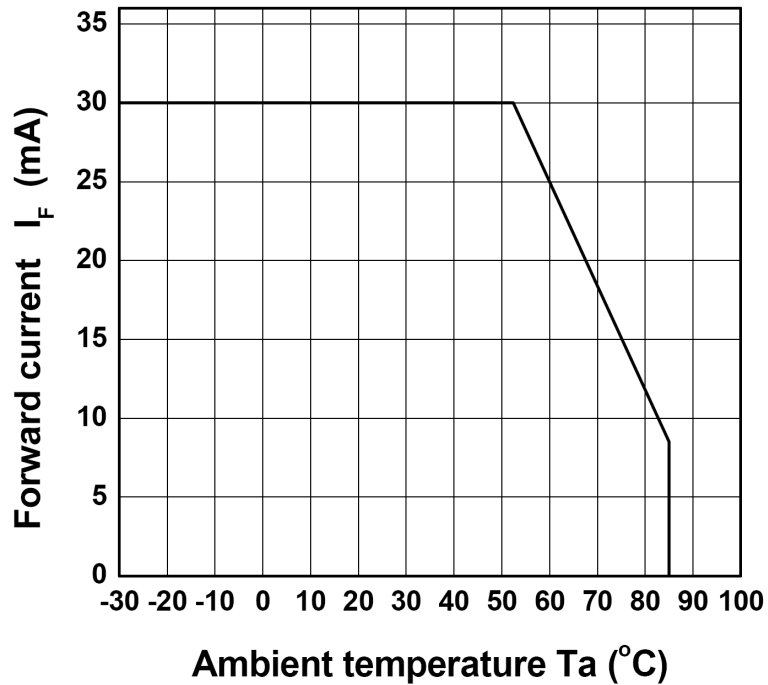
The back-light system is an edge-lighting type with 8 white LED(Light Emitting Diode)s.

( $T_a=25 \pm 2^\circ\text{C}$ )

Item	Symbol	Min.	Typ.	Max.	Unit	Note
LEDs Current	$I_B$	-	20	22	mA	(1)
Power Consumption	$P_{BL}$	-	530	580	mW	(2)

Note (1) 8 LEDs serial type.

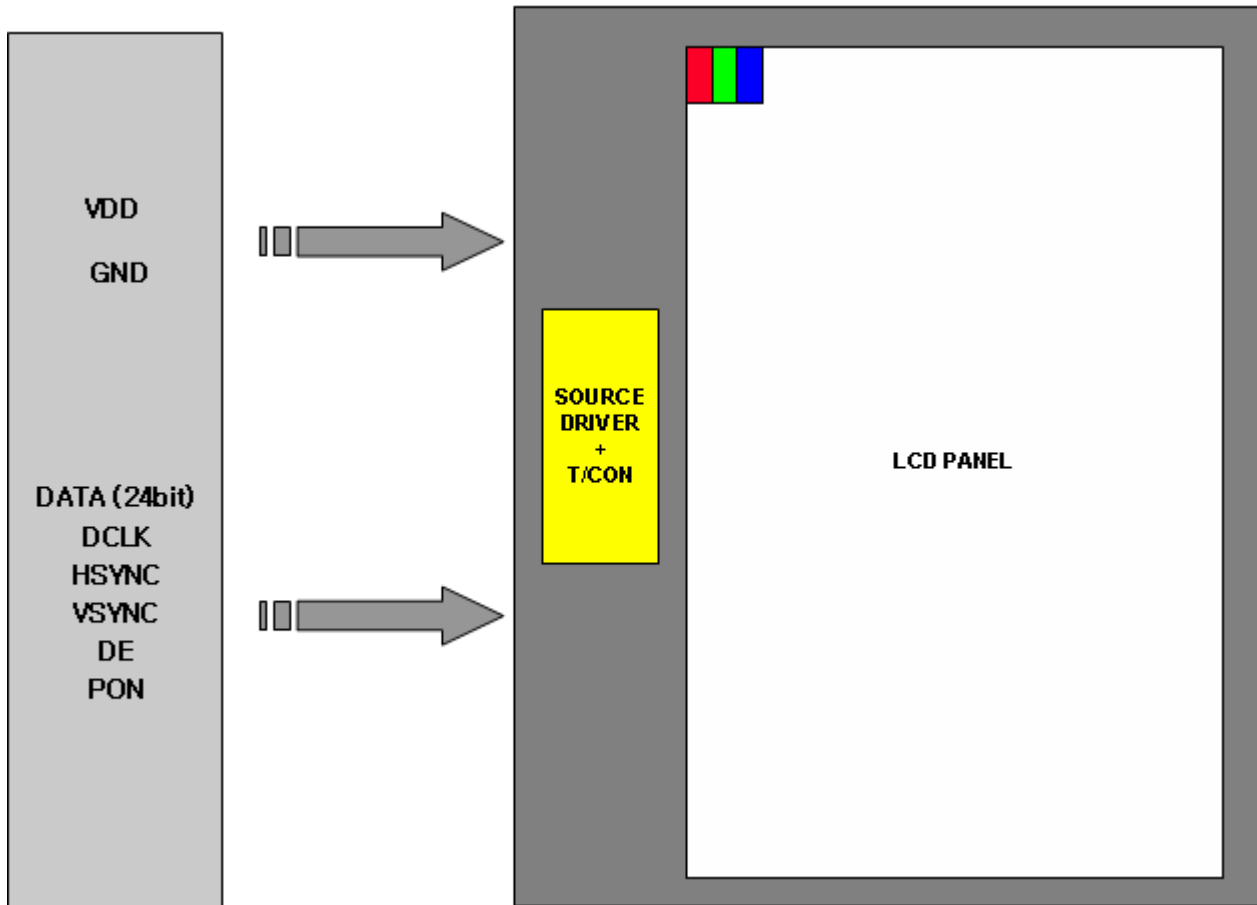
(2) Where  $I_B = 20 \text{ mA}$ ,  $V_B = P_{BL} / I_B$



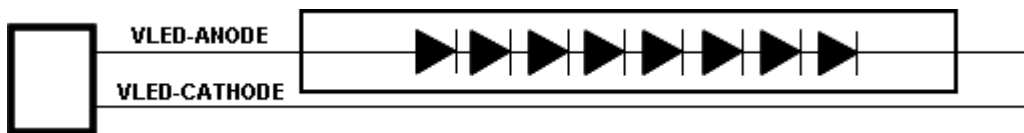
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## 4. Block Diagram

### 4.1 TFT-LCD Module (Interface System Structure) with Back Light Unit



### 4.2 Back-light Unit (Connector : 2 pin FPC Solder type)



Pin No.	Symbol	Function
1	LED+	LED Anode
2	LED-	LED Cathode

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## 5. Input Terminal Pin Assignment

### 5.1 Input Signal & Power (Connector type : 45Pin / 0.5mm pitch / top contact)

- Hirose : FH12 series

Pin#	Symbol	Function	Remark	Pin#	Symbol	Function	Remark
1	VSS	Ground		26	B5	Blue Data	
2	VSS	Ground		27	B6	Blue Data	
3	VDD	Power Supply (2.5or3.3V)		28	B7	Blue Data	(MSB)
4	VDD	Power Supply (2.5or3.3V)		29	VSS	Ground	
5	R0	Red Data	(LSB)	30	PCLK	Pixel Clock	
6	R1	Red Data		31	PON	Display on/off	
7	R2	Red Data		32	HSYNC	Horizontal Sync Signal	
8	R3	Red Data		33	VSYSN	Vertical Sync Signal	
9	R4	Red Data		34	DE	Data Enable	
10	R5	Red Data		35	PWRSEL	VDD power select	High:3.3V Low:2.5V
11	R6	Red Data		36	VSS	Ground	
12	R7	Red Data	(MSB)	37	NC	Non Connect	
13	G0	Green Data	(LSB)	38	NC	Non Connect	
14	G1	Green Data		39	NC	Non Connect	
15	G2	Green Data		40	NC	Non Connect	
16	G3	Green Data		41	VSS	Ground	
17	G4	Green Data		42	VLED1-	LED Cathode	
18	G5	Green Data		43	VLED+	LED Anode	
19	G6	Green Data		44	NC	Non Connect	
20	G7	Green Data	(MSB)	45	NC	Non Connect	
21	B0	Blue Data	(LSB)	-	-	-	-
22	B1	Blue Data		-	-	-	-
23	B2	Blue Data		-	-	-	-
24	B3	Blue Data		-	-	-	-
25	B4	Blue Data		-	-	-	-

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### 5.2 Input Signal, Basic Display Colors and Gray Scale of Each Colors

COLOR	DISPLAY	DATA SIGNAL																				GRAY SCALE LEVEL			
		RED							GREEN							BLUE									
		R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3		B4	B5	B6
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	-
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0	
	DARK ↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1	
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~R252
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	LIGHT ↓	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R253	
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R254	
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R255	
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0	
	DARK ↑	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	G1	
		0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	G2	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~G252	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		:
	LIGHT ↓	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	G253	
		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	G254	
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	G255	
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0	
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	B1	
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	B2	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~B252	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		:
	LIGHT ↓	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	B253	
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	B254	
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	B255	

Note) Definition of Gray :

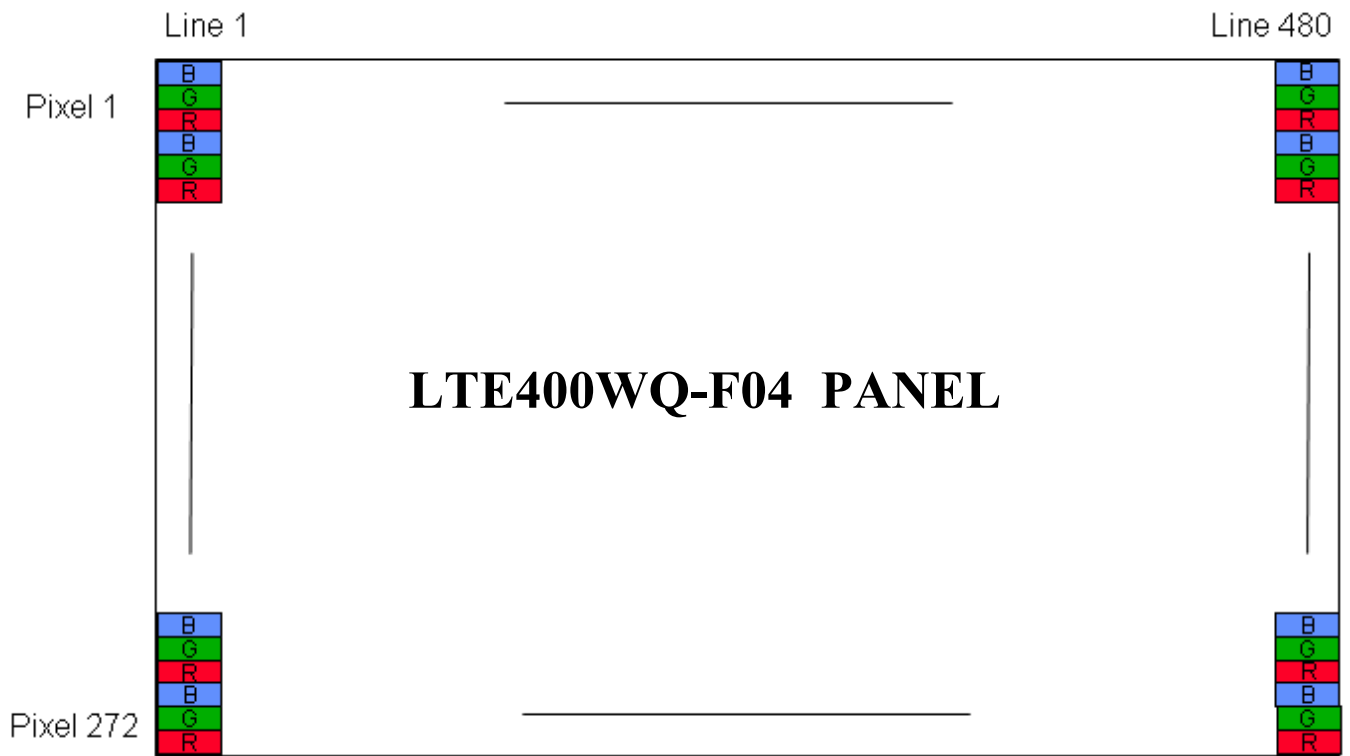
Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

Input Signal : 0 = Low level voltage, 1 = High level voltage



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### 5.3 PIXEL FORMAT



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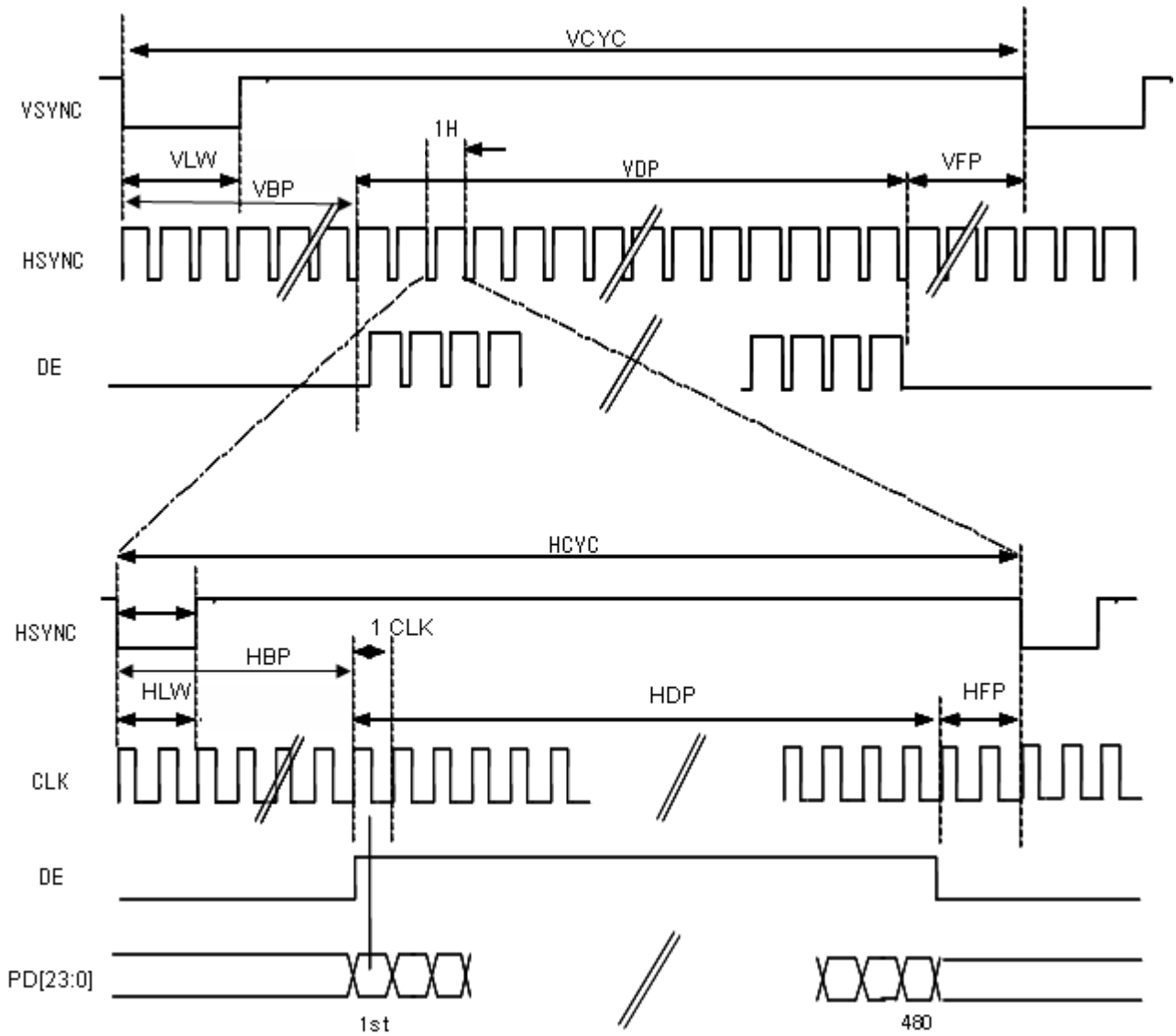
**6. INTERFACE TIMING****6-1. Vertical timing**

Signal	Symbol	Min.	Typ.	Max.	Unit	Note
Frame Frequency	fFRM	-	60	70	Hz	
VSYNC(Frame) Period	VCYC	248	288	335	H	
VSYNC Low width	VLW	1	10	-	H	
Vertical Display Period	VDP		272	-	H	
Vertical Back porch	VBP	-	12	-	H	
Vertical Front porch	VFP	2	4	-	H	

**6-2. Horizontal timing**

Signal	Symbol	Min.	Typ.	Max.	Unit	Note
HSYNC(1H) Period	HCYC	490	533	605	DOTCLK	
HSYNC Low width	HLW	4	41	-	DOTCLK	
Horizontal Display Period	HDP	-	480	-	DOTCLK	
Horizontal Back porch	HBP	8	45	-	DOTCLK	
Horizontal Front porch	HFP	2	8	-	DOTCLK	
DOTCLK Frequency	f <sub>DOTCLK</sub>	-	9.2	15	MHz	@fFRM=60Hz

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## 6-3. AC characteristics

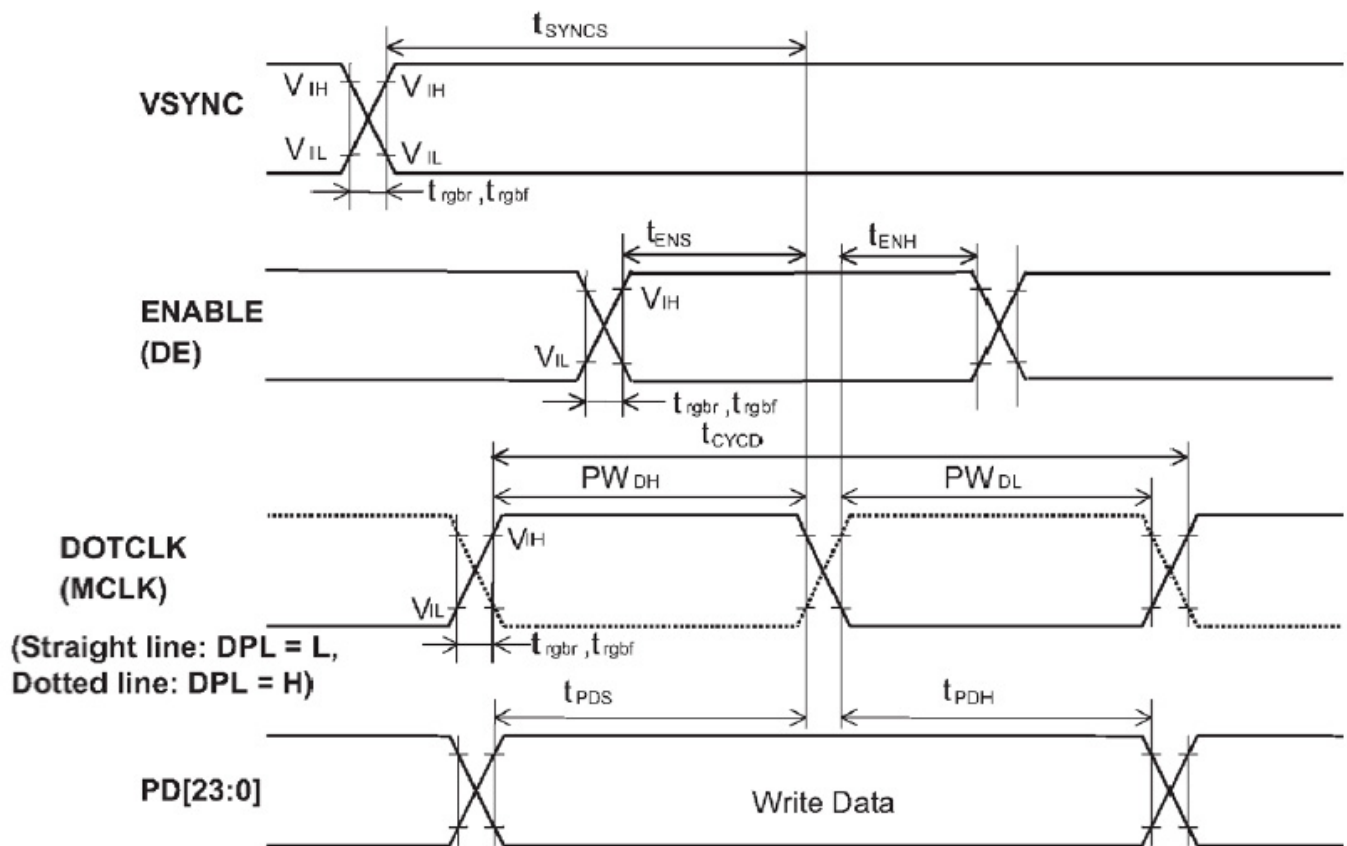
※ Operating at VDD=2.25~2.9V , Ta = -40°C~+85°C

Parameter	Symbol	Min.	Typ.	Max.	Unit
VSYNC,HSYNC setup time	t <sub>SYNCS</sub>	10	-	-	ns
ENABLE (DE) setup time	t <sub>ENS</sub>	10	-	-	
ENABLE (DE) hold time	t <sub>ENH</sub>	10	-	-	
DOTCLK "Low" level pulse width	P <sub>WDL</sub>	25	-	-	
DOTCLK "High" level pulse width	P <sub>WDH</sub>	25	-	-	
DOTCLK cycle time	t <sub>CYCD</sub>	66.7	-	-	
Data setup time	t <sub>PDS</sub>	10	-	-	
Data hold time	t <sub>PDH</sub>	10	-	-	
DOTCLK,VSYNC,HSYNC clock rise/fall time	t <sub>rgbr</sub> t <sub>rgbf</sub>	-	-	5	

※ Operating at VDD=2.9~3.5V , Ta = -40°C~+85°C

Parameter	Symbol	Min.	Typ.	Max.	Unit
VSYNC,HSYNC setup time	t <sub>SYNCS</sub>	10	-	-	ns
ENABLE (DE) setup time	t <sub>ENS</sub>	10	-	-	
ENABLE (DE) hold time	t <sub>ENH</sub>	10	-	-	
DOTCLK "Low" level pulse width	P <sub>WDL</sub>	25	-	-	
DOTCLK "High" level pulse width	P <sub>WDH</sub>	25	-	-	
DOTCLK cycle time	t <sub>CYCD</sub>	66.7	-	-	
Data setup time	t <sub>PDS</sub>	10	-	-	
Data hold time	t <sub>PDH</sub>	10	-	-	
DOTCLK,VSYNC,HSYNC clock rise/fall time	t <sub>rgbr</sub> t <sub>rgbf</sub>	-	-	5	

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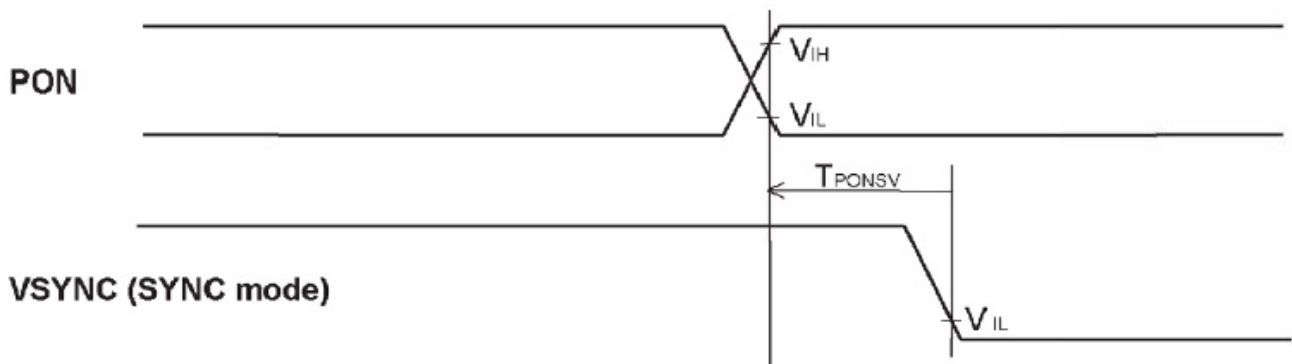


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## 6-4. PON timing characteristics.

※ Operating at VDD=2.25~3.5V , Ta = -40°C~+85°C

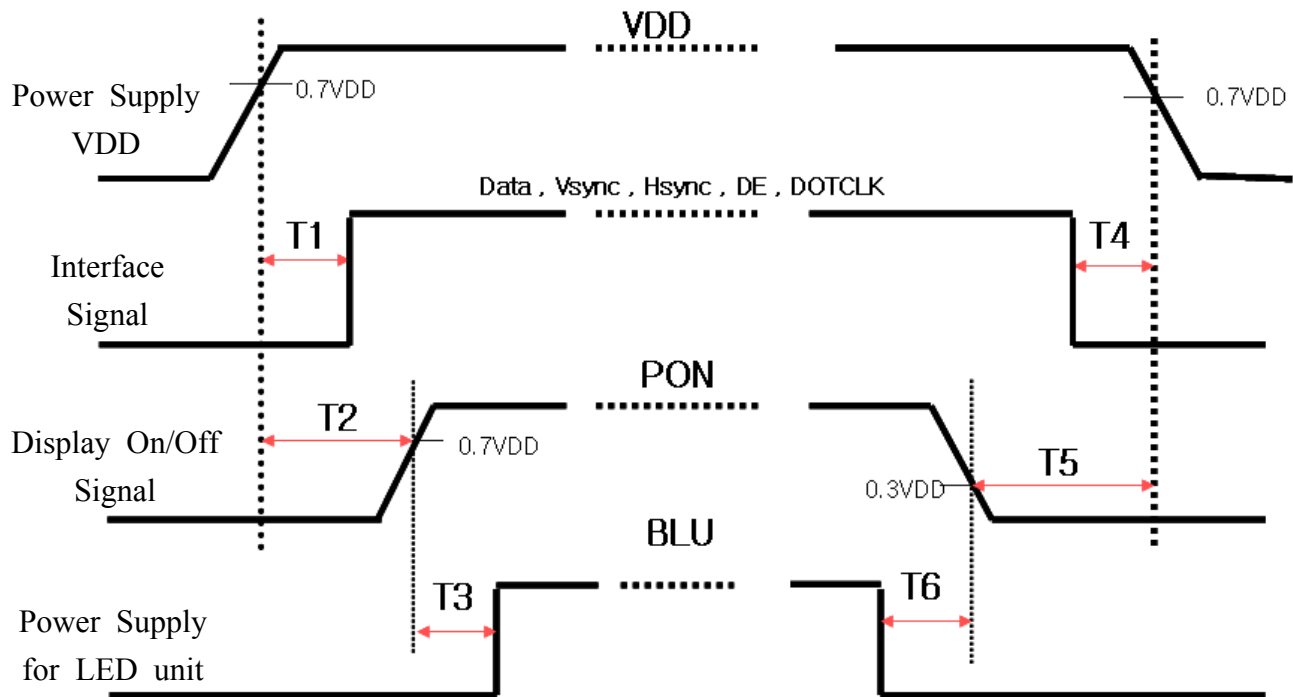
Item	Symbol	Min.	Typ.	Max.	unit
PON setup (SYNC mode)	$t_{PONS\ V}$	10	-	-	DOTCLK



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### 7. Power On/Off Sequence

- To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.



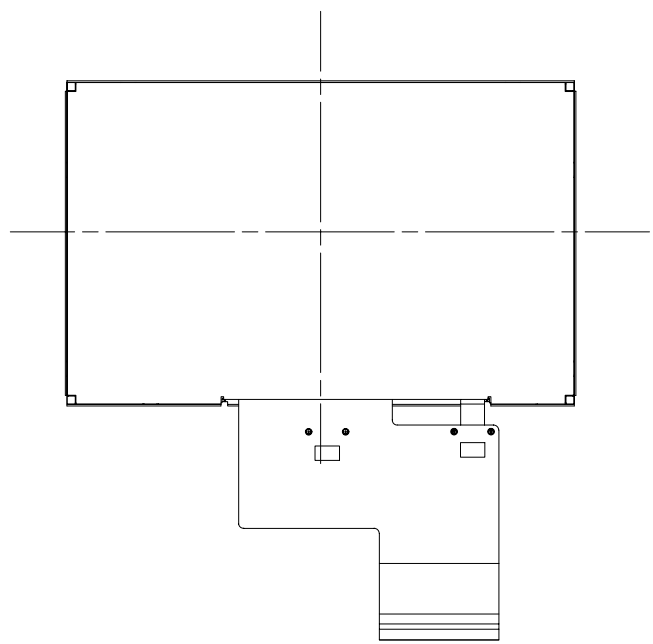
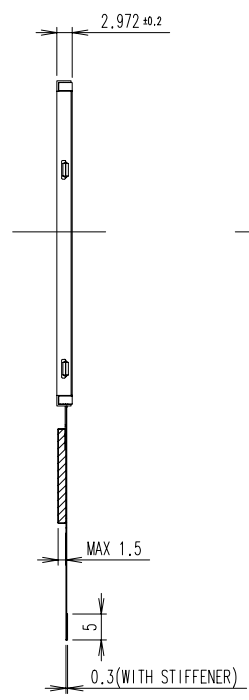
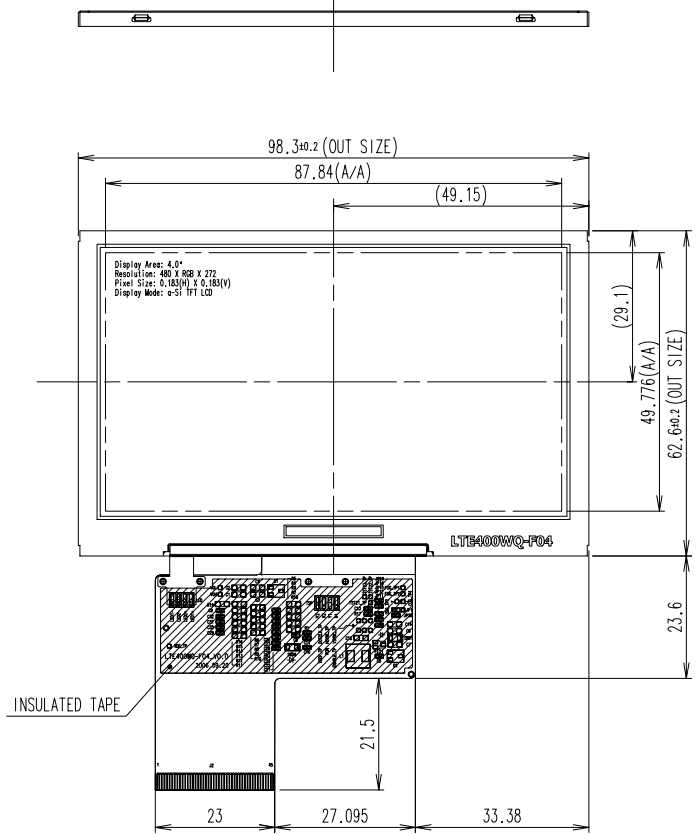
Symbol	Specification	Note
T1	$0 \text{ ms} < T1 < T2$	
T2	$10 \text{ ms} < T2$	(1)
T3	$10 \text{ frames} < T3$	
T4	$10 \text{ ms} < T4 < T5$	
T5	$(5 \text{ frames} + T4) < T5$	(1)
T6	$10 \text{ frames} < T6$	

Note(1) Refer to "6-4. PON timing characteristics."

# 8. Outline Dimension

NO	PART NAME	CODE NO	SPECIFICATION	Q'TY	SPEC NO	REMARK
1	OUTLINE DIMENSION		LTE400WQ-F04	1EA		

2.5  
2.0  
1.5  
1.0  
0



NO. REVISION

GENERAL TOLERANCE				
STEP	LEVEL 1	LEVEL 2	LEVEL 3	
0 < X ≤ 4	±0.05	±0.1	±0.2	
4 < X ≤ 16	±0.08	±0.15	±0.3	
16 < X ≤ 64	±0.12	±0.25	±0.5	
64 < X ≤ 256	±0.25	±0.4	±0.8	

REV	DATE				
UNIT	mm	DRA'N BY	DES'D BY	CHK'D BY	APP'D BY
SCALE	1 / 1	T.J.KIM			Y.B.CHU
TOLERANCE	LEVEL 3	06.12.15			06.12.15
SAMSUNG ELECTRONICS					SPEC. NO

MODEL NAME	LTE400WQ-F04		
PART/SHEET NAME	OUTLINE DIMENSION	SHEET	1 / 1
CODE NO.		VER.	000





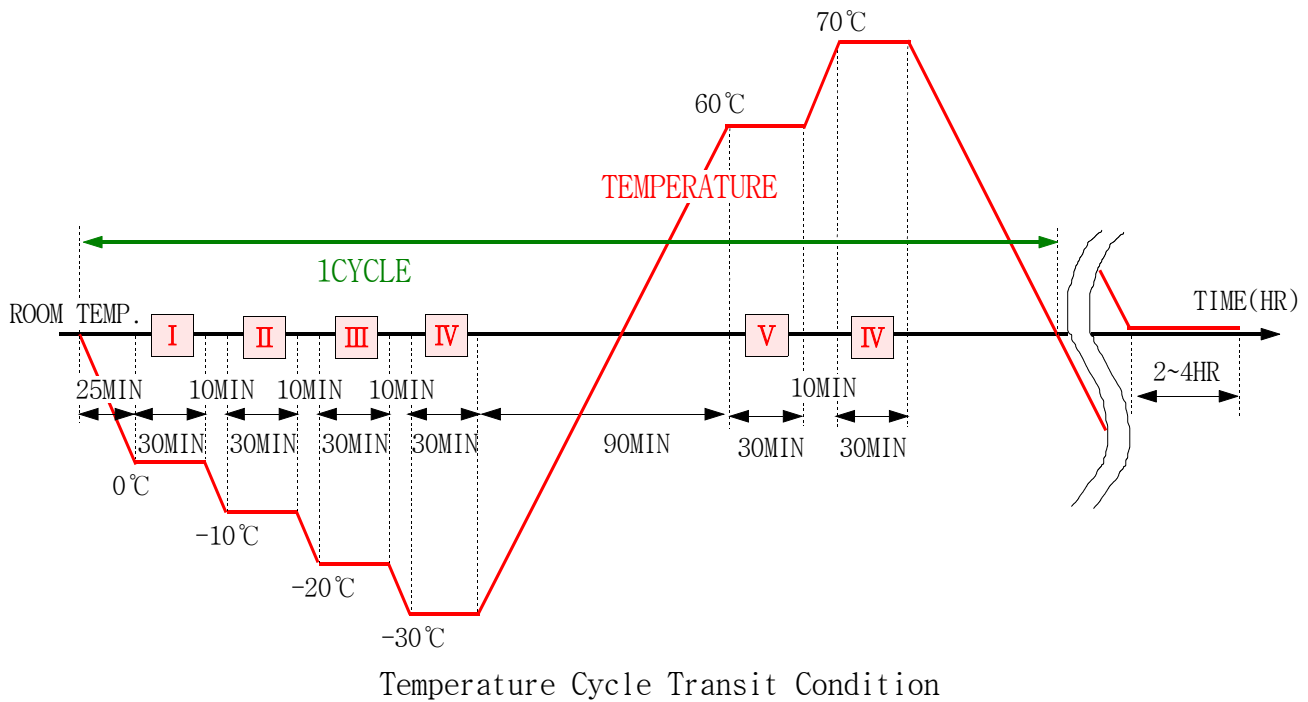
Approval

## 9. Reliability Test

### 9.1 Condition

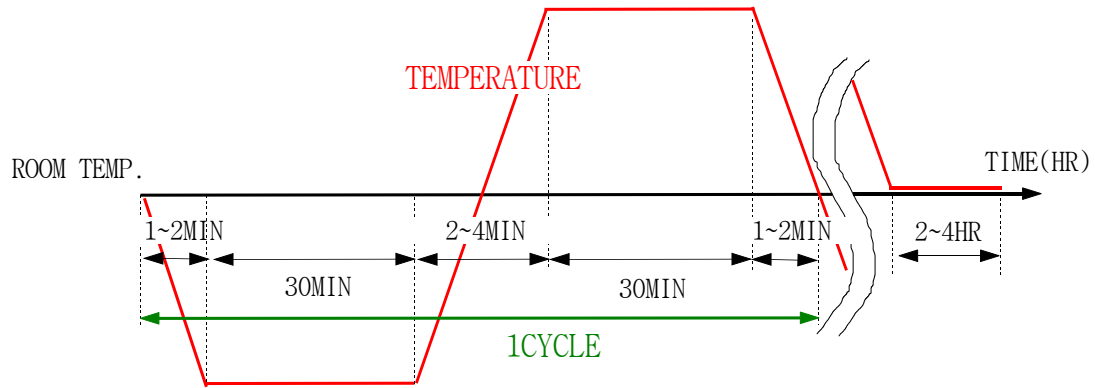
ITEM	CONDITION	REMARK	Test Result
High Temperature Operating Life-test	60°C, 240HR		PASS
Low Temperature Operating Life-test	-20°C, 240HR		PASS
Temperature Humidity Bias test	50°C 90%RH, 240HR		PASS
Temperature Cycle ON/OFF test	-30°C ⇔ 70°C ON/OFF, 5CY	Note(1)	PASS
High Temperature Storage test	70°C, 240HR		PASS
Low Temperature Storage test	-30°C, 240HR		PASS
Wet High Temperature Storage test	60°C 90%RH, 240HR		PASS
Thermal Shock test	-30°C ⇔ 70°C, 100CY	Note(2)	PASS
Electro-Static Discharge test	CONTACT: ±4 kV, 20times A I R: ±8 kV, 20times	Note(3)	PASS
Box Vibration test	RANDOM 0.74Grms, 1HR/Y axis(SMALL BOX)	Note(4)	PASS
Box Drop test	1 Corner 3 Edges 6 faces, 66cm(MEDIUM BOX)		PASS

Note(1) ON Time over 10 seconds, OFF Time under 10 seconds



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Note(2) STORAGE



Thermal Shock Transit Condition

Note(3) Main-LCD, 5 times to every 4 corners of active area

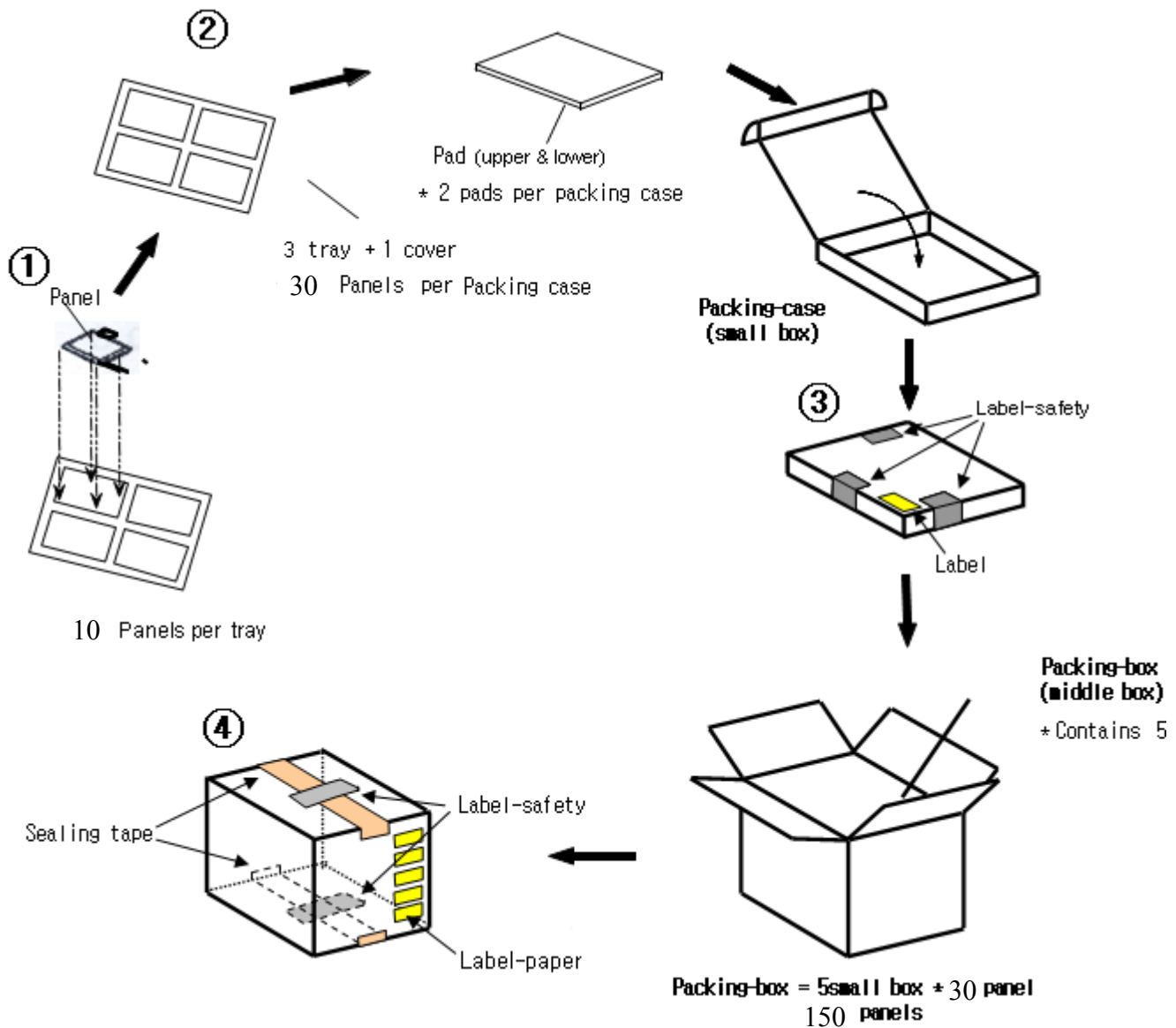
Note(4) Basic transportation by common carrier environmental,  
514.4 MIL-STD-810E

OVERALL RMS LEVEL	BREAK POINT					
	FRQUENCY	PSD VALUE	FRQUENCY	PSD VALUE	FRQUENCY	PSD VALUE
0.74G	10Hz	0.00650	121Hz	0.00300	340Hz	0.00003
	20Hz	0.00650	200Hz	0.00300	500Hz	0.00015
	120Hz	0.00020	240Hz	0.00150	-	-

### 9.2 Judgement

- > Main LCD should work under the normal condition.
- > After the temperature and humidity test,  
the luminance and CR(Contrast Ratio) should not be changed over 50% compared with those before the test.

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**10. Packing****Note**

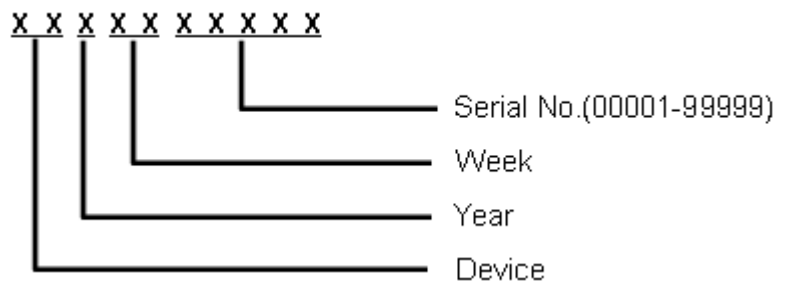
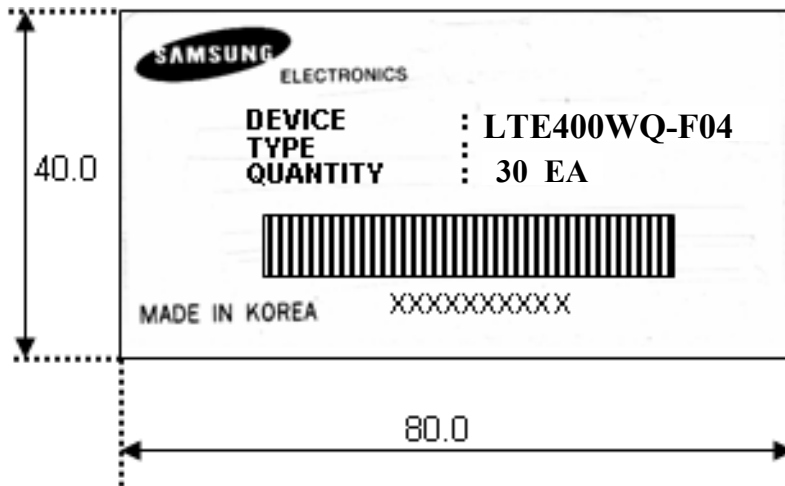
- (1) Total : Case: Approx. : 2.13 Kg  
Box: Approx. : 11.9 Kg
- (2) Size : Case: 490(W) x 342(D) x 58(H)  
Box: 505(W) x 355(D) x 312(H)
- (3) Place the panels in the tray facing the direction shown in the figure.
- (4) Place 3 tray and cover(empty tray) and pads inside the packing-case.
- (5) Place 5 packing-case inside the packing-box. (Affix the label)
- (6) Seal the packing-box. Affix the label-safety.

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## 11. Marking & Others

A nameplate bearing followed by is affixed to a shipped product at the Specified location on each product.

(1) Packing case attach



## 12. General Precautions

### 12.1 Handling

- (a) When the module is assembled, it should be attached to the system firmly. Be careful not to twist and bend the module.
- (b) Refrain from strong mechanical shock and / or any force to the module. In addition to damage, this may cause improper operation or damage to the module and back-light unit.
- (c) Note that polarizers are very fragile and could be easily damaged. Do not press or scratch the surface harder than a HB pencil lead.
- (d) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, Staining and discoloration may occur.
- (e) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
- (f) The desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (g) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.
- (h) Protect the module from static , it may cause damage to the Integrated Gate Circuit.
- (i) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (j) Do not disassemble the module.
- (k) Do not adjust the variable resistor which is located on the back side.
- (l) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (m) Pins of I/F connector shall not be touched directly with bare hands

## 12.2 Storage

- (a) Do not leave the panel in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 0 to 35°C and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD module in direct sunlight.
- (c) The module shall be stored in a dark place. It is prohibited to apply sunlight or fluorescent light during the store.

## 12.3 Operation

- (a) Do not connect, disconnect the module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

## 12.4 Others

- (a) The Liquid crystal is deteriorated by ultraviolet, do not leave it in direct sunlight and strong ultraviolet ray for many hours.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. ( the supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the panel may be damaged.
- (d) If the panel displays the same pattern continuously for a long period of time, it can be the situation when the image "Sticks" to the screen.
- (e) This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed.
- (f) Avoid shortness between LED soldering pad.